# The Method of Double Averaging: An Approach for Modeling Power-Factor-Correction Switching Converters

Siu-Chung Wong, Member, IEEE, Chi K. Tse, Fellow, IEEE, Mohamed Orabi, Member, IEEE, and Tamotsu Ninomiya, Fellow, IEEE

Abstract—This paper describes the modeling of power-factorcorrection converters under average-current-mode control, which are widely used in switch-mode power supply applications. The objective is to identify stability boundaries in terms of major circuit parameters for facilitating design of such converters. The approach employs a double averaging procedure, which first applies the usual averaging over the switching period and subsequently applies generalized averaging over the mains period. The resulting model, after two averaging steps and application of a harmonic balance procedure, is nonlinear and capable of describing the low-frequency nonlinear dynamics of the system. The parameter ranges within which stable operation is guaranteed can be accurately and easily found using this model. Experimental measurements are provided for verification of the analytical results.

*Index Terms*—Averaged model, closed-form stability condition, double averaging, power factor correction (PFC), switching converters.

### I. INTRODUCTION

**P**OWER-FACTOR-CORRECTION (PFC) boost stages are widely used in off-line switching power supplies. The basic configuration of a PFC boost stage consists of 1) an average-current-mode (ACM) control loop which "forces" the input current to follow the waveform of the input voltage; and 2) a slow output voltage feedback loop which controls the amplitude of the "shaped" input current according to the power demanded by the load. The aim of the control is to achieve a unity power factor at the input side and to maintain power balance while keeping the output voltage at a nearly constant level [1], [2]. Previously, several attempts have been made to model the dynamics of power converters based on dynamic phasors [3]–[5], which can be simplified in some specific applications to averaging and linearization [6]–[10]. However, because the actual process is

S.-C. Wong and C. K. Tse are with the Department of Electronic and Information Engineering, Hong Kong Polytechnic University, Hong Kong, China (e-mail: encktse@polyu.edu.hk).

M. Orabi is with the Electrical Engineering Department, Aswan Faculty of Engineering, South Valley University, Aswan, Egypt (e-mail: orabi@ieee.org).

T. Ninomiya is with the Department of Electrical and Electronic Systems Engineering, Kyushu University, Fukuoka 812-8581, Japan.

Digital Object Identifier 10.1109/TCSI.2005.855744

nonlinear, stability analyzes based on linearized models are very restricted and often provide stability information (e.g., parameter ranges for stable operation) which are not necessarily consistent with reality. In particular, low-frequency period-doubling phenomena in PFC boost preregulators have been reported by Orabi et al. [11]-[13]. Also, fast-scale instability has also been reported by Iu et al. [15]. In this paper we make use of a hierachical modeling approach, a basic form of which was proposed in [6], and apply generalized averaging together with a harmonic balance procedure to identify the low-frequency nonlinear dynamics of the PFC boost converter. This method results in a model that is capable of revealing the phenomena found in [11]–[13]. Essentially we apply averaging twice in succession, over the switching period and the mains period, to obtain a double-averaged model in the form of a system of simple continuous-time differential equations which can be readily analyzed and numerically simulated. Stability information generated from this model is accurate for the purpose of locating the ranges of parameters within which the PFC boost converter operates in its normally expected regime. This technique was also used in Gordillo et al. [14] for the study of the dynamics of a power factor preregulator under a special control. The work of Gordillo et al. [14], however, focuses on varying the output voltage. More comprehensive stability information that is presented in terms of major circuit parameters is unavailable. Furthermore, as is common to switching converters, operation may assume either continuous conduction mode (CCM) or discontinuous conduction mode (DCM). While much of the previous work has focused on the CCM case [11]-[15], very few has addressed the case of DCM [16]. Moreover, the problem of bifurcation of a supposedly CCM operation into DCM due to variation of some parameter(s) is a complex issue, which requires further investigation. In this paper we restrict ourselves to CCM operation, as it is the usual operating condition for most of medium to high power applications.

This paper is organized as follows. In Section II, we will explain the usual operation of the boost converter as employed in typical PFC applications, with emphasis on the salient operational conditions in the presence of a slowly varying input voltage. Our main proposal is described in Section III, where we explain the mathematical technique of *doubling averaging*, and apply it to produce a model for the PFC boost converter under average current-mode control. The derivation of the steady-state solution will be given in Section IV. Application of the doubleaveraged model for analysis of the stability of PFC converters

1057-7122/\$20.00 © 2006 IEEE

Manuscript received March 1, 2004; revised March 11, 2005 and July 6, 2005. This work was supported by the Hong Kong Research Grants Council under Grant PolyU 5237/04E, a Research Grant on "Control of Electromagnetic Environment in Low-Frequency Band Below 100 kHz" of the future research promotion business in Japan provided by the Japan Society for Promotion of Science. This paper was recommended by Associate Editor I. A. Hiskens.



Fig. 1. Boost PFC converter under ACM control.

will be illustrated in Section V, where closed-form stability conditions are also derived. Finally, in Sections VI and VII, the stability information obtained from the model will be presented graphically and verified by experimental measurements taken from a practical industrial-standard PFC converter circuit.

#### II. PFC BOOST CONVERTER UNDER ACM CONTROL

The system under study is a boost converter under ACM control, as in typical PFC applications. As shown in Fig. 1, the input voltage is taken as a template for input current shaping. This templete is multiplied by a factor which effectively adjusts the amplitude of the input current. This amplitude control is derived from a nonlinear computational process involving a variable from the output feedback loop and the input voltage. To see how it works, we consider the basic requirement of the input current waveform, which is to be "ideally shaped" as

$$i_{\rm in}(t) = \frac{p(t)\sqrt{2}}{V_{\rm in}} |\sin \omega_m t| \tag{1}$$

where  $\omega_m$  is the mains angular frequency,  $V_{in}$  is the rms input voltage, and p(t) is the output power which is given by a feedback circuit. Furthermore, if we use the input voltage as a template for shaping the input current, the amplitude must include an additional denominator  $V_{in}$ , as depicted in Fig. 1, i.e.,

$$i_{\rm in}(t) = \frac{p(t)}{V_{\rm in}^2} v_{\rm in}(t) \tag{2}$$

where  $v_{in}(t) = \sqrt{2}V_{in}|\sin \omega_m t|$ . As mentioned earlier, p(t) is a feedback variable, which is derived from the output voltage. Obviously, p(t) should vary very slowly relative to the mains half-cycle so that the sinusoidal waveform of the input current can be maintained. In practice, a low-pass type of feedback is



Fig. 2. First averaged model, where d is implicitly defined by the ACM control.

applied to keep the output voltage nearly fixed. The usual control equation for p(t) in the frequency domain is

$$\frac{P(s)}{V_o(s)} = \frac{-G_F}{\tau_F s + 1} \tag{3}$$

where P(s) and  $V_o(s)$  are the frequency-domain variables of  $\Delta p(t)$  and  $\Delta v_o(t)$ , respectively, and  $G_F$  and  $1/\tau_F$  are the dc gain and cutoff frequency of the feedback circuit, respectively. Note that the equivalent time-domain equation is

$$\tau_F \frac{dp(t)}{dt} + p(t) = -G_F(v_o - V_{\text{ref}}) \tag{4}$$

where  $V_{\rm ref}$  is the reference output voltage. It is also worth mentioning that the overall dc gain in the loop must also include the factor  $1/V_{\rm in}$  which is introduced by the divider at the input.

#### III. MODELING BY "DOUBLE AVERAGING"

# A. First Averaging and Equation of Forced Oscillation

For the purpose of studying the low-frequency dynamics, we may ignore the details within the switching cycle. This amounts to a standard averaging step that is to be taken over one switching period. The resulting model has appeared repeatedly in the literature of power electronics [17], [18]. We omit the details of the derivation here, but show the initial circuit model in Fig. 2. This model is incomplete because the duty cycle d should be implicitly defined by the ACM control law. We first write down the Kirchhoff's law equations

$$(1-d)v_o = v_{\rm in}(t) - L\frac{di_{\rm in}}{dt}$$
<sup>(5)</sup>

$$(1-d)i_{\rm in} = C\frac{dv_o}{dt} + \frac{v_o}{R}.$$
(6)

In the case of ACM control, the averaged inductor current has been programmed according to (1) or (2). From (5) and (6), and eliminating d, we get the following average power balance equation:

(

$$\frac{C}{2}\frac{dv_o^2}{dt} = -\frac{v_o^2}{R} + i_{\rm in}v_{\rm in} - \frac{L}{2}\frac{di_{\rm in}^2}{dt}.$$
(7)

For PFC applications, if the ACM achieves its objective of programming the current perfectly, the dynamics of the inductor can be ignored. However, failure of the ACM in programming the current is possible, and may lead to other forms of instability which are not studied here. Thus, we get

$$\frac{C}{2}\frac{dv_o^2}{dt} = -\frac{v_o^2}{R} + 2p(t)\sin^2\omega_m t 
\frac{C}{2}\frac{dv_o^2}{dt} = -\frac{v_o^2}{R} + p(t)(1 - \cos 2\omega_m t)$$
(8)

for  $n\pi \leq \omega_m t < (n+1)\pi$ ,  $n = 0, 1, 2, \dots$  It should be noted that p(t) is connected with  $v_o$  via the feedback circuit. Therefore, (8) is a continuous-time differential equation in  $v_o$ .

Thus, the system under study can be described by the set of equations consisting of (4) and (8). To analyze this system, we will make use of the fact that it describes a system of *forced oscillation*, where the variables are "driven" periodically, containing frequency components up to twice the mains frequency.

# B. Second Averaging

We now consider further application of averaging to the system of equations under study, which consists of (4) and (8). Specifically, we take *moving average* over the mains period [19]. For any variable a(t), we may assume that it takes the following form:

$$a(t) = A_0 + A_1 \sin(\omega_m t - \theta_1(t)) + A_2 \sin(2\omega_m t + \theta_2(t)).$$
(9)

We define each component of the moving average of a(t), denoted by  $[a(t)]_k$  or simply  $[a]_k$  for k = 0, 1, 2, by

$$[a(t)]_k = [a]_k = \frac{\omega_m}{2\pi} \int_{t-\frac{2\pi}{\omega_m}}^t a(\tau) \exp(-jk\omega_m\tau) d\tau.$$
(10)

For brevity and without confusion, we simply write  $[a]_k$  as  $a_k$ . In other words,  $a_0$  is the dc component of the moving average of a(t),  $a_1$  is the component of the moving average of a(t) at  $\omega_m$ , and  $a_2$  is the component of the moving average of a(t) at  $2\omega_m$ . Thus, we may write

$$a(t) = a_0(t) + a_1(t)e^{j\omega_m t} + [a_1(t)e^{j\omega_m t}]^* + a_2(t)e^{2j\omega_m t} + [a_2(t)e^{2j\omega_m t}]^*$$
(11)

where superscript \* denotes complex conjugation. Upon expanding, we get

$$a_0(t) = a_0$$
  
=  $A_0$  (12)

$$a_{1}(t) = a_{1r} + ja_{1i}$$
  
=  $-\frac{A_{1}}{2}\sin\theta_{1} - j\frac{A_{1}}{2}\cos\theta_{1}$  (13)

$$a_{2}(t) = a_{2r} + ja_{2i}$$
  
=  $-\frac{A_{2}}{2}\sin\theta_{2} - j\frac{A_{2}}{2}\cos\theta_{2}.$  (14)

It is readily shown that the moving average of the time-derivative of each component is related to the time-derivative of the component by

$$\begin{bmatrix} \frac{da(t)}{dt} \end{bmatrix}_{k} = \frac{\omega_{m}}{2\pi} \int_{t-\frac{2\pi}{\omega_{m}}}^{t} \frac{da(\tau)}{d\tau} \exp(-jk\omega_{m}\tau)d\tau$$
$$= \frac{da_{k}(t)}{dt} + jk\omega_{m}a_{k}(t)$$
(15)

where []<sub>k</sub> denotes consistently the kth component of the moving average, with k = 0 corresponding to the dc component, k = 1 the  $\omega_m$  component, and k = 2 the  $2\omega_m$  component. Furthermore, the following relations are readily obtained:

$$[a(t)\cos 2\omega_m t]_k = \frac{1}{2}(a_{k-2} + a_{k+2})$$
(16)

$$\begin{bmatrix} a^{2}(t) \end{bmatrix}_{0} = a_{0}^{2} + 2|a_{1}|^{2} + 2|a_{2}|^{2}$$
(17)  
$$\begin{bmatrix} a^{2}(t) \end{bmatrix}_{i} = 2a_{0}a_{1} + 2(a_{1i}a_{2i} + a_{1r}a_{2r})$$

$$+ j(a_{1r}a_{2i} - a_{1i}a_{2r}))$$
(18)

$$[a^{2}(t)]_{2} = (a_{1}^{*})^{2} + 2a_{0}a_{2}.$$
(19)

*Remarks:* It should be noted that the essential procedure of taking the moving average differs from the basic double-averaging method of [6] where a constant steady-state point is assumed. Specifically, the method of [6] results in a model that reduces all variables varying at the mains frequency to fixed steady-state points. Thus, the subsequent small-signal analysis only reflects stability of the mains cycle but falls short of identifying oscillations at other subharmonics which have been found experimentally. As will be shown shortly, a moving-averaged model, coupled with the application of a harmonic balance procedure at any specific harmonic of interest, permits a quick identification of forced oscillation which may occur at half, quarter, etc. of the mains frequency. A similar application was also reported in Gordillo et al. [14], where bifurcation resulting from varying the output voltage was studied. However, practical stability information that is presented in terms of major circuit parameters is still unavailable.

# *C. Derivation of the Double Averaged Model for PFC Boost Converter*

In this subsection we apply the foregoing averaging to (4) and (8). We expect to get three equations from each of (4) and (8), corresponding to the dc,  $\omega_m$  and  $2\omega_m$  components. To avoid possible confusion due to mixup in subscripts, we define

$$\begin{cases} x = v_o(t) \\ y = p(t). \end{cases}$$
(20)

In the following, the use of subscripts will be consistent with the description of the second averaging in Section III-B, i.e., subscript 0 for dc components, subscript 1 for mains frequency components, and subscript 2 for twice mains frequency components. Now, application of second averaging to (8) gives

$$\frac{C}{2} \frac{d}{dt} \left( x_{0}^{2} + 2x_{1r}^{2} + 2x_{1i}^{2} + 2x_{2r}^{2} + 2x_{2r}^{2} \right) \\
+ \frac{\left( x_{0}^{2} + 2x_{1r}^{2} + 2x_{1i}^{2} + 2x_{2r}^{2} + 2x_{2r}^{2} \right)}{R} \\
= y_{0} - y_{2r} \tag{21} \\
\frac{C}{2} \frac{d}{dt} \left( x_{0}x_{1} + x_{1r}x_{2r} + x_{1i}x_{2i} + j(x_{1r}x_{2i} - x_{1i}x_{2r}) \right) \\
+ \left( \frac{j\omega_{m}C}{2} + \frac{1}{R} \right) \left( x_{0}x_{1} + x_{1r}x_{2r} + x_{1i}x_{2i} \\
+ j(x_{1r}x_{2i} - x_{1i}x_{2r}) \right) \\
= \frac{y_{1}}{2} - \frac{y_{-1}}{4} \tag{22} \\
\frac{C}{2} \frac{d}{dt} \left( 2x_{0}x_{2} + (x_{1}^{*})^{2} \right) + \left( j\omega_{m}C + \frac{1}{R} \right) \left( 2x_{0}x_{2} + (x_{1}^{*})^{2} \right) \\
= y_{2} - \frac{y_{0}}{2}. \tag{23}$$



Fig. 3. Stability boundaries with: (a)  $G_F = 12.8$  A,  $C = 60 \ \mu\text{F}$ , and  $\omega_m = 2\pi(60)$  rad/s; (b)  $\tau_F = 8.6$  ms,  $C = 60 \ \mu\text{F}$ , and  $\omega_m = 2\pi(60)$  rad/s; (c)  $G_F = 12.8$  A,  $C = 60 \ \mu$ F, and  $\omega_m = 2\pi(60)$  rad/s; (d)  $\tau_F = 8.6$  ms,  $G_F = 12.8$  A, and  $\omega_m = 2\pi(60)$  rad/s. Region above the boundary surface or curve corresponds to stable operating region. At the boundary, period-doubling occurs to disrupt normal operation. Region under the boundary surface or curve corresponds to "unstable operation" in the sense that normal operation cannot be maintained.

Note that  $y_{-1}$  is equal to  $y_1^*$ . Moreover, (4) is decomposed into

$$\tau_F \frac{dy_0}{dt} + y_0 = -G_F(x_0 - V_{\text{ref}}) \quad (24)$$

$$\tau_F \frac{dy_1}{dt} + (j\omega_m \tau_F + 1)y_1 = -G_F x_1$$
(25)

$$\tau_F \frac{dy_2}{dt} + (j2\omega_m \tau_F + 1)y_2 = -G_F x_2.$$
 (26)

Thus, (21)-(26) are the double averaged equations for the PFC boost converter under study.

# **IV. STEADY-STATE SOLUTION**

To find the steady-state solution, we put all time-derivatives to zero, giving

$$x_1 = y_1 = 0 \tag{27}$$

$$x_{2r} = \left\lfloor \frac{V_{\text{ref}} - x_0}{2\Delta} \right\rfloor \left\lfloor 1 + 2x_0 \frac{1 + 4\omega_m^2 \tau_F^2}{G_F R} \right\rfloor$$
(28)

$$x_{2i} = \left[\frac{\omega_m (V_{\text{ref}} - x_0)}{\Delta}\right] \left[-\tau_F + x_0 \frac{1 + 4\omega_m^2 \tau_F^2}{G_F}\right]$$
(29)

$$y_{2r} = \left[\frac{V_{\text{ref}} - x_0}{\Delta}\right] \left[G_F + 2x_0 \frac{1 - 2\omega_m^2 C R \tau_F}{R}\right] \quad (30)$$

$$y_{2i} = \left\lfloor \frac{v_{\text{ref}} - x_0}{\Delta} \right\rfloor \left\lfloor \omega_m C + \frac{2\omega_m \tau_F}{R} \right\rfloor x_0 \tag{31}$$

where

. 1.

$$\Delta = 1 + \frac{4\left(1 - 2\omega_m^2 C R \tau_F\right) x_0}{G_F R} + \frac{4\left(1 + \omega_m^2 C^2 R^2\right)\left(1 + 4\omega_m^2 \tau_F^2\right) x_0^2}{G_F^2 R^2}.$$
 (32)

Putting the above equations in (21), and using (24), we get

$$8 \left(1 + \omega_m^2 C^2 R^2\right) \left(1 + 4\omega_m^2 \tau_F^2\right) x_0^4 + 8 G_F R \left[2 - 2\omega_m^2 C R \tau_F + 4\omega_m^2 \tau_F^2 + \omega_m^2 C^2 R^2 \left(1 + 4\omega_m^2 \tau_F^2\right)\right] x_0^3 + G_F R \left[-8 V_{\text{ref}} \left(1 + \omega_m^2 C^2 R^2\right) \left(1 + 4\omega_m^2 \tau_F^2\right) + G_F R \left(9 - 12\omega_m^2 C R \tau_F + 4\omega_m^2 \tau_F^2\right)\right] x_0^2 + G_F^2 R^2 \left[G_F R + 2 V_{\text{ref}} \left(-4 + 6\omega_m^2 C R \tau_F - 4\omega_m^2 \tau_F^2\right)\right] x_0 + G_F^2 R^2 V_{\text{ref}} \left[-G_F R + V_{\text{ref}} \left(1 + 4\omega_m^2 \tau_F^2\right)\right] = 0$$
(33)

Authorized licensed use limited to: IEEE Xplore. Downloaded on December 18, 2008 at 02:16 from IEEE Xplore. Restrictions apply.



Fig. 4. Corresponding stability boundaries with: (a)  $G_F = 128$  A,  $C = 600 \ \mu$ F, and  $\omega_m = 2\pi(60)$  rad/s; (b)  $\tau_F = 8.6$  ms,  $C = 600 \ \mu$ F, and  $\omega_m = 2\pi(60)$  rad/s; (c)  $G_F = 128$  A,  $C = 600 \ \mu$ F, and  $\omega_m = 2\pi(60)$  rad/s; (d)  $\tau_F = 8.6$  ms,  $G_F = 128$  A, and  $\omega_m = 2\pi(60)$  rad/s.

from which  $x_0$  can be solved numerically. Note that  $x_0$  is the steady-state dc component of the output voltage, which is close to  $V_{\text{ref}}$ .

# V. APPLICATION OF DOUBLE AVERAGED MODEL TO STABILITY ANALYSIS

Our purpose in this section is to examine the stability of the system with respect to its operation at the forcing frequency  $2\omega_m$ , which is twice the mains frequency. This is because the input is a rectified sine wave repeating at  $2\omega_m$ , and all variables in the system are expected to repeat at this forcing frequency. Moreover, if the system is entrapped in an orbit of frequency  $\omega_m$ , i.e., at half the expected repetition frequency,<sup>1</sup> the operation is considered undesirable for practical purposes since the device stresses would be drastically altered.

<sup>1</sup>In the literature of dynamical systems, such a change in operation is known as period doubling, and the transition from the normal operation to a double-period operation is typically a sudden transition. The usual term used to describe this transition is *bifurcation* [20], [21].

In fact, such "period-doubling" has been observed experimentally when certain parameters are changed irrespective of the stability of the  $2\omega_m$ -orbit [11], [12]. It is therefore of practical importance to locate the occurrence of period-doubling in the parameter space. In our double-averaged model, this is equivalent to finding the condition under which the system is entrapped into an  $\omega_m$ -orbit.

Our approach to solve this stability problem is to consider the loop gain of  $x_1$  (or  $y_1$ ) in our double averaged model. Thus, from (22) and (25), and the steady-state values solved from (28) through (31), putting the time derivatives to zero, we get the loop gain for  $x_1$  as shown in (34) at the bottom of the next page.

The system operates at the expected forcing frequency if it is not entrapped in the  $\omega_m$ -orbit, i.e.,  $T_{x1} < 1$ . (It turns out that the condition  $T_{x1} > -1$  is irrelevant for practical sets of parameter values.) This condition is necessary for the system to operate in the expected regime. We may simplify (34) by putting  $x_0 \approx V_{\text{ref}}$ , giving (35), shown at the bottom of the next page. We note that the condition of  $T_{x1} < 1$  is equivalent to (36), also shown at the bottom of the next page.



Fig. 5. Schematic of experimental PFC boost converter under average current-mode control.

This means that normal operation of the circuit at  $2\omega_m$  can be disrupted by period-doubling if there are parametric variations that violates (36). This is sometimes considered as instability from an engineering viewpoint as the system fails to operate in the expected manner. In particular, we observe the following.

- 1) There is a lower limit of  $v_o/V_{\rm in}$  below which the system cannot operate normally at the forced frequency  $2\omega_m$  (i.e, operating range is limited).
- 2) This lower limit of  $v_o/V_{in}$  increases as the load resistance increases (i.e., normal operation is disrupted by period-doubling as output power decreases to a certain threshold).
- 3) This lower limit of  $v_o/V_{in}$  generally increases as  $G_F$  increases (i.e., operating range is reduced as the feedback gain increases).

- 4) This lower limit of  $v_o/V_{in}$  generally increases as  $\tau_F$  decreases (i.e., operating range is reduced as the feedback time-constant decreases).
- 5) This lower limit of  $v_o/V_{\rm in}$  generally increases as the output capacitance decreases (i.e., operating range is reduced as output capacitance decreases).

# VI. GRAPHICAL REPRESENTATIONS OF STABILITY BOUNDARIES

To help visualize the above results, we use (36) to plot a few indicative boundary surface and curves. Our purpose is to highlight the regions in the parameter space where normal operation is expected. Figs. 3 and 4 show several operation boundaries corresponding to some specific sets of parameters. On the boundary surface or curves, period-doubling occurs and normal operation

$$T_{x_{1}} = \frac{G_{F}R}{2(4+\omega_{m}^{2}C^{2}R^{2})(1+\omega_{m}^{2}\tau_{F}^{2})(x_{0}^{2}-x_{2r}^{2}-x_{2i}^{2})} \left\{ \omega_{m}CR(\omega_{m}\tau_{F}(2x_{0}-x_{2r})+x_{2i})-2(\omega_{m}\tau_{F}x_{2i}+2x_{0}+x_{2r}) + \sqrt{-3(4+\omega_{m}^{2}C^{2}R^{2})(1+\omega_{m}^{2}\tau_{F}^{2})(x_{0}^{2}-x_{2r}^{2}-x_{2i}^{2})+\left[-2(\omega_{m}\tau_{F}x_{2i}+2x_{0}+x_{2r})+\omega_{m}CR(\omega_{m}\tau_{F}(2x_{0}-x_{2r})+x_{2i})\right]^{2}} \right\}$$
(34)

$$T_{x_1} = \frac{-\frac{3}{8}G_F R}{x_0 \left[1 - \frac{1}{2}\omega_m^2 CR\tau_F + \frac{1}{2}\sqrt{1 - 4\omega_m^2 CR\tau_F - 3\omega_m^2 \tau_F^2 + \frac{1}{4}\omega_m^2 C^2 R^2 \left(\omega_m^2 \tau_F^2 - 3\right)}\right]}$$
(35)

$$x_{0} > \frac{G_{F}R\left[\omega_{m}^{2}CR\tau_{F} - 2 + \sqrt{1 - 4\omega_{m}^{2}CR\tau_{F} - 3\omega_{m}^{2}\tau_{F}^{2} + \frac{1}{4}\omega_{m}^{2}C^{2}R^{2}\left(\omega_{m}^{2}\tau_{F}^{2} - 3\right)\right]}{(4 + \omega_{m}^{2}C^{2}R^{2}\left(1 + \omega_{m}^{2}\tau_{F}^{2}\right)}$$
(36)



Fig. 6. Measured waveforms and phase portraits. (a), (b) Normal operation with  $R = 454 \Omega$  and  $V_o = 297$  V; (c), (d) normal operation with  $R = 645 \Omega$  and  $V_o = 360$  V; (e), (f) period-doubled (unstable) operation with  $R = 645 \Omega$  and  $V_o = 297$  V. For all cases,  $G_F \approx 20$  A, input voltage is 100 V rms,  $\tau_F = 8.46$  ms, and line frequency is 50 Hz. For waveforms in (a), (c) and (e), upper trace is line voltage, middle trace is input current (1 A/div) and lower trace is output voltage ripple. For phase portraits in (b), (d) and (f), x axis is input current and y axis is output voltage.

ceases. Region above the boundary corresponds to normal operation, where the system is driven periodically at twice the mains frequency. These results are consistent with our observation. Specifically, the operation is stable up to a certain threshold set of parameter values. In Fig. 3, the capacitor values are relatively small and the output ripple is therefore relatively large, causing instability when such a large ripple is being fed back. Moreover, in Fig. 4, the capacitor values are large (normal in practical sense), and instability can only occur for relatively high feedback gain values. Our analysis provides a quantitative tool for evaluating the stability boundaries. We will verify this tool in the next section by experiments.

## VII. EXPERIMENTAL VERIFICATIONS

An experimental PFC boost converter under average current mode control has been constructed for verification purposes. The complete schematic is shown in Fig. 5. The current-shaping control employs a standard 3854A integrated circuit controller. The line frequency is 50 Hz. The input voltage is fixed at 100 V rms.

Experiments have been performed to verify the stability boundaries predicted in Sections V and VI. First, for a quick illustration, typical waveforms and phase portraits for normal operation and period-doubled (unstable) operation are shown in Fig. 6.

Our main objective is to locate the parameter points where period-doubling occurs. For convenience we replot the stability curves along with the experimental data in Fig. 7. We clearly see that the experimental data are in good agreement with the analytical results.

A few practical points are worth noting here.

- 1) One of the determining parameters is  $v_o/V_{in}$ , as shown in our analytical study. However, for measurement convenience, we fix the input voltage in our experiments and vary the output voltage.
- 2) In our analytical model, the feedback loop is defined by two parameters, namely  $G_F$  and  $\tau_F$ . In order to find the corresponding value of  $G_F$  for the experimental circuit, we have to carefully identify all gains that may possibly contribute to  $G_F$  since the model assumes no gain in the multiplier-divider block. In our experiment, we find this gain by direct measurement.
- 3) Our definition of  $\tau_F$  is the real time constant. Thus, its reciprocal should be compared directly with the angular mains frequency in rad/s instead of the frequency in



Fig. 7. Measured stability boundaries taken at  $V_{in} = 100 \text{ V}$  (rms) and 50 Hz line frequency. Experimental data points are plotted as dots. Solid curves are from analytical expressions. (a)  $C = 69 \ \mu\text{F}$  and  $\tau_F = 8.46 \text{ ms}$ ; (b)  $C = 69 \ \mu\text{F}$  and  $G_F = 17.46 \text{ A}$ ; (c)  $G_F = 17.46 \text{ A}$  and  $\tau_F = 8.46 \text{ ms}$ . Note that the y axis would have been  $v_o/V_{in}$  for better generality, as shown in our analytical study. However, for measurement convenience, we have fixed the input voltage in our experiments and varied the output voltage.

Hertz. For instance,  $\tau_F = 1/2\pi(50)$  s = 3.18 ms refers to a time constant equal to the period of a 50 Hz signal.

4) We have performed the experiments for relatively small output capacitor values in order to allow instability to be seen with the usual gain values readily implementable by the particular controller UC3854A. However, we should stress that the phenomenon remains invariant for practical capacitor values (e.g., hundreds of  $\mu$ F) provided that higher feedback gain values are used accordingly, as per Fig. 4 or (36).

## VIII. CONCLUSION

At present, the main technique for the analysis of power converters is based on averaging and subsequent small-signal linearization. Since PFC converters are subject to a time-varying input voltage and the current-shaping process is nonlinear, the results obtained from small-signal linear analysis have limited validity and often provide inaccurate stability information, as has been explained and demonstrated by Orabi *et al.* [11]–[13] with computer simulations and laboratory measurements. Nonlinear analytical models would be desirable for stability evaluation. This paper has applied a method of double averaging for modeling PFC converters, leading to nonlinear models from which closed-form stability conditions can be derived. This method is capable of revealing the *low-frequency stability* of the circuit. For instability near the switching frequency, moreover, we need a different type of models, such as those based on discrete-time mapping [15], [22], for revealing the fast-scale instability.

#### ACKNOWLEDGMENT

The authors wish to express their gratitude to the reviewers for drawing their attention to [6] and [14], and to one reviewer for clarifying the issue of omitting the inductor current dynamics.

#### REFERENCES

- L. H. Dixon, Jr., "High power factor preregulator for off-line power supplies," in Unitrode Switching Regulated Power Supply Design Manual. Merrimack, NH: Unitrode Corp., 1990.
- [2] R. Redl, "Power-factor-correction in single-phase switching-mode power supplies—An overview," *Int. J. Electron.*, vol. 77, no. 5, pp. 555–582, 1994.
- [3] P. C. Stefanov and A. M. Stanković, "Modeling of UPFC operation under unbalanced conditions with dynamic phasors," *IEEE Trans. Power Syst.*, vol. 17, no. 2, pp. 395–403, May 2002.
- [4] A. M. Stanković, S. R. Sanders, and T. Aydin, "Dynamic phasors in analysis of unbalanced polyphase ac machines," *IEEE Trans. Energy Convers.*, vol. 17, no. 1, pp. 107–113, Mar. 2002.

- [5] D. Maksimović, A. M. Stanković, V. J. Thottuvelil, and G. C. Verghese, "Modeling and simulation of power electronic converters," *Proc. IEEE*, vol. 89, no. 6, pp. 898–912, Jun. 2001.
- [6] V. J. Thottuvelil, D. Chin, and G. Verghese, "Hierachical approaches to modeling high-power-factor AC-DC converters," *IEEE Trans. Power Electron.*, vol. 6, no. 2, pp. 179–187, Apr. 1991.
- [7] G. Zhu, H. Wei, P. Kornetzky, and I. Batarseh, "Small-signal modeling of a single-switch AC/DC power factor correction circuit," in *IEEE Power Electron. Specialists Conf. Rec.*, Fukuoka, Japan, 1998, pp. 601–607.
- [8] G. Zhu, C. Iannello, P. Kornetzky, and I. Batarseh, "Large-signal modeling of a single-switch power factor correction converter," in *IEEE Power Electron. Specialists Conf. Rec.*, Galway, Ireland, 2000, pp. 1351–1357.
- [9] V. S. Murali, C. K. Tse, and M. H. L. Chow, "Small-signal analysis of single-stage cascaded boost-and-buck PFC converters," in *IEEE Power Electron. Specialists Conf. Rec.*, Fukuoka, Japan, 1998, pp. 608–614.
- [10] M. Shen and Z. Qian, "A unified average model for single stage and two stage power factor correction converters," in *Proc. Appl. Power Electron. Conf. Expo.*, 2001, pp. 125–129.
- [11] M. Orabi, T. Ninomiya, and C. Jin, "Nonlinear dynamics and stability analyzes of boost power-factor-correction circuit," in *Proc. Int. Conf. Power Syst. Tech.*, 2002, pp. 600–605.
- [12] —, "A novel modeling of instability phenomena in PFC converter," in Proc. IEEE Int. Telecommun. Energy Conf., 2002, pp. 566–573.
- [13] M. Orabi and T. Ninomiya, "Nonlinear dynamics of power-factor-correction converter," *IEEE Trans. Ind. Electron.*, vol. 50, no. 6, pp. 1116–1125, Dec. 2003.
- [14] F. Gordillo, G. Escobar, and J. Aracil, "Bifurcation analysis of a power factor precompensator," in *Stability and Stabilization of Nonlinear Systems.* London, U.K.: Springer-Verlag, 1999.
- [15] H. H. C. Iu, Y. Zhou, and C. K. Tse, "Fast-scale instability in a PFC boost converter under average current mode control," *Int. J. Circuit Theory Appl.*, vol. 31, no. 6, pp. 611–624, 2003.
- [16] X. Wu, C. K. Tse, O. Dranga, and J. Lu, "Fast-scale instability of single-stage power-factor-correction power supplies," *IEEE Trans. Circuits Syst. I*, vol. 53, no. 1, pp. 204–213, Jan. 2006.
- [17] R. D. Middlebrook and S. Ćuk, "A general unified approach to modeling switching-converter power stages," in *IEEE Power Electron. Spec. Conf. Rec.*, Pasadena, CA, 1976, pp. 18–34.
- [18] C. K. Tse, Y. S. Lee, and W. C. So, "An approach to modeling DC-DC converters using graph theoretic concepts," *Int. J. Circuit Theory Appl.*, vol. 21, no. 4, pp. 371–384, 1993.
- [19] S. R. Sanders, J. M. Noworolski, X. Z. Liu, and G. Verghese, "Generalized averaging method for power electronic circuits," *IEEE Trans. Power Electron.*, vol. 6, no. 2, pp. 251–259, Mar. 1991.
- [20] S. Banerjee and G. Verghese, Eds., Nonlinear Phenomena in Power Electronics: Attractors, Bifurcations, Chaos and Nonlinear Control. New York: IEEE Press, 2001.
- [21] C. K. Tse, Complex Behavior of Switching Power Converters. Boca Raton, FL: CRC Press, 2003.
- [22] O. Dranga, C. K. Tse, H. H. C. Iu, and I. Nagy, "Bifurcation behavior in power-factor-correction boost converter," *Int. J. Bifurc. Chaos*, vol. 13, no. 10, pp. 3107–3114, 2003.



**Chi K. Tse** (M'90–SM'97–F'06) received the B.Eng. (Hons) degree with first class honors in electrical engineering and the Ph.D. degree from the University of Melbourne, Melbourne, Australia, in 1987 and 1991, respectively.

He is presently Chair Professor of Electronic Engineering at the Hong Kong Polytechnic University, Hong Kong, and his research interests include chaotic dynamics and power electronics. He is the author of *Linear Circuit Analysis* (Reading, MA: Addison-Wesley, 1998) and *Complex Behavior of Switching* 

*Power Converters* (Boca Raton, FL: CRC Press, 2003), and co-author of *Chaos-Based Digital Communication Systems* (New York: Springer-Verlag, 2003) and *Signal Reconstruction With Applications to Chaos-Based Communications* (Beijing, China: Tsinghua University Press, 2005), and co-holder of a U.S. patent.

Dr. Tse served as an Associate Editor for the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS—I: FUNDAMENTAL THEORY AND APPLICATIONS from 1999 to 2001, and since 1999, he has been an Associate Editor for the IEEE TRANSACTIONS ON POWER ELECTRONICS. He currently also serves as an Associate Editor of the *International Journal of Systems Science*, a Guest Associate Editor of the *IEICE Transactions on Fundamentals of Electronics, Communications and Computers*, and a Guest Editor of *Circuits, Systems and Signal Processing*. He was awarded the L.R. East Prize by the Institution of Engineers, Australia, in 1987. He won the IEEE TRANSACTIONS ON POWER ELECTRONICS Prize Paper Award for 2001 and the *International Journal of Circuit Theory and Applications* Best Paper Award for 2003. In 2005, he was appointed an IEEE Distinguished Lecturer. While with Hong Kong Polytechnic University, he twice received the President's Award for Achievement in Research, the Faculty's Best Researcher Award, the Research Grant Achievement Award, and a few other teaching awards.



**Mohamed Orabi** (S'02–M'04) was born in Kena, Egypt, in 1974. He received the B.Sc. and M.Sc. degrees in electrical engineering from Elminia University, Elminia, Egypt, in 1996 and 2000, respectively, and the Ph.D. degree in power electronics from Kyushu University, Fukuoka, Japan, in 2004.

He has been with the Department of Electrical Engineering, Aswan Faculty of Engineering, South Valley University, Aswan, Egypt, first as an Administrator from 1996 to 2000, then as Research Assistant from 2000 to 2004, and as an Assistant

Professor since 2004. His research interests include developing and designing switched-mode power converters, applications to power factor correction, stability problems, nonlinear phenomena, chaos, and period doubling bifurcation. He has authored more than 30 published papers.

Dr. Orabi received the 2002 Excellent Student Award of the IEEE Fukuoka Section, the Best Paper Award of 28th Annual Conference of the IEEE Industrial Electronics Society in 2002, the IEEE IES Student Grant from 2003 IEEE International Symposium on Industrial Electronics, and the IEICE Best Young Researcher Award for 2004.



Tamotsu Ninomiya (M'89–SM'98–F'01) received the B.E., M.E., and Dr. Eng. degrees in electronics from Kyushu University, Fukuoka, Japan, in 1967, 1969, and 1981, respectively.

Since 1969, he has been with the Department of Electronics, Kyushu University, first as a Research Assistant and, since 1988, as a Full Professor. Since the reorganization in 1996, he has been a Professor in the Department of Electrical and Electronic Systems Engineering of the Graduate School of Information Science and Electrical Engineering. He is a

specialist in the field of power electronics, including the analysis of switching power converters and their electromagnetic interference problems, the development of noise-suppression techniques, and piezoelectric transformer converters. He has authored about 200 published technical papers.

Prof. Ninomiya has served as a Member of the Program Committee and Session Chairman of the IEEE Power Electronics Specialist Conference (PESC) sponsored by the IEEE Power Electronics Society (PELS) for many years. He was Program Vice-Chairman for the 1988 PESC and General Chairman for the 1998 PESC. He was a Member of the Administrative Committee of the IEEE PELS from 1993 through 1998. He served as Chairman of the Professional Group on High-Frequency Applied Magnetics of the Institute of Electrical Engineers of Japan, and as Chairman of the Technical Group on Power Engineering in Electronics and Communications of the Institute of Electronics, Information, and Communication Engineers of Japan.



Siu-Chung Wong (M'01) received the B.Sc. degree in physics from the University of Hong Kong, Hong Kong, the M.Phil. degree in electronics from the Chinese University of Hong Kong, Hong Kong, and the Ph.D. degree from the University of Southampton, Southampton, U.K., in in 1986, 1989, and 1997, respectively.

He is currently an Assistant Professor in the Department of Electronic and Information Engineering at Hong Kong Polytechnic University, Hong Kong. His research interests include modeling of power

converters, nonlinear analysis of power electronics and Internet traffic analysis. Dr. Wong is a member of the Electrical College, The Institution of Engineers, Australia.