

# V2G Control Strategy for Single-Phase Matrix Converter-Based WPT System to Suppress Grid Current Harmonics

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**Abstract:** The bidirectional power transfer capability of matrix converter-based wireless power transfer (WPT) systems renders them suitable for vehicle-to-grid (V2G) applications. However, grid voltage harmonics induce severe grid current distortion, which limits their practical applicability. In this paper, a linearized model of the matrix converter-based WPT system under V2G operation is established. Based on this, the impact of grid voltage harmonics on grid current harmonics is analyzed. To address this issue, a composite control architecture integrating grid voltage feedforward compensation and grid current feedback regulation is proposed. Building upon this architecture, a proportional-resonant composite iterative learning controller is developed for closed-loop current control. The parameters of both the feedforward and closed-loop controllers are designed to ensure system stability and enhance harmonic suppression. Finally, the feasibility of the proposed method is verified through experimental verification.

**Item index:** Wireless power transfer, vehicle-to-grid, harmonic suppression, control strategy.

## I. INTRODUCTION

Wireless power transfer (WPT) technology [1-3] holds great promise in the field of electric vehicle (EV) charging [4-7]. It has several advantages, including contactless operation, high automation, and strong environmental adaptability. Bidirectional WPT technology further enables vehicle-to-grid (V2G) operation. EVs are transformed from passive electricity consumers into mobile energy storage units. These units can participate in peak shaving, grid frequency regulation, and voltage stabilization [8, 9].

The key to achieving V2G lies in constructing a power conversion system capable of bidirectional AC-DC conversion [10]. This requirement inherently eliminates all topologies based on diode rectifiers, which support only unidirectional power flow [11, 12]. Currently, bidirectional WPT systems typically adopt active full-bridge converters

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on the EV battery side. On the grid side, a two-stage topology is usually employed, consisting of a low-frequency full-bridge rectifier cascaded with a high-frequency full-bridge inverter [13-15]. However, the two-stage topology involves an intermediate DC bus and large electrolytic capacitors, increasing the system volume and significantly reducing the lifespan. Moreover, the cascaded nature of the two-stage topology reduces the system stability margin [16].

Matrix converters (MC) are highly suitable for V2G applications, due to their capability of direct and bidirectional conversion from low-frequency AC to high-frequency AC [17-19]. MC-based WPT systems eliminate the intermediate DC link and bulky electrolytic capacitors. This greatly improves system power density and operational lifespan, addressing the issues associated with conventional two-stage topologies. With the advent of integrated bidirectional SiCs and GaNs [20], the number of power switches required in MC can be halved. This significantly reduces hardware costs and further enhances power density and efficiency in WPT systems.

To ensure stable energy exchange between the grid and EVs, control strategies for bidirectional WPT systems are also important. Current research primarily focuses on accurate power flow regulation [21-22] and unity power factor control [23]. Control strategies based on phase-shift modulation combined with phase synchronization are widely verified to support switching between V2G and grid-to-vehicle (G2V) modes [19]. By introducing control variables such as primary-side phase shift, secondary-side phase shift, and the phase delay between the two, the active and reactive power regulation is achieved [21]. It maintains unity power factor operation and significantly reduces harmonic content in the system.

However, existing V2G control strategies [21-23] are generally based on the assumption of an ideal sinusoidal grid voltage and fail to consider the impact of grid voltage harmonics on system performance. In practical grid conditions, voltage distortion becomes a non-negligible issue [24]. Harmonics in grid voltage lead to increased grid current harmonics on the output side of the WPT system. This degrades power factor, increases system losses, and worsens grid pollution. Conventional PI and PR controllers lack sufficient gain at harmonic frequencies, resulting in poor harmonic suppression capability [25]. Therefore, under non-ideal grid conditions, how to effectively suppress harmonic components in the grid current is a key technical challenge hindering the further development of WPT V2G applications.

To address the above issues, this paper proposes a V2G control strategy for the MC-based WPT system aimed at suppressing grid current harmonics under distorted grid

conditions. The innovations and contributions of this paper are as follows. (1) A linearized model of the MC-based WPT system under V2G operation is established, enabling a clear analysis of the impact of grid voltage harmonics on grid current distortion. (2) A composite control architecture integrating grid voltage feedforward compensation and grid current closed-loop regulation is proposed to mitigate the effect of voltage harmonics. (3) A proportional-resonant composite iterative learning controller is proposed to further suppress grid current harmonics. (4) Based on the built model, the design process of the proposed composite controller is presented to improve the stability margin and transient performance.

## II. OPERATION PRINCIPLE OF MC-BASED WPT SYSTEM UNDER V2G OPERATION

The topology of the MC-based WPT system is shown in Fig. 1. Owing to the bidirectional power transfer capability of the full-bridge inverter and MC, the system can operate in both G2V and V2G modes. While the G2V operation in MC-WPT systems is extensively researched [17-19], this paper focuses on the V2G operation, particularly improving its grid current quality. By the inverter ( $S_1$ - $S_4$ ), the DC voltage ( $U_b$ ) of the EV battery is converted to a high-frequency voltage ( $u_{ab}$ ). Through the series-to-series (S-S) network and coils, the power is transmitted wirelessly from the primary side to the secondary side. On the secondary side, the high-frequency AC voltage ( $u_{cd}$ ) is directly converted to the low-frequency AC voltage by the matrix converter ( $Q_1$ - $Q_4$ ). Through the grid filter, the power is delivered to the grid, and the V2G operation is achieved.

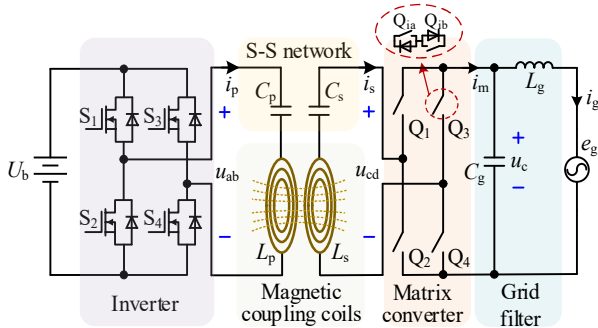


Fig. 1. Topology of single-phase MC-based WPT system under V2G operation.

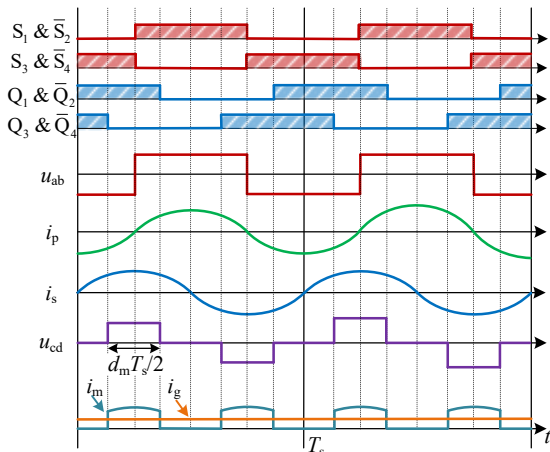


Fig. 2. Key waveforms of MC-based WPT system under V2G operation.

In Fig. 1,  $i_p$  and  $i_s$  are the primary and secondary coil

currents, respectively.  $i_m$  is the MC output current, and  $i_g$  is the grid current.  $u_c$  is the filter capacitor voltage.  $e_g$  is the grid voltage. In an ideal grid,  $e_g = U_g \sin(\omega_1 t)$ .  $U_g$  represents the grid voltage amplitude, and  $\omega_1$  represents the grid fundamental angular frequency. The fundamental RMS values of  $u_{ab}$ ,  $u_{cd}$ ,  $i_p$ , and  $i_s$  are represented by  $U_{ab}$ ,  $U_{cd}$ ,  $I_p$ , and  $I_s$ , respectively.

The key operation waveforms under the V2G operation are shown in Fig. 2. On the primary side, the phase-shift modulation is adopted to adjust the inverter. The duty cycle ratio is set as 1, and the waveform of  $u_{ab}$  is a square wave. The fundamental RMS value of  $u_{ab}$  is expressed as

$$U_{ab} = \frac{2\sqrt{2}}{\pi} U_b \quad (1)$$

Then, the power is transmitted from the primary side to the secondary side through the S-S network. According to the steady-state characteristic of the S-S network under the resonant state, the RMS value of the secondary coil current is derived as

$$I_s = \frac{U_{ab}}{\omega_r M} = \frac{2\sqrt{2} U_b}{\pi \omega_r M} \quad (2)$$

where  $\omega_r$  is the resonant angular frequency.  $M$  is the mutual inductance of the magnetic coupling coils.

On the secondary side, the MC is also controlled by the phase-shift modulation. By adjusting the duty cycle ratio of MC ( $0 \leq d_m \leq 1$ ), the average value of the MC output current ( $I_m$ ) at a switching cycle is controlled as

$$I_m = \frac{2\sqrt{2}}{\pi} I_s \sin\left(\frac{\pi}{2} d_m\right) \text{sgn}(u_c) \quad (3)$$

Since the time constant of the grid filter is small,  $u_c \approx e_g$ , and  $I_m \approx i_g$  under the ideal grid. Therefore, the grid current is controlled by adjusting  $d_m$ . To ensure the high quality of the grid current,  $i_g$  should be controlled as  $I_g^* \sin(\omega_1 t)$ .  $I_g^*$  is the desired amplitude of the grid current. According to (2) and (3),  $d_m$  under the ideal grid is derived as

$$d_{m\_ideal} = \frac{2}{\pi} \arcsin \left[ \frac{\pi^2 \omega_r M I_g^* |\sin(\omega_1 t)|}{8 U_b} \right] \quad (4)$$

By adjusting  $d_m$  according to (4), the grid current can be shaped into a sinusoidal waveform that is in phase with the grid voltage under ideal grid conditions.

## III. MODELING AND CHARACTERISTIC ANALYSIS UNDER GRID HARMONICS CONDITION

In practical grids, odd-order harmonics are commonly present in the grid voltage, which adversely affects the quality of the grid current. In this section, the model of the MC-based WPT system under V2G operation is built. Based on this, the grid current characteristics under the grid voltage harmonics are analyzed to provide the theoretical basis for the solution method.

In the MC-based WPT system, the MC duty cycle ratio  $d_m$  is the control variable, and the non-ideal grid voltage with harmonics is the perturbation variable. To analyze the system behavior, the transfer functions from both the MC duty cycle ratio and the grid voltage to the grid current must be derived through system modeling.

The overall model of the MC-based WPT system consists of four parts, including the primary inverter, S-S resonant network, secondary MC, and grid filter. The inverter model

is given in (1). The other three parts are derived as follows.

First, the model of the S-S resonant network is built. According to the extended description function method [26], the transfer function from  $U_{cd}$  to  $I_s$  is derived as

$$G_{U_{cd}-I_s} = \frac{I_s}{U_{cd}} = C(sI - A)^{-1} B \quad (5)$$

$$A = \begin{bmatrix} \frac{L_s R_p}{\Delta} & \omega_r & \frac{L_s}{\Delta} & 0 & \frac{M}{\Delta}(R_s + R_{ac}) & 0 & \frac{M}{\Delta} & 0 \\ -\omega_r & \frac{L_s R_p}{\Delta} & 0 & \frac{L_s}{\Delta} & 0 & \frac{M}{\Delta} R_s & 0 & \frac{M}{\Delta} \\ \frac{1}{C_p} & 0 & 0 & \omega_r & 0 & 0 & 0 & 0 \\ 0 & \frac{1}{C_p} & -\omega_r & 0 & 0 & 0 & 0 & 0 \\ \frac{M}{\Delta} R_p & 0 & \frac{M}{\Delta} & 0 & \frac{L_p}{\Delta}(R_s + R_{ac}) & \omega_r & \frac{L_p}{\Delta} & 0 \\ 0 & \frac{M}{\Delta} R_p & 0 & \frac{M}{\Delta} & -\omega_r & \frac{L_p}{\Delta} R_s & 0 & \frac{L_p}{\Delta} \\ 0 & 0 & 0 & 0 & \frac{1}{C_s} & 0 & 0 & \omega_r \\ 0 & 0 & 0 & 0 & 0 & \frac{1}{C_s} & -\omega_r & 0 \end{bmatrix} \quad (6)$$

$$B = \begin{bmatrix} 0 & \frac{M}{\Delta} & 0 & 0 & 0 & \frac{L_p}{\Delta} & 0 & 0 \end{bmatrix}^T \quad (7)$$

$$C = [0 \ 0 \ 0 \ 0 \ 0 \ 1 \ 0 \ 0] \quad (8)$$

where  $R_p$  and  $R_s$  are the parasitic resistances of the primary and secondary coils, respectively.  $R_{ac} = 8U_g / (\pi^2 I_g^*)$  is the AC equivalent resistance of the load.  $\Delta = M^2 - L_p L_s$ .  $\omega_r$  is the resonant angular frequency.

Next, the model of the single-phase MC is derived as

$$U_{cd} = \frac{2\sqrt{2}}{\pi} |u_c| \sin\left(\frac{\pi}{2} d_m\right) \quad (9)$$

Then, the state equations of the grid filter are derived as

$$\begin{cases} \frac{di_g}{dt} = \frac{1}{L_g} u_c - \frac{1}{L_g} e_g - \frac{R_g}{L_g} i_g \\ \frac{du_c}{dt} = \frac{1}{C_g} i_m - \frac{1}{C_g} i_g \end{cases} \quad (10)$$

From (3), (5), (9) and (10), the large-signal model is built, as shown in Fig. 3(a). Since  $|u_c| \times \text{sgn}(u_c) = u_c$ , the links of the absolute value function (ABS) and  $\text{sgn}(u_c)$  are canceled. Due to the nonlinearization of  $t_1 = (2\sqrt{2}/\pi) I_s \sin(\pi d_m / 2)$  and  $t_2 = (2\sqrt{2}/\pi) u_c \sin(\pi d_m / 2)$ , this large-signal model is nonlinear and difficult to analyze. To linearize the state equations, the small-signal perturbations are introduced as

$$\begin{cases} \hat{i}_1 = \left[ \frac{2\sqrt{2}}{\pi} \sin\left(\frac{\pi}{2} D_{m,r}\right) \hat{I}_s + \sqrt{2} I_{s,r} \cos\left(\frac{\pi}{2} D_{m,r}\right) \hat{d}_m \right] \\ \hat{i}_2 = \left[ \frac{2\sqrt{2}}{\pi} \sin\left(\frac{\pi}{2} D_{m,r}\right) \hat{u}_c + \sqrt{2} U_{c,r} \cos\left(\frac{\pi}{2} D_{m,r}\right) \hat{d}_m \right] \end{cases} \quad (11)$$

where  $\hat{i}_1$ ,  $\hat{i}_2$ ,  $\hat{d}_m$ ,  $\hat{u}_c$ , and  $\hat{I}_s$  are the small-signal perturbations of  $t_1$ ,  $t_2$ ,  $d_m$ ,  $u_c$ , and  $I_s$ , respectively.  $D_{m,r}$ ,  $U_{c,r}$ , and  $I_{s,r}$  are the steady-state values of  $d_m$ ,  $u_c$ , and  $I_s$ , respectively.

According to (5), (10), and (11), the small-signal model is derived as Fig. 3(b). The perturbation of the grid current is further derived as

$$\begin{cases} \hat{i}_g = G_{dm-ig} \hat{d}_m + G_{eg-ig} \hat{e}_g \\ G_{dm-ig} = \frac{\hat{i}_g}{\hat{d}_m} = \frac{\sqrt{2} I_{s,r} \cos(\pi D_{m,r} / 2) + 2U_{c,r} \sin(\pi D_{m,r}) G_{U_{cd}-I_s} / \pi}{1 + (L_g s + R_g) [C_g s + 8 \sin^2(\pi D_{m,r} / 2) G_{U_{cd}-I_s} / \pi^2]} \\ G_{eg-ig} = \frac{\hat{i}_g}{\hat{e}_g} = -\frac{C_g s + 8 \sin^2(\pi D_{m,r} / 2) G_{U_{cd}-I_s} / \pi^2}{1 + (L_g s + R_g) [C_g s + 8 \sin^2(\pi D_{m,r} / 2) G_{U_{cd}-I_s} / \pi^2]} \end{cases} \quad (12)$$

where  $G_{dm-ig}$  and  $G_{eg-ig}$  are the transfer functions from  $d_m$  and  $e_g$  to  $i_g$ , respectively.

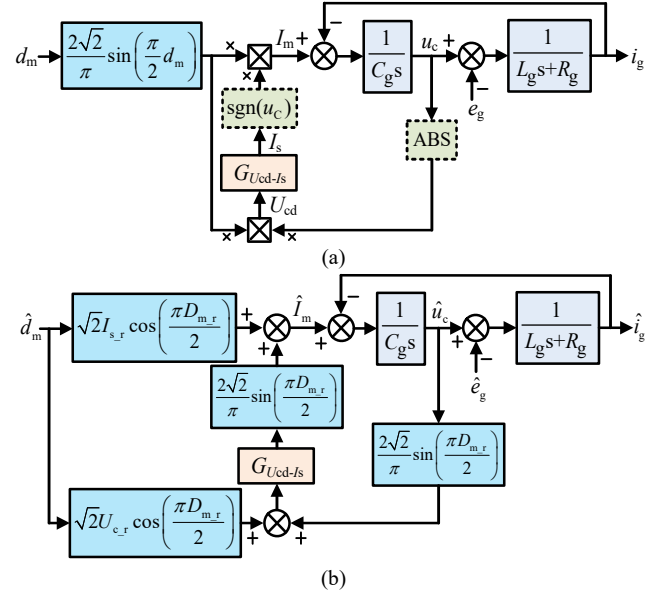


Fig. 3. State space model of MC-based WPT system. (a) Large-signal model. (b) Small-signal model.

Based on the built model, the effect of the grid voltage harmonics on the grid current can be analyzed. The grid voltage with the  $2i+1$ th order harmonics is expressed as

$$e_g = U_{g1} \sin(\omega_1 t) + U_{g3} \sin(\omega_3 t) + \dots + U_{g(2i+1)} \sin(\omega_{2i+1} t) \quad (13)$$

According to (12) and (13), the harmonic components of the grid current are derived as

$$i_{g(2i+1)} = |G_{eg-ig}(j\omega_{2i+1})| U_{g(2i+1)} \sin(\omega_{2i+1} t + \angle G_{eg-ig}(j\omega_{2i+1})) \quad (14)$$

Based on (14), the effect of the grid voltage harmonics on the grid current harmonics is shown in Fig. 4.

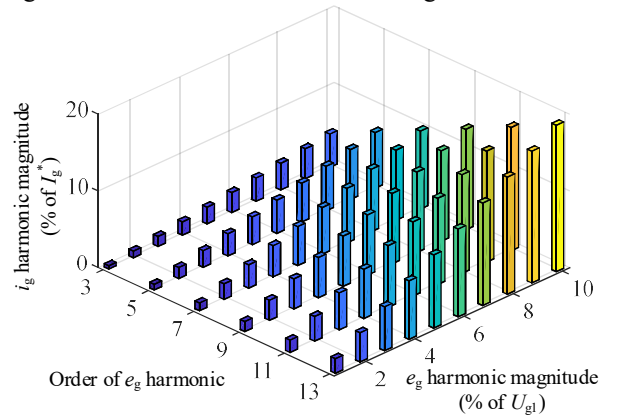


Fig. 4. Effect of grid voltage harmonics on grid current.

As the harmonic order and amplitude of the grid voltage increase, the corresponding harmonic amplitudes in the grid current rise accordingly. For instance, when the grid voltage contains 10% 3rd, 5% 5th, and 5% 7th harmonics, the resulting grid current exhibits 4.34% 3rd, 3.62% 5th, and 5.08% 7th harmonics, respectively. The total harmonic distortion (THD) of the grid current reaches 7.6%. This exceeds the acceptable limits defined by grid standards and thus limits the applicability of the MC-based WPT system in V2G scenarios.

#### IV. PROPOSED CLOSED-LOOP CONTROL STRATEGY

To improve grid current quality under distorted grid voltage conditions, an advanced control strategy must be employed. The conventional PI and PR controllers exhibit limited gains at harmonic frequency points [25], and thus cannot suppress harmonics. In this section, a dual-aspect optimization approach focusing on both control architecture and closed-loop controller is researched. On this basis, the controller parameters are designed to ensure system stability and enhanced harmonic suppression capability.

##### A. Configuration of Closed-Loop Control System

The block diagram of the proposed closed-loop control strategy is shown in Fig. 5. Since the bandwidth of the closed-loop control system is much lower than the control frequency, the control delay is neglected to facilitate the analysis. The proposed control strategy consists of two parts, namely the feedforward control (FC) of the grid voltage and the closed-loop control of grid current.

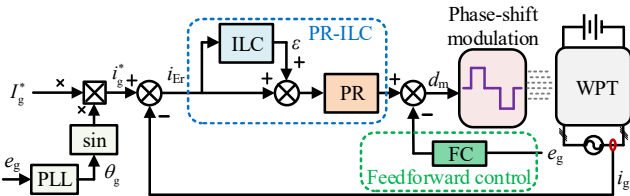


Fig. 5. Block diagram of proposed closed-loop control strategy.

First, the feedforward control of the grid voltage is used to mitigate the adverse impact of voltage harmonics on the grid current. According to Fig. 3(b) and (12), the harmonic components of grid voltage act as the system disturbances, as shown in Fig. 6(a). From Fig. 15(a), the grid voltage harmonics affect this system via the transfer path  $G_{eg-ig}$ . Therefore, a feedforward branch of  $-G_{eg-ig}$  can be added to counteract the disturbance. According to the classical control theory, the resulting block diagram is equivalent to Fig. 6(b), effectively achieving harmonic disturbance rejection through feedforward compensation. The detailed structure and parameter design method of the feedforward controller are presented in Section IV-B.

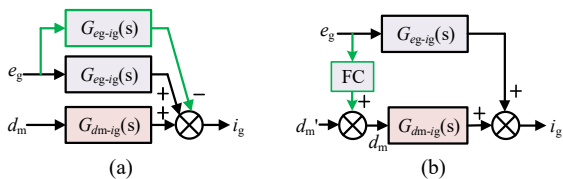


Fig. 6. Block diagram of feedforward control. (a) Principal diagram. (b) Equivalent diagram.

Since feedforward control is inherently open-loop and

lacks robustness, it is complemented by a closed-loop grid current controller. To achieve the zero steady-state error tracking and further suppress grid current distortion, a proportional-resonant composite iterative learning controller (PR-ILC) is proposed as shown in Fig. 5.

The PR controller is used to realize the zero steady-state error control of the fundamental current. The discrete domain expression of the PR controller is expressed as

$$G_{PR}(z) = K_p + \frac{4K_r T_{sa} \omega_c (z^2 - 1)}{4(z-1)^2 + 4\omega_c T_{sa} (z^2 - 1) + \omega_c^2 T_{sa}^2 (z+1)^2} \quad (15)$$

where  $K_p$  and  $K_r$  denote the proportional and resonant gains, respectively.  $\omega_c$  is the resonant cutoff frequency.  $T_{sa}$  is the sample period. They will be designed in Section IV-C.

The ILC can effectively suppress periodic disturbance signals. Therefore, building upon the PR controller framework, the ILC is embedded to achieve grid current suppression. The output of the ILC is used as the compensation value, and it is summed with the grid current error as the input of the PR controller. The block diagram of the ILC is shown in Fig. 7. The iterative learning law and discrete-domain transfer function are expressed as

$$\varepsilon(k+1) = (1-\Lambda)\varepsilon(k) + \gamma i_{Er}(k) + \zeta i_{Er}(k+1) \quad (16)$$

$$G_{ILC}(z) = \frac{\zeta z + \gamma}{z-1+\Lambda} \quad (17)$$

where  $\Lambda$  is the forgetting factor, and  $\gamma$  and  $\zeta$  are the iterative learning gains. They are greater than zero and less than one.  $i_{Er}$  is the current deviation.  $\varepsilon$  is the ILC output.

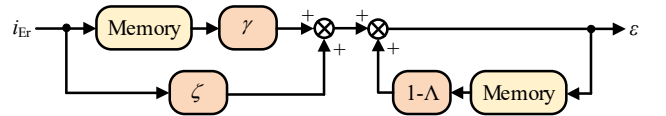


Fig. 7. Block diagram of ILC.

Through the combination of feedforward compensation and PR-ILC-based closed-loop control, the proposed strategy achieves dual-stage suppression of grid current harmonics, ensuring robust and precise current regulation under distorted grid conditions.

##### B. Design of Feedforward Controller

In this section, the structure and parameter design of the feedforward controller are presented.

From Fig. 6, the transfer function of the feedforward controller is derived as

$$G_{FC}(s) = -\frac{G_{eg-ig}(s)}{G_{dm-ig}(s)} \quad (18)$$

where  $G_{dm-ig}$  and  $G_{eg-ig}$  are derived from (12). By substituting (12) into (18),  $G_{FC}$  is derived as

$$G_{FC}(s) = \frac{C_g s + 8 \sin^2(\pi D_{m,r}/2) G_{Ucd-Is}(s) / \pi^2}{\sqrt{2} I_{s,r} \cos(\pi D_{m,r}/2) + 2U_{c,r} \sin(\pi D_{m,r}) G_{Ucd-Is}(s) / \pi} \quad (19)$$

As derived in (19), the order of the feedforward controller  $G_{FC}$  is eight. Such a high-order controller is challenging to implement on a digital signal processor (DSP) due to computational and real-time constraints. For practical engineering implementation,  $G_{FC}$  should be simplified.

Considering that the dominant harmonic components in the grid voltage are typically the 3rd, 5th, ..., and 25th-order

harmonics, the simplified controller should retain similar frequency-domain characteristics at these specific harmonic frequencies. According to (18), the Bode diagram of the original  $G_{FC}$  is shown in Fig. 8. The magnitude slope of  $G_{FC}$  is approximately 20 dB/dec, and its phase remains close to  $90^\circ$  within the frequency range of 150 Hz to 1250 Hz. These frequency-domain characteristics indicate that  $G_{FC}$  behaves similarly to an integrator over the harmonic frequency range. To facilitate practical implementation, the feedforward controller is simplified as an integrator. Its discrete-domain transfer function is given as

$$G'_{FC}(z) = \frac{2K_{FC}(z-1)}{T_{sa}(z+1)} \quad (20)$$

where  $K_{FC}$  is the gain of the simplified feedforward controller. Since the magnitude of  $G_{FC}$  at 150Hz is  $-42.2\text{dB}$ ,  $K_{FC}$  is derived as  $8.24 \times 10^{-6}$ .

The Bode diagram of the simplified feedforward controller is also shown in Fig. 8. Its magnitude and phase responses closely match those of  $G_{FC}$  at the dominant harmonic frequencies of the grid voltage. As a result, the simplified controller retains the desired harmonic rejection capability while being suitable for DSP implementation, thereby offering enhanced practicality.

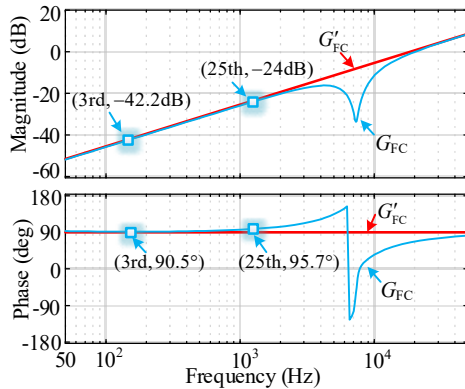


Fig. 8. Bode diagrams of  $G_{FC}$  and  $G'_{FC}$ .

### C. Performance Analysis and Parameter Design of PR-ILC

In this section, the stability and harmonic suppression characteristics of the proposed closed-loop control system based on the PR-ILC are analyzed. On this basis, the parameter design of the PR-ILC is presented.

First, the stability of the proposed closed-loop control system is analyzed. As the feedforward control branch does not affect system stability, it is excluded from the analysis. According to Fig. 7, the structure of the closed-loop control system is shown in Fig. 9.

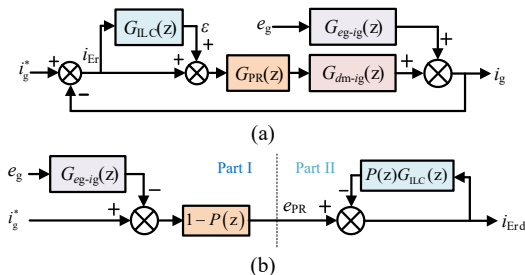


Fig. 9. Block diagram of proposed closed-loop control strategy. (a) Control structure. (b) Equivalent structure.

From Fig. 9(a), the stability is co-determined by the PR controller and ILC. The dynamic coupling between these two controllers complicates their independent parameter tuning. To facilitate the design of the composite controller, the current tracking error  $i_{Er}$  is analyzed in detail.

According to Fig. 9(a),  $i_{Er}$  is derived as

$$i_{Er}(z) = \frac{i_g^*(z) - G_{eg-ig}(z)e_g(z)}{1 + [1 + G_{ILC}(z)]G_{PR}(z)G_{dm-ig}(z)} \quad (21)$$

The transfer function of the closed-loop control system with only the PR controller is expressed as

$$P(z) = \frac{G_{PR}(z)G_{dm-ig}(z)}{1 + G_{PR}(z)G_{dm-ig}(z)} \quad (22)$$

According to (21) and (22), the current control error is derived as

$$i_{Er}(z) = \frac{[1 - P(z)][i_g^*(z) - G_{eg-ig}(z)e_g(z)]}{1 + P(z)G_{ILC}(z)} \quad (23)$$

Based on (23), the control block diagram is equivalently represented in the equivalent configuration depicted in Fig. 9(b). The overall system stability depends on two subsystems, and the system is stable only if both satisfy their respective stability criteria. According to classical control theory, these criteria are defined as follows:

*Part I:* The closed-loop transfer function  $P(z)$ , considering only the PR controller, must be stable. Specifically, the phase margin of  $G_{PR}(z)G_{dm-ig}(z)$  should exceed  $45^\circ$ .

*Part II:* The phase margin of  $G_{ILC}(z)P(z)$  should exceed  $45^\circ$ .

From these criteria, the design of the PR controller parameters is independent of the ILC, whereas the ILC design depends on the PR controller. Therefore, the PR controller parameters are designed first to ensure the stability of Part I. Then,  $P(z)$  is obtained and used as the plant for the ILC parameter design. Based on these theories, the controller parameters will be designed.

From Fig. 9(a), the controlled objective is  $G_{dm-ig}(z)$ . Based on the built model in Section II, the transfer function  $G_{dm-ig}(s)$  in the continuous domain is obtained from (12). However, the proposed control strategy is implemented on a DSP platform, and it operates in the discrete-time domain. To bridge this domain gap, the Tustin transformation is adopted, and the transfer function of the control object in the discrete domain is obtained as

$$G_{dm-ig}(z) = G_{dm-ig}(s) \Big|_{s=\frac{2}{T_s} \frac{1-z^{-1}}{1+z^{-1}}} = \frac{\sqrt{2}I_{s,r} \cos(\pi D_{m,r}/2) + 2U_{c,r} \sin(\pi D_{m,r}) G_{Ucd-fs}(z) / \pi}{1 + \left[ \frac{2(z-1)L_g}{T_{sa}(z+1)} + R_g \right] \left[ \frac{2(z-1)C_g}{T_{sa}(z+1)} + \frac{8G_{Ucd-fs}(z)}{\pi^2} \sin^2\left(\frac{\pi}{2} D_{m,r}\right) \right]} \quad (24)$$

Based on (24), the PR controller parameters are designed first. According to the classical control theory, the desired phase margin is greater than  $45^\circ$  to ensure stability. The desired cutoff frequency is set to 500 Hz, which is one-tenth of the grid-side LC filter's cutoff frequency, to avoid oscillations caused by the filter. Additionally, the magnitude gain at 50 Hz should exceed 40 dB to achieve the zero steady-state error tracking for the desired fundamental grid current. Accordingly, the parameters of the PR controller in



(15) are designed as  $K_p=0.2$ ,  $K_r=43$ ,  $\omega_c=2$  rad/s, and  $\omega_1=100\pi$  rad/s. Fig. 10 shows the Nyquist curve of  $G_{PR}(z)G_{dm-ig}(z)$ . The phase margin is approximately  $48^\circ$ , confirming the stability of the closed-loop system with only the PR controller.

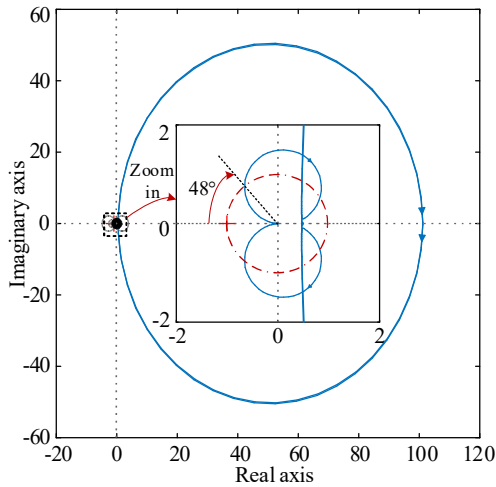


Fig. 10. Nyquist curve of  $G_{PR}(z)G_{dm-ig}(z)$  when only considering PR controller.

After designing the PR controller parameters, the closed-loop transfer function  $P(z)$ , considering only the PR controller, is obtained from (22), and the ILC parameters are then designed. Prior to the design, the effects of the ILC parameters on system stability and harmonic suppression capability are analyzed.

According to the stability criteria of Part II, the Nyquist curves of  $G_{ILC}(z)P(z)$  under different ILC parameters are plotted in Fig. 11. In Fig. 11(a),  $\Lambda$  is constant, and  $\gamma$  and  $\zeta$  vary from 0.3 to 0.9. As  $\gamma$  and  $\zeta$  increase, the magnitude of  $G_{ILC}(z)P(z)$  grows larger, pushing the system toward instability. When  $\gamma$  and  $\zeta$  exceed 0.7, the Nyquist curve encircles the  $(-1, j0)$  point in the complex plane, resulting in an unstable closed-loop control system. In Fig. 11(b),  $\gamma$  and  $\zeta$  are constant, and  $\Lambda$  varies from 0.1 to 0.9. Within the variation range of  $\Lambda$ , the Nyquist curve does not encircle the  $(-1, j0)$  point in the complex plane. This indicates that  $\Lambda$  has minimal effect on system stability.

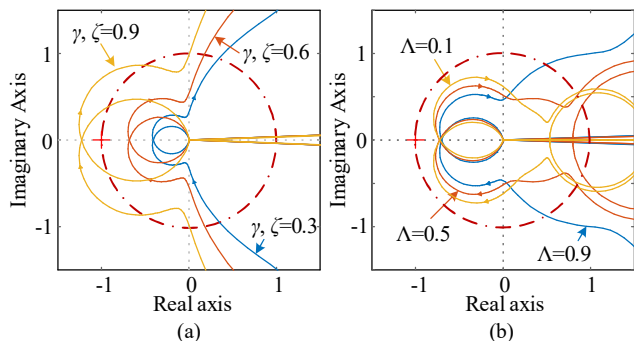


Fig. 11. Nyquist curve of  $G_{ILC}(z)P(z)$  under different ILC parameters. (a) Variation with  $\gamma$  and  $\zeta$ . (b) Variation with  $\Lambda$ .

Subsequently, the effect of the ILC parameters on harmonic suppression capability is researched. To characterize this capability, the transfer function  $G_{eg-iEr}$ ,

representing the relationship between grid voltage and current control error, is analyzed. From (21),  $G_{eg-iEr}$  is derived as

$$G_{eg-iEr}(z) = \frac{i_{Er}(z)}{e_g(z)} = \frac{-G_{eg-ig}(z)}{1 + [1 + G_{ILC}(z)]G_{PR}(z)G_{dm-ig}(z)} \quad (25)$$

Based on (25), the amplitude-frequency characteristic of  $G_{eg-iEr}$  under different ILC parameters is shown in Fig. 12. The control error decreases with increasing  $\gamma$  and  $\zeta$ , and with decreasing  $\Lambda$ . This indicates that the harmonic suppression capability of the PR-ILC improves as  $\gamma$  and  $\zeta$  increase and  $\Lambda$  decreases. To achieve a trade-off between system stability and harmonic suppression performance, the optimized parameters are selected as  $\gamma=\zeta=0.6$  and  $\Lambda=0.1$ .

Compared to the standalone PR controller, the proposed PR-ILC significantly reduces the current control error at harmonic frequencies, thereby validating its enhanced harmonic suppression performance.

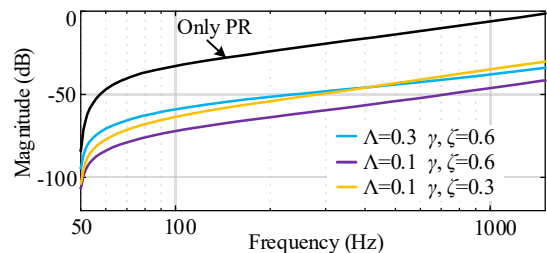


Fig. 12. Amplitude-frequency characteristic of current control error under proposed PR-ILC with different parameters.

The relationship between key controller parameters ( $K_p$ ,  $K_r$ ,  $\gamma$ ,  $\zeta$ ,  $\Lambda$ ) and system performance in terms of phase margin and grid current THD is shown in Table I. The table enables readers to quickly observe how changes in controller parameters influence the system's phase margin and harmonic suppression.

TABLE I RELATIONSHIP BETWEEN CONTROLLER PARAMETER AND SYSTEM PERFORMANCE

$K_p$	$K_r$	$\gamma$	$\zeta$	$\Lambda$	Phase margin	THD
0.2	43	0.9	0.9	0.1	$-8^\circ$	/
		0.6	0.6	0.1	$32^\circ$	1.24%
		0.3	0.3	0.1	$42^\circ$	2.55%
		0.6	0.6	0.3	$39^\circ$	2.31%
		0.6	0.6	0.5	$47^\circ$	3.26%

## V. EXPERIMENTAL VERIFICATION

To validate the effectiveness of the proposed closed-loop control strategy, an experimental platform for the MC-based WPT system is established, as shown in Fig. 13. A programmable AC source is employed to emulate the grid, while a DC power supply simulates the EV battery. The digital controller is implemented on a TMS320F28377S DSP. The key experimental parameters are summarized in Table II. To replicate the grid harmonic conditions, the AC source is configured to inject harmonic components into the grid voltage. Specifically, 10% 3rd, 5% 5th, and 5% 7th harmonics are introduced into the output voltage, thereby enabling the evaluation of harmonic suppression performance under distorted grid scenarios.

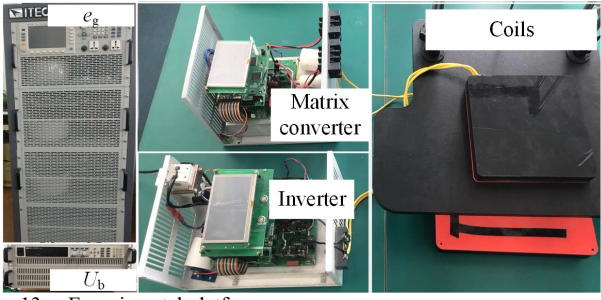


Fig. 13. Experimental platform.

TABLE II MAIN EXPERIMENTAL PARAMETERS

Parameter	Value	Parameter	Value
Output power	500 W	$M$	23 $\mu\text{H}$
Grid fundamental voltage	110 V <sub>RMS</sub>	$C_p$	20.6 nF
Input DC voltage $U_b$	100 V	$C_s$	35 nF
$f_s$	85 kHz	$L_g$	50 $\mu\text{H}$
$L_p$	170 $\mu\text{H}$	$C_g$	20 $\mu\text{F}$
$L_s$	100 $\mu\text{H}$		

The experimental waveforms under the PR controller alone are shown in Fig. 14. Fig. 14(a) and (b) show the steady-state waveforms and fast Fourier transform (FFT) results under the ideal grid voltage condition. The grid current amplitude reaches 6.5 A, with a THD of 1.08%. These results demonstrate accurate zero steady-state error tracking of the fundamental component and validate the successful realization of V2G operation in the MC-based WPT system.

Fig. 14(c) and (d) show the experimental results under distorted grid voltage conditions. The significant distortion is observed in the grid current, primarily comprising 4.4% 3rd-harmonic, 3.6% 5th-harmonic, and 3.8% 7th-harmonic components. The dominant harmonic orders directly correspond to the injected grid voltage harmonics. The THD of the grid current reaches 7.16%, exceeding the IEEE 519-2014 limit of 5%. These results indicate that although the conventional PR controller ensures zero steady-state error tracking of the fundamental current, it cannot suppress harmonic distortions caused by non-ideal grid conditions.

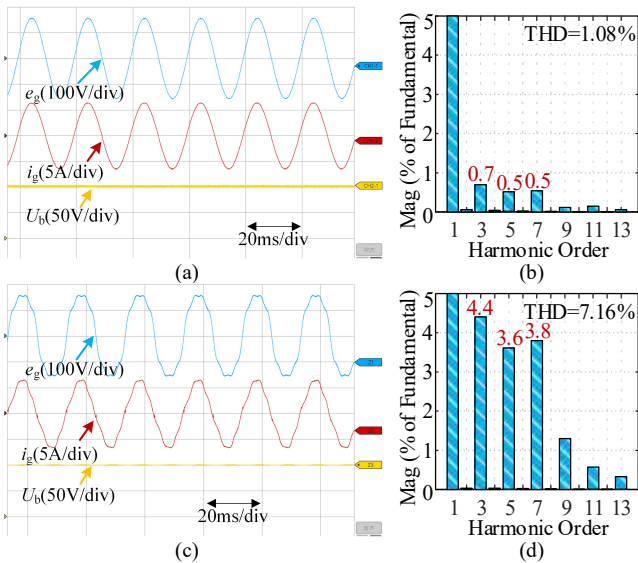


Fig. 14. Steady-state experimental waveforms under PR controller alone. (a) Waveforms under ideal grid voltage. (b) FFT of grid current under ideal grid voltage. (c) Waveforms under grid voltage harmonics. (d) FFT of grid current under grid voltage harmonics.

Fig. 15 shows the experimental waveforms of the proposed control strategy applied to the MC-based WPT system. Fig. 15(a) and (b) present the experimental results under the proposed composite control architecture integrating feedforward control + PR control without ILC. The grid current distortion is significantly mitigated, with the 3rd, 5th, and 7th harmonic components reduced to 1.7%, 0.9%, and 2.2%, respectively. As a result, the grid current THD decreases to 3.52%. Although the feedforward control effectively compensates for grid voltage harmonics, it cannot fully suppress them in practical systems. The discretization errors at lower sampling frequencies and parameter mismatches, such as coil displacement affecting inductance, further limit the feedforward control performance in harmonic compensation.

Fig. 15(c) and (d) show the results under the PR-ILC-based closed-loop controller alone, without feedforward compensation. The 3rd, 5th, and 7th harmonic components are 1.8%, 1.2%, and 1.1%, respectively, and the THD is 2.61%. These results confirm that both the feedforward control architecture and the PR-ILC independently provide effective harmonic suppression, each ensuring compliance with grid standards.

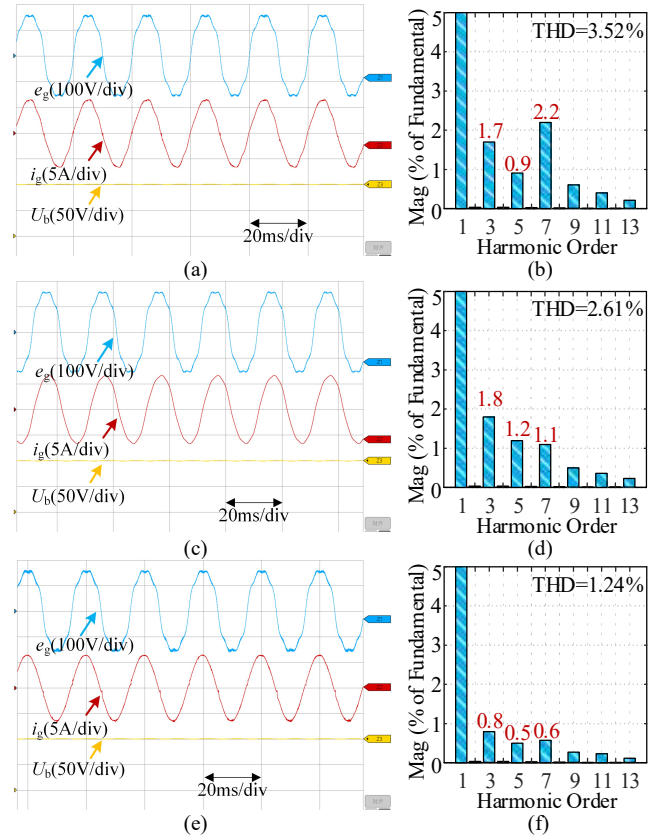


Fig. 15. Steady-state experimental waveforms under proposed control strategy. (a) Waveforms under feedforward control + PR control without ILC. (b) FFT of grid current under feedforward control + PR control without ILC. (c) Waveforms under PR-ILC. (d) FFT of grid current under PR-ILC. (e) Waveforms under feedforward control + PR-ILC. (f) FFT of grid current under feedforward control + PR-ILC.

Further, the proposed feedforward control architecture and PR-ILC-based closed-loop controller are both implemented, with corresponding waveforms depicted in Fig. 15(e) and (f). The 3rd, 5th, and 7th harmonic components are further reduced to 0.8%, 0.5%, and 0.6%,

and the THD is significantly reduced to 1.24%. This synergistic control approach outperforms either method alone, achieving a substantial improvement in grid current quality. Compared to the conventional PR control, which yields a THD of 7.16%, the proposed strategy achieves a marked reduction to 1.24%, effectively eliminating the adverse impact of grid voltage harmonics on the grid current waveform.

To verify the transient performances of the MC-based WPT system under the proposed control strategy, the transient experimental waveforms under different operation conditions are shown in Fig. 16~Fig. 18. Fig. 16 shows the waveforms when the closed-loop reference of grid current amplitude steps from 4 A to 6.5 A and from 6.5 A to 4 A, respectively. The grid current exhibits fast and accurate tracking, with a transient response time of less than 2 ms. Fig. 17 shows the waveforms when the grid voltage amplitude steps up from 124 V to 155 V and down from 155 V to 124 V. Fig. 18 shows the waveforms when the input DC voltage  $U_b$  varies from 100 V to 120 V and from 120 V to 100 V. In both cases, the grid current maintains stable and highly sinusoidal waveforms. This validates that the proposed control strategy ensures the system stability and superior steady-state and transient performance across diverse operation conditions.

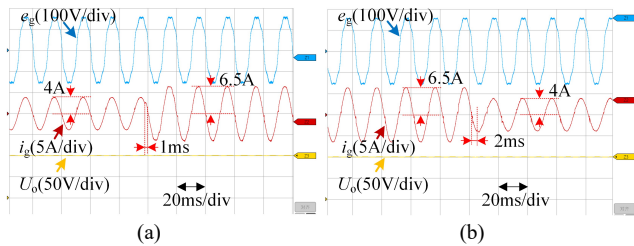


Fig. 16. Experimental waveforms with stepping of grid current reference. (a) From 4 A to 6.5 A. (b) From 6.5 A to 4 A.

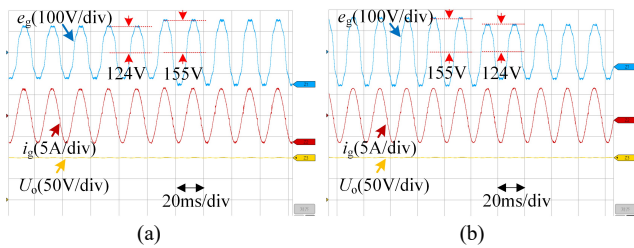


Fig. 17. Experimental waveforms with variation of grid voltage. (a) From 124 V to 155 V. (b) From 155 V to 124 V.

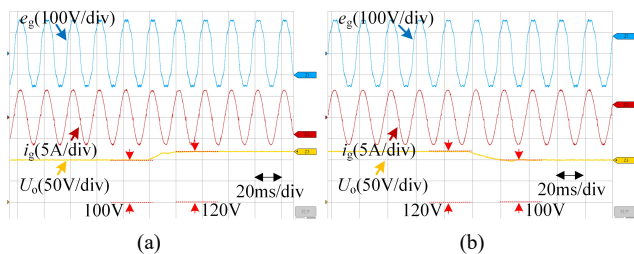


Fig. 18. Experimental waveforms with variation of input DC voltage. (a) From 100 V to 120 V. (b) From 120 V to 100 V.

To verify the ride-through capability under grid-fault conditions, the experimental waveforms under a 50% grid-voltage sag lasting 20 ms are shown in Fig. 19. The grid current remains smooth during the voltage sag, with virtually no oscillations, overshoot, or other undesirable

transient behaviors. These results confirm that the proposed FC + PR-ILC control strategy not only achieves effective harmonic suppression but also maintains stable operation and fast recovery under dynamic grid-voltage disturbances, thereby further validating its robustness and practical applicability.

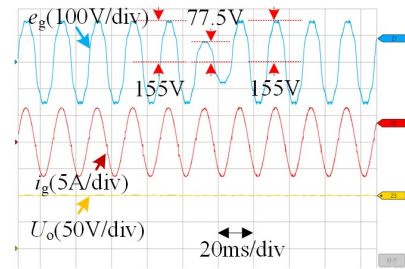


Fig. 19. Experimental waveforms under a 50% grid-voltage sag lasting 20 ms with proposed FC + PR-ILC control strategy.

Finally, the grid current THD under various load conditions is compared, as shown in Fig. 20. Across the entire load range, the proposed FC + PR-ILC control strategy consistently outperforms both the feedforward control and PR-ILC strategies when applied individually. All proposed methods exhibit better performance than the conventional PR control. Moreover, the proposed control strategy ensures that the grid current THD remains within the limits specified by grid standards under all tested load conditions, demonstrating its robustness and effectiveness in maintaining power quality across varying operational scenarios.

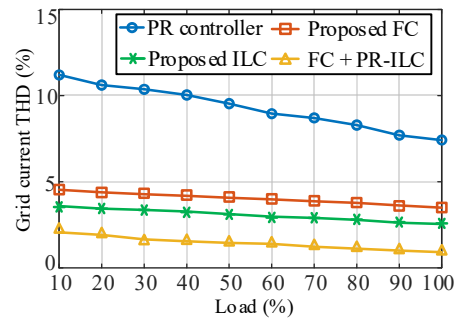


Fig. 20. Comparison of grid current THD under different load conditions.

The experimental results under variations in mutual inductance, self-inductance, and resonant capacitance are shown in Fig. 21. The FC + PR control strategy exhibits a significant increase in THD with parameter changes. However, the PR-ILC effectively compensates for these variations, ensuring robust harmonic suppression. With the FC + PR-ILC strategy, THD consistently remains below 2%, outperforming the other strategies. This confirms that effective harmonic suppression is maintained even under substantial parameter variations and coil misalignment, demonstrating the strong robustness and generalization capability of the proposed FC + PR-ILC control strategy.

Table III provides a comparison across different methods. The proposed FC + PR-ILC strategy achieves the lowest grid current THD while keeping DSP and memory costs at a moderate level, demonstrating trade-offs of performance and complexity.



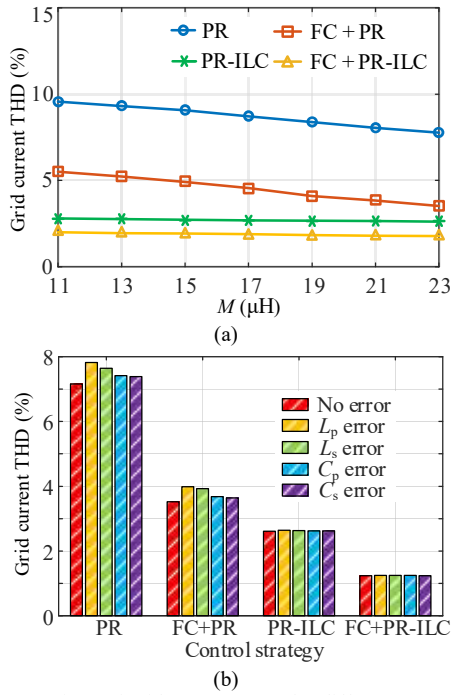


Fig. 21. Comparison of grid current THD for different control strategies under parameter variations. (a) Mutual inductance variation. (b) Inductance and capacitance variation.

TABLE III COMPARISON OF COMPUTATIONAL BURDEN AND PERFORMANCE OF DIFFERENT CONTROL STRATEGIES

Control strategies	Current THD	Transient time	DSP computational time	Memory footprint
Conventional PR [25]	7.16%	<10 ms	115 ns	28 Bytes
Multiple PR [24]	2.89%	<10 ms	575 ns	140 Bytes
FC + PR	3.52%	<10 ms	160 ns	44 Bytes
PR-ILC	2.61%	<10 ms	170 ns	52 Bytes
FC + PR-ILC	1.24%	<10 ms	215 ns	68 Bytes

## VI. CONCLUSION

In this paper, the linearized model of the MC-based WPT system under V2G operation is built, revealing that the grid voltage harmonics induce severe current distortion. On this basis, a composite control architecture integrating grid voltage feedforward compensation and grid current closed-loop regulation is proposed. Furthermore, a PR-ILC-based closed-loop controller is developed to enhance harmonic suppression performance. Both strategies demonstrate effective harmonic mitigation individually, while their synergistic integration achieves a substantial reduction in grid current THD. The proposed method is crucial for the practical V2G application of the MC-based WPT system. It also offers broad applicability to other V2G-enabled systems requiring high power quality.

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