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ORIGINAL RESEARCH

DC‐side stability analysis of grid‐tied converter with different control modes based on electrical torque analysis

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Abstract

The DC‐side stability of the grid‐tied converter under different control modes is fully investigated using electrical torque analysis. The small‐signal model of a single converter connected to an ideal DC bus under various control modes is formulated. Accordingly, the damping and synchronising coefficient contributed by the DC network and controllers of grid‐tied converter are separately accessed using the electrical torque analysis method and the stabilising conditions of the grid‐tied converter operating under different control modes are further derived. The system stability mainly corresponds with DC network dynamics under constant active power control mode. On the contrary, the gridtied converter under constant DC‐link voltage control mode has no stability problem. Generally, elevating the DC‐link capacitance or decreasing the droop gain can greatly improve the stability margin reserve of the VSC‐HVDC links. In addition, the control gains of the classical PQ controller are proven to have limited impacts on DC‐side system stability. Finally, the results of numerical simulation prove the validity of the proposed stability analysis method and the stable boundary for the grid‐tied converter with different control modes.

KEYW ORDS

power convertors, power grids, power system stability

1 | **INTRODUCTION**

With the large-scale construction and operation of renewable energy power stations, the long‐distance bulk power transmission via converter‐based HVDC systems gradually become popular in modern power grids [[1](#page-12-0)]. The widespread application of voltage source converters (VSCs) brings convenience to the operation of HVDC systems [\[2\]](#page-12-0) but simultaneously generates new challenges to the stability analysis. Many researchers focus on analysing the impacts of grid‐tied converters on AC‐side stability. For example, it is indicated that the system stability can be influenced by the interaction between phase locked loops' (PLLs') control dynamics, network dynamics and converter operating conditions [\[3\]](#page-12-0), and inappropriately selected parameters of DC‐link voltage control may lead to the different frequency oscillations of AC-side systems [[4\]](#page-12-0).

However, only a few articles provide analytical stabilising conditions for VSC‐HVDC systems.

1.1 | **Related works**

When investigating the stability problem of modern power system, modal analysis is one of the classical and useful methods, which examines the system stability by calculating all the eigenvalues of the system state matrix: The studied system is always under a stable operation if and only if the real part of all calculated eigenvalues is negative. Based on modal analysis, some useful guidance can be given to the system operators by examining the overall system stability based on several critical system operating points [\[5](#page-12-0)]. In addition, some other methods derived from modal analysis, such as the gain array (RGA), are

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applied in analysing the power system stability [\[6\]](#page-12-0). The dynamic model of the MMC‐HVDC system is proposed in Ref. [\[7](#page-12-0)]. Based on modal analysis, the system dominant oscillatory modes can be identified, and their propensity to instability is quantified. Moreover, severe oscillations might be introduced to the HVDC system when DC-link voltage control parameters are selected improperly [\[8,](#page-13-0) 9]. Furthermore, the control effects of DC current flow controllers (CFCs) are analysed using Nyquist stability analysis and Bode diagram [[10,](#page-13-0) 11], and the results indicate that the CFC integration has more impact on the DC‐side dynamics of the multi‐terminal DC (MTDC) links rather than on the AC‐side dynamics of the MTDC system. In addition, the contribution of the DC network and DC-link control dynamics to the overall system stability is accessed in Ref. [\[12\]](#page-13-0) based on net‐damping criterion. However, modal analysis not only needs to acquire the detailed parameters of the whole system, but also relies on the complex computations, which fails to explain the instability mechanism of the overall system and map the stability boundary from the physical interpretation.

In order to overcome the shortcomings of the above analysis method, impedance‐based analysis (IMA) is proposed and gradually become the mainstream stability analysis method [\[13\]](#page-13-0). To apply IMA in studying the modern power system stability, the research object should be divided into two series‐ connected sub‐systems. The overall system will remain stable if the ratio between the impedances of the sub‐systems satisfies the Nyquist stability criterion. The impedance expressions for the gird-connected converter are well derived in Refs. [[14–16\]](#page-13-0). Resonance stability in HVDC systems is fully investigated in Refs. [[14,](#page-13-0) 17], and it is indicated that power transmission distance, installed capacity of renewable energy and converter controller dynamics have a profound impact on DC‐side resonances. In Ref. [\[18\]](#page-13-0), it reveals that the stability problem might be introduced to HVDC systems with the alternation of power flow direction. In addition, a geometrical approach is proposed based on the conventional IMA method to well evaluate the small-signal synchronisation stability of grid-forming converter in Ref. [[19](#page-13-0)]. Although the system stability can be judged only with the information of impedance using the IMA method, the proper separation of two sub‐systems limits its application in complex multi-terminal HVDC systems. Furthermore, it still fails to provide analytical and concise stabilising conditions via this method.

Recently, some other novel methods are proposed for system stability assessment. Based on the graph theory, the stability of converter-tied DC system considering system structure and parameter uncertainties can be well studied, however, due to the ignorance of the interactions of the subsystems, the proposed stability criterion may be rather con-servative [\[20\]](#page-13-0). Furthermore, [[21](#page-13-0)] proposes a stage-by-stage stability analysis method to identify which converters are mainly responsible for the overall instability, though it requires the whole system to be separated into multiple stages and fails to provide analytical stabilising conditions. The Routh Judgement applied in small‐signal stability in Refs. [\[22,](#page-13-0) 23] assess the DC-side stability using dominant frequency analysis. Both of

them find the critical variables, which have a significant impact on the stable operation of VSC‐HVDC links. But only low‐ order power system stability can be well studied through these methods, and it is unable to provide an analytical criterion for the complex high‐order system.

1.2 | **Main contributions**

Most existing methods are based on modal analysis or Nyquist mapping to analyse the system stability. It is difficult to obtain concise stabilising conditions using these methods, which rely on complicated mathematical modelling, complex numerical calculations and drawing quantities of the Nyquist curve. Hence, the electrical torque analysis method [\[24\]](#page-13-0) is utilised to derive the analytical stabilising conditions for VSC‐HVDC links, which provide simple and efficient small‐signal stability assessment for high‐order power systems without complex mathematical modelling, complicated numerical calculations and Nyquist plots. In this study, the dynamics of the grid‐tied converter are firstly investigated, and the small‐signal model of a single converter connected to an ideal DC bus via DC cables is formulated. Based on electrical torque analysis, the damping and synchronising coefficients contributed by DC network dynamics and VSC controllers' dynamics can be well evaluated separately. Hence, the DC-side stability of the VSC-HVDC system under different control modes can be well investigated as per the derived damping coefficient. The system stability is mainly related to the DC network dynamics under constant active power control mode, while the studied system has no stability problem under constant DC-link voltage control mode. In addition, the concise and analytical tuning rules of the power‐voltage droop controller and the reactive power compensator are further proposed to improve the system stability margin, which may provide useful guidance for the control design and equipment selection.

2 | **MODELLING OF GRID‐TIED CONVERTER**

As illustrated by Figure [1](#page-2-0), the VSC‐HVDC system to be investigated in this study consists of a two-level VSC connected to an ideal DC bus via DC cables. The DC cables adopt the classical *RL* model, which is sufficiently accurate for the small-signal stability analysis since the DC-side electrical oscillation frequencies do not exceed the feasible frequency band of the *RL* line model [[11,](#page-13-0) 25]. *Uac* is the voltage of the converter-tied AC system. U^r_{dc} denotes the ideal DC bus voltage, and U_{dc}^{s} represents the DC voltage at the terminal of grid-tied converter. I_{dc} is the current flowing through the DC cables, and the transmitted active power in the overall system is noted as P_t .

Furthermore, it is interesting to mention that, due to its less switch frequency, less power losses and higher reliability, more engineers prefer to choose the modular multilevel converters (MMCs). However, except that the electrical parameters of two types of converters are different, the topology of two‐ level VSC is almost the same as the standard average value models of MMC [22, [23,](#page-13-0) 26]. Therefore, in order not to lose the generality, the two‐level VSC is selected as the research object for the appropriate analytical model formulation and small‐signal stability analysis.

2.1 | **Network dynamics of grid‐tied converter**

The topology of the grid-tied converter is illustrated by Figure 2. I_{ac} represents the current flowing through the AC transmission line. U_c denotes the AC voltage at the terminal of grid-tied converter. R_{ac} and L_{ac} are the equivalent resistance and inductance of the converter-tied AC system, respectively. In addition, the grid-tied converter usually operates under the unit power factor control mode to take full advantage of its active power transfer capability. It is indicated that the *q‐axis* current is regulated to zero, and only *d‐axis* equations are taken into consideration. Therefore, the network dynamic equation in the conventional *dq* coordinate system can be written as follows.

$$
U_{ac}^d - U_c^d = L_{ac} \frac{dI_{ac}^d}{dt} + R_{ac} I_{ac}^d - \omega_{ac} L_{ac} I_{ac}^q \tag{1}
$$

where the superscript *d* and *q* separately denote the *d‐axis* and *q‐axis* component of one certain variable. *ωac* is the angular speed of the converter-tied AC system.

In order to establish the relationship between the converter‐tied AC system and converter‐tied DC system, the power balance equations must be considered. Since the grid‐ tied converter utilises the unit power factor control in this study, the direction of the grid voltage phasor is consistent with

FIGURE 1 Scheme of a single converter connected to an ideal DC bus.

FIGURE 2 Topology of the grid‐tied converter.

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the direction of the selected *d‐axis*. In other words, the reactive power balance can be neglected. Furthermore, the active power loss generated by the electrical equipment during the operation of the VSC‐HVDC system is very small. Hence, the following equation can be held below.

$$
P_t = P_{ac} = 1.5U_{ac}^d I_{ac}^d \approx P_{dc} = U_{dc}^s(-I_d) \tag{2}
$$

where P_{ac} and P_{dc} separately represent the transmitted active power in the converter‐tied AC system and converter‐tied DC system. *I_d* denotes the DC current contributed by controllers, which will be derived in the next section.

2.2 | **Control dynamics of grid‐tied converter**

Since the DC-side stability of the grid-tied converter is the focus of this paper, the renewable energy generations are regarded as an ideal AC voltage source, and the phase locked loop (PLL) dynamics are neglected so that the influence of the interaction between network dynamics and controller dynamics on DC‐side stability can be thoroughly discussed in the following deduction [[11,](#page-13-0) 21].

For the conventional PV systems or wind power generation systems, PQ controllers are widely used, and the whole control system operates in the *dq* frame [\[26\]](#page-13-0), which is shown in Figure [3.](#page-3-0) The current flowing through the converter‐tied AC system can be well limited to its threshold value by the adopting inner current control strategy. Since the grid‐tied converter operates under the unit power factor control mode, only *d*-axis dynamics of the controllers are considered. Hence, the inner current control can be depicted as follows.

$$
\begin{cases}\n dx_c/dt = K_I^C \left(I_{ac}^{dref} - I_{ac}^d \right) \\
 U_{ac}^d - U_c^d = K_P^C \left(I_{ac}^{dref} - I_{ac}^d \right) + x_c - \omega_{ac} L_{ac} I_{ac}^q\n\end{cases}
$$
\n(3)

where x_c is the state variable associated with the integrator. K_P^C and K_I^C are the proportional and integral gain of the inner current control, respectively. I_{ac}^{dref} is the threshold value of the *d‐axis* component of the current flowing through the converter‐tied AC system.

In addition, the outer power control usually utilises the transmitted active power to generate and regulate the current reference so that the inner current control loop can track the value, which yields

$$
\begin{cases}\n dx_p/dt = K_I^V \left(P_t^{ref} - P_t \right) \\
 I_{ac}^{dref} = K_P^V \left(P_t^{ref} - P_t \right) + x_p\n\end{cases} \tag{4}
$$

where K_P^V and K_I^V are the proportional and integral gain of the outer power control. P_t^{ref} is the selected active power reference of the grid‐tied converter.

Furthermore, as for the classical master-slave control, if the only master converter suddenly stops operating, the remaining converters are incapable of maintaining the DC‐ link voltage, and the multi‐terminal direct current (MTDC) grid will collapse. With the rapid development and wide application of MTDC systems, the power‐voltage droop‐ based control strategy is highly recommended due to its advantage that each of the converters can make its own contribution to the overall system stability. Hence, based on the control scheme shown in Figure 4, the control model can be easily obtained.

$$
P_t^{ref} = K_V \left(U_{dc}^s - U_{dc}^{sref} \right) + P_n \tag{5}
$$

where P_n is the transmitted active power reference of the overall system. U_{dc}^{sref} denotes the DC-link voltage reference of the grid-tied converter. K_V is the droop gain of the powervoltage droop‐based controller.

2.3 | **Small‐signal model of grid‐tied converter**

Combining Equations [\(1\)](#page-2-0) and [\(3](#page-2-0)), (6) can be held around the equilibrium of the studied system.

$$
\frac{\Delta I_{ac}^d(s)}{\Delta I_{ac}^{derf}(s)} = \frac{sK_P^C + K_I^C}{s(sL_{ac} + K_P^C) + (sR_{ac} + K_I^C)}
$$
(6)

where Δ represents a small deviation around the equivalent operating points. *s* denotes the complex frequency of the VSC‐ HVDC system.

Generally, in the actual engineering applications, the inner current control dynamics can be reduced to a simple first-order oscillator by selecting appropriate PI coefficients as follows [\[22\]](#page-13-0). Hence, the control model can be further simplified.

$$
\frac{L_{dc}}{K_P^C} = \frac{R_{dc}}{K_I^C} = \tau_c \quad \Rightarrow \quad \frac{\Delta I_{ac}^d(s)}{\Delta I_{ac}^{def}(s)} = \frac{1}{(1 + \tau_c s)} \tag{7}
$$

FIGURE 3 Generic control logic of conventional PQ controller.

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where τ_c is the time constant of the grid-tied converter.

Linearising Equations [\(4\)](#page-2-0) and (5) around the equilibrium of the studied system and substituting it to Equations (7), the AC current deviation influenced by various controllers can be obtained, which yields as follows

$$
\Delta I_{ac}^d(s) = \frac{sK_P^V + K_I^V}{s(1 + \tau_c s)} \left[K_V \Delta U_{dc}^s(s) - \Delta P_t(s) \right]
$$
(8)

Combining Equations ([2\)](#page-2-0) and (8), the intermediate variables ΔP_t and ΔI_{ac}^d can be eliminated, and the DC current deviation contributed by control effects can be derived.

$$
\Delta I_d(s) = \left[g_{dc} - k_u K_V B(s)\right] \Delta U_{dc}^s(s) \tag{9}
$$

where g_{dc} is defined as the equivalent input conductance of the grid-tied converter and k_u is the voltage scaling factor of the studied VSC‐HVDC system, which yields as follows.

$$
\begin{cases}\ng_{dc} = \frac{I_{d(0)}}{U_{dc(0)}^s} = \frac{P_{t(0)}}{(U_{dc(0)}^s)^2}, k_u = \frac{3U_{ac(0)}^d}{2U_{dc(0)}^s} \\
A(s) = \frac{1}{1 + \tau_c s} \left(K_p^V + \frac{K_I^V}{s} \right), B(s) = \frac{A(s)}{1 + 1.5U_{ac(0)}^d A(s)}\n\end{cases} (10)
$$

Equations (9) and (10) constitute the general small‐signal model of the VSC‐based converter, which provides a foundation for the following deduction and stability analysis.

3 | **DC‐SIDE STABILITY ANALYSIS OF A SINGLE CONVERTER CONNECTED TO AN IDEAL DC BUS**

In the generic two-terminal or multi-terminal VSC-HVDC systems, the grid‐tied converters may operate under different control modes to satisfy the local requirements of the power system. To fully investigate the DC-side stability of grid-tied converter under different control modes, the small‐signal model of a single converter connected to an ideal DC bus is formulated, and the stabilising conditions of the overall system are also deduced based on the electrical torque method in this section.

FIGUR E 4 Generic control logic of power‐voltage droop‐based controller.

3.1 | **Small‐signal model of a single converter connected to an ideal DC bus**

With reference to the scheme shown in Figure [1](#page-2-0), the equivalent circuit of a single converter connected to an ideal DC bus can be illustrated by Figure 5. R_{dc} and L_{dc} separately represent the equivalent resistance and inductance of DC cables. C_{dc} denotes the DC-link capacitance installed at the terminal of the grid-tied converter. I_c is the DC current flowing through the DC-link capacitance. I_g represents the constant DC current source affected by renewable energy generations. Hence, the dynamics of the converter‐tied DC system can be described by the following differential algebraic equations.

$$
\begin{cases}\nU_{dc}^{s} - U_{dc}^{r} = R_{dc}I_{dc} + L_{dc}\frac{dI_{dc}}{dt} \\
I_{c} = C_{dc}\frac{dU_{dc}^{s}}{dt}, I_{g} = I_{d} + I_{c} + I_{dc}\n\end{cases}
$$
\n(11)

Generally, appropriate input/output state variables can be used to obtain suitable *transfer functions* or *state–state expressions* for stability analysis. As for the synchronous generators (SGs) dominated power system, the power angle and angular speed are always selected as the state variables [\[23](#page-13-0)]. By contrast, the DC‐link voltage and the DC current flowing through the DC‐link capacitance are often chosen as the state variables for a clear physical interpretation in the converter‐ interfaced generations (CIGs) dominated power systems [\[22\]](#page-13-0). While, in this study, the equivalent inductance voltage noted as U_L and the DC current I_{dc} are more suitable for the electrical torque method to verify its effectiveness in the DC‐side stability analysis of CIGs dominated power systems. Therefore, the inductance dynamics should be considered, which yields as follows.

$$
L_{dc}\frac{dI_{dc}}{dt} = U_L
$$
 (12)

Combining Equations (11) and (12) and linearising them at the equilibrium, the *state–space expression* can be obtained.

$$
\begin{array}{|c|c|} \hline & 35 \\ \hline \end{array}
$$

 \overline{a} $\Big\}$ $\Big\}$ $L_{dc} \frac{d\Delta I_{dc}}{dt} = \Delta U_L$ $C_{dc} \frac{d\Delta U_L}{dt} = (\Delta I_g - \Delta I_d)$ $\frac{R_{dc}C_{dc}}{L_{dc}}\Delta U_L - \Delta I_{dc}$ (13)

In addition, based on the above small‐signal model, the following transfer function can be derived, which yields

$$
\frac{\Delta I_{dc}(s)}{\Delta I_g(s) - \Delta I_d(s)} = \frac{1}{L_{dc}C_{dc}s^2 + R_{dc}C_{dc}s + 1}
$$
(14)

The second-order denominator of the above transfer function corresponds to the system dominant oscillation mode. Hence, after ignoring the converter control effects, the oscillation frequency of the studied power system can be approximated as follows.

$$
\omega_{dc} \approx \frac{1}{\sqrt{L_{dc}C_{dc}}}
$$
\n(15)

Equations (9) , (13) and (15) constitute the small-signal model of a single converter connected to an ideal DC bus. Based on the derived model, the stabilising conditions can be obtained using the electrical torque analysis method.

3.2 | **Stable equilibrium points computation**

As for the general power system, the system dynamics can be depicted by the following equation, which yields [\[27](#page-13-0)],

$$
\dot{x} = f(x), x \in R \Rightarrow f(x_e) = 0 \tag{16}
$$

where, f is the system equations. x represents a vector of system operating states, and x_e is the equilibrium states of the power system.

Generally, the stable equilibrium points (SEPs) can be computed by utilising algebraic solvers when an initial point sufficient close to the SEP is provided, and one common solver used for SEP computation is the Newton–Raphson (NR) method, which yields,

$$
x_{i+1} = x_i - Df(x)^{-1}f(x)
$$
 (17)

Once the approximated iterative error is less than the predetermined iterative error, the NR method is terminated, and the SEPs can be obtained.

With the rapid development of power electronics-based power systems, various power electronic equipment is installed in the modern power system, which brings new challenges to equilibrium point computation. In Ref. [[28](#page-13-0)], the steady‐state model of VSC is established, and the efficient power flow algorithm for AC/MTDC links is proposed. In addition, the feasibility and applicability of Newton–Raphson FIGURE 5 Equivalent circuit of a single converter connected to an **FIGURE FIGURE 5** Equivalent circuit of a single converter connected to an **FIGURE 5** Equivalent circuit of a single converter connected to an **FIGURE** for power electronics‐based power system are fully studied in several papers [\[29–31](#page-13-0)].

As for the power system investigated in this study, the SEPs can be directly calculated based on the steady-state model of the whole system without using the Newton– Raphson method, which involves quantities of numerical calculations. Hence, the system plant model and control model can be re‐written in a steady state as follows.

System Plant Model (Steady State):

 \overline{a}

$$
\begin{cases}\nU_{acn} - U_{c(0)}^d = R_{ac} I_{ac(0)}^d - \omega_{ac} L_{ac} I_{ac(0)}^q \\
U_{dc}^s - U_{dcn} = R_{dc} I_{dc(0)} \\
I_{c(0)} = 0, I_g - I_{d(0)} = I_{dc(0)} \\
P_{t(0)} = U_{acn} I_{ac(0)}^d = U_{dc(0)}^s (I_g - I_{d(0)})\n\end{cases}
$$
\n(18)

Control Model (Steady State):

 \overline{a}

$$
\begin{cases}\nI_{ac(0)}^d = I_{ac(0)}^{dref}, P_{t(0)} = P_{t(0)}^{ref} \\
x_{p(0)} = I_{ac(0)}^{dref} \\
U_{acn} - U_{c(0)}^d = x_{c(0)} - \omega_{ac} L_{ac} I_{ac(0)}^q \\
P_{t(0)}^{ref} = K_V \left(U_{dc(0)}^f - U_{dcn} \right) + P_n\n\end{cases}
$$
\n(19)

where the subscript (0) represents the steady-state value of a certain state variable. U_{acn} and U_{dcn} are separately the nominal voltage value of an ideal AC voltage source and ideal DC bus.

Based on Equations (18) and (19), the SEPs of the studied VSC‐HVDC system can be obtained.

$$
\begin{cases}\nU_{dc(0)}^{s} = \left[(U_{dcn} + K_V R_{dc}) + \sqrt{\Delta} \right] / 2 \\
\Delta = (U_{dcn} + K_V R_{dc})^2 + 4R_{dc} (P_n - K_V U_{dcn})\n\end{cases}
$$
\n
$$
\begin{cases}\nP_{t(0)} = P_{t(0)}^{ref} = P_n + K_V \left(U_{dc(0)}^s - U_{dcn} \right)\n\end{cases}
$$
\n(20)

$$
\begin{cases}\nP_{t(0)} = P_{t(0)}^{ref} = P_n + K_V \left(U_{dc(0)}^s - U_{dcn} \right) \\
I_{dc(0)} = I_g - I_{d(0)} = P_{t(0)} / U_{dc(0)}^s \\
I_{ac(0)}^d = I_{ac(0)}^{dref} = P_{t(0)} / U_{acn}, I_{ac(0)}^q = 0 \\
U_{c(0)}^d = U_{acn} - R_{ac} I_{ac(0)}^d \\
x_{c(0)} = R_{ac} I_{ac(0)}^d, x_{p(0)} = I_{ac(0)}^d\n\end{cases} \tag{21}
$$

Hence, the derived small-signal model of the studied VSC-HVDC system is linearised around the SEPs by Equations (21), and the small‐signal stability of the converter‐tied DC system will be thoroughly evaluated in the next section.

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3.3 | **Electrical torque analysis**

It is indicated by classical electrical torque analysis that the feedback torque deviation consists of two individual parts. Generally, the torque deviation component whose phasor direction is perpendicular to the direction of the power angle is defined as a damping torque, while the synchronising torque is defined as the component whose phasor direction is parallel to that of the power angle. The authors in Ref. [[23\]](#page-13-0) point out that although both damping torque and synchronising torque may influence the small‐signal stability of the whole system, the impacts contributed by the damping torque is dominant. Hence, it is necessary to derive the concrete expression of the damping torque for stability analysis.

Based on Equations (9) , (13) and (15) , the transfer function model describes the dynamics of the studied VSC‐HVDC system can be obtained, which is illustrated by Figure 6. Following the definitions of damping torque and synchronising torque in the SGs dominated power system in Ref. [\[22\]](#page-13-0), the DC current deviation component perpendicular to the direction of *Idc* is defined as a damping current, which is denoted by superscript *D*, and the component parallel to the direction of *Idc* is defined as a synchronising current, which is distinguished by superscript *S*. *H*(*s*) is the transfer function between the DC current flowing through the DC cables I_{dc} and equivalent inductance voltage *UL*, which yields as follows.

$$
H(s) = \left[g_{dc} - k_u K_V B(s)\right] \left(1 + \frac{R_{dc}}{sL_{dc}}\right) \tag{22}
$$

Based on Equations (22) and the transfer function model shown in Figure 6, the damping coefficient noted as K_D and the synchronising coefficient marked as K_S can be obtained.

FIGUR E 6 Transfer function model of a single converter connected to an ideal DC bus.

$$
\begin{cases}\nK_D = \frac{R_{dc}C_{dc}}{L_{dc}} + Re[H(s)] \\
K_S = 1 + \omega_{dc}L_{dc}Im[H(s)]\n\end{cases}
$$
\n(23)

In addition, the authors in Ref. [[23](#page-13-0)] indicate that the system stability can be guaranteed when the damping coefficient is positive. Based on the stability criterion, the small‐signal stability of the grid-tied converter under different control modes can be well‐studied in stabilising conditions.

3.4 | **Stabilising conditions under different control modes**

3.4.1 | Mode 1: constant active power control:

Equation [\(5](#page-3-0)) reveals that when the droop gain K_V is selected as zero, the grid‐tied converter operates under constant active power control mode, and the active power reference is constant. Hence, the damping coefficient of the studied VSC‐ HVDC system can be reduced below.

$$
K_V = 0 \quad \Rightarrow \quad K_D = g_{dc} + \frac{R_{dc} C_{dc}}{L_{dc}} \tag{24}
$$

Based on the electrical torque analysis, the stabilising condition under constant active power control mode can be obtained.

$$
K_D > 0 \quad \Rightarrow \quad P_{t(0)} > -\frac{R_{dc}L_{dc}}{C_{dc}} \left(U_{dc(0)}^s\right)^2 \tag{25}
$$

It can be easily summarised from Equation (25) that the studied system can always remain stable when the grid‐tied converter works as a rectifier under constant active power control mode. On the contrary, when the grid-tied converter operates as an inverter, the network dynamics may introduce negative virtual damping to the power system. Based on Equation (25), the system stability will be destroyed once the transmitted active power exceeds the threshold, which yields as follows.

$$
P_t^{cri} = \frac{R_{dc}C_{dc}}{L_{dc}}U_{dcn}^2
$$
\n(26)

Due to the slight fluctuation of DC‐link voltage during the normal operation of the VSC‐HVDC system, the steady‐state voltage value marked as $U_{dc(0)}^s$ can be replaced by the nominal voltage of the converter-tied DC system noted as U_{dcn} . P_t^{cri} is the active power transfer limit of the studied system. In addition, in the normal operation of the VSC‐HVDC system, the smoothing reactors are often installed at the terminal of DC cables to suppress the DC short‐circuit current. In addition, the transmission lines usually adopt a double‐circuit line to guarantee the voltage stability and reduce the power loss. Hence, the equivalent resistance and inductance can be defined as follows.

$$
R_{dc} = r_0 l \quad L_{dc} = l_0 l + 2L_T \tag{27}
$$

where r_0 and l_0 separately denote the unit resistance and unit inductance of the DC cables. *l* is the power transmission distance of the studied system. L_T represents the smoothing reactor installed at the terminal of DC cables.

It is obviously indicated by Equations (26) and (27) that if the active power transfer capacity is expected to be enhanced as much as possible without destroying the system stability, and a larger DC‐link capacitance should be selected, and the level of DC‐link voltage should be raised.

Furthermore, considering the capacity of the grid-tied converter, the active power transfer limit of the studied system when the converter works under this control mode can be re‐written below.

$$
P_t^{cri} = \min\left\{P_B, \frac{r_0 l C_{dc}}{(l_0 l + 2L_T)} U_{dcn}^2\right\}
$$
 (28)

where P_B is the capacity of the grid-tied converter.

3.4.2 | Mode 2: constant DC‐link voltage control:

When the grid-tied converter operates under constant DC-link voltage control mode, the DC‐link voltage deviation is the same as the DC-link voltage reference deviation. Hence, the following relationship is satisfied.

$$
\frac{\Delta P_t^{ref}(s)}{K_V} = 0 \quad \Rightarrow \quad K_V \to -\infty \tag{29}
$$

Equation (29) reveals that the droop gain K_V needs to be regulated to one sufficient small value to ensure that the grid‐ tied converter operates under constant DC‐link voltage control mode. Based on Equation [\(22](#page-5-0)), the damping coefficient affected by control dynamics will approach a relative large positive value, which reveals that the grid‐tied converter under constant DC‐link voltage control mode will always introduce positive virtual damping to the power system. Hence, under this circumstance, the damping coefficient will satisfy the following approximation.

$$
K_D = \frac{R_{dc}C_{dc}}{L_{dc}} + Re[H(s)] \rightarrow +\infty
$$
 (30)

(30) reveals that the damping coefficient will always be positive for the grid‐tied converter under constant DC‐link voltage control mode, implying the system will always remain stable.

3.4.3 | Mode 3: universal droop‐based control

In order to derive the stabilising conditions for the grid‐tied converter under universal droop‐based control mode, the real and imaginary parts of $B(s)$ need to be separated. Hence, the transfer function $B(s)$ is re-written as follows.

$$
B(s) = \frac{K_P^V s + K_I^V}{bs + a}, \begin{cases} a = 1.5U_{ac(0)}^d K_I^V - \tau_c \omega_{dc}^2\\ b = 1 + 1.5U_{ac(0)}^d K_P^V \end{cases}
$$
(31)

Rationalising the dominator of the expression Equation (31) , the real part of the transfer function $B(s)$ can be separated out, which yields as follows,

$$
Re[B(s)] = \frac{aK_I^V + bK_P^V \omega_{dc}^2}{a^2 + (b\omega_{dc})^2}
$$
 (32)

Substituting Equation (32) into Equation (23) , the damping coefficient of the studied system under universal droop‐based control mode can be expressed as follows.

$$
K_D = g_{dc} + \frac{R_{dc}C_{dc}}{L_{dc}} - \frac{k_u K_V (cK_P^V + dK_I^V)}{a^2 + (b\omega_{dc})^2}
$$
(33)

where

$$
c = b\omega_{dc}^2 + R_{dc}a/L_{dc}, d = a - R_{dc}b/L_{dc}
$$
 (34)

Equation (33) indicates that the system stability is related to all three controllers when the grid‐tied converter is under this kind of control mode. Hence, it is necessary to discuss the contribution of different controllers to the system stability.

In order to investigate the impacts of inner current control on the VSC-HVDC system stability, the sensitivity analysis of the time constant of the grid-tied converter can be carried out. Hence, by derivation of τ_c , the following expression can be held.

$$
\frac{\partial K_D}{\partial \tau_c} = -\frac{k_1(a + b\omega_{dc})(a - b\omega_{dc})}{\left[a^2 + (b\omega_{dc})^2\right]}
$$
(35)

with

$$
k_1 = k_u K_V \left(K_I^V + \frac{R_{dc} K_P^V}{L_{dc}} \right) < 0 \tag{36}
$$

Based on Equations (35) and (36), it can be concluded that increasing the time constant within the following range can improve the system stability.

$$
\frac{1.5U_{acn}K_I^V - b\omega_{dc}}{\omega_{dc}^2} < \tau_c < \frac{1.5U_{acn}K_I^V + b\omega_{dc}}{\omega_{dc}^2} \tag{37}
$$

where U_{acn} is the nominal voltage of the converter-tied AC system.

It is interesting to mention that the time constant is always selected as several milliseconds to meet the action speed of inner current control in the normal operation of the grid-tied

converter. Hence, the impacts the inner current control imposed on the system stability are monotonic and limited.

In addition, since the voltage level of the converter‐tied AC system is relatively high, Equation (31) can be further reduced for more detailed and concise conclusions, which yields as follows.

$$
\begin{cases}\na = 1.5U_{ac(0)}^d K_I^V - \tau_c \omega_{dc}^2 \approx 1.5U_{ac(0)}^d K_I^V = \overline{a} \\
b = 1.5U_{ac(0)}^d K_P^V + 1 \approx 1.5U_{ac(0)}^d K_P^V = \overline{b}\n\end{cases}
$$
\n(38)

Substituting Equation (38) into Equation (33), the damping coefficient of the studied system can be simplified.

$$
K_D = g_{dc} + \frac{R_{dc}C_{dc}}{L_{dc}} - \frac{K_V}{U_{dc(0)}^5} \frac{\overline{a}^2 + (\overline{b}\omega_{dc})^2}{\overline{a}^2 + (\overline{b}\omega_{dc})^2}
$$

= $g_{dc} + \frac{R_{dc}C_{dc}}{L_{dc}} - \frac{K_V}{U_{dc(0)}^5} > 0$ (39)

Equation (39) indicates that the damping coefficient is hardly influenced by outer power control, which means that this kind of control also has limited impacts on the small‐signal stability of the VSC‐HVDC system. Thus, the dynamics of PQ controllers can be neglected when the VSC‐HVDC system stability is further investigated in the future. Furthermore, it indicates that increasing the droop gain may deteriorate the system stability due to the negative value of K_V .

Substitution of Equations ([10\)](#page-3-0) into (39), the critical transmitted power mapping the stability boundary can be obtained.

$$
P_{t(0)} > K_V U_{dc(0)}^s - \frac{R_{dc} C_{dc}}{L_{dc}} \left(U_{dc(0)}^s \right)^2 = -P_t^{cri}
$$
 (40)

Considering the minor voltage fluctuations and converter capacity limitations, the active power transfer limit under this control mode can be expressed as follows.

$$
P_t^{cri} = \min \left\{ P_B, -K_V U_{dcn} + \frac{r_0 l C_{dc}}{(l_0 l + 2L_T)} U_{dcn}^2 \right\}
$$
 (41)

It is indicated by Equation (41) that the active power transfer limit can be greatly improved due to the negative value of the droop gain. In addition, when the droop gain is set as zero, the expression of active power transfer limit is the same as Equation [\(26\)](#page-6-0), and when the droop gain is set as a sufficient small value, VSC controllers will no longer influence the system active power transfer capacity.

In addition, in order to better characterise the stability of the grid‐tied converter, the stability margin can be defined as the relative distance between the actual transmitted active power and the calculated active power transfer limit by Equation (41), which can be expressed as follows.

$$
M = 1 + \frac{P_t}{P_t^{cri}} \ge 1 - \frac{P_B}{P_t^{cri}} \tag{42}
$$

It is indicated by Equation (42) that the stability margin decays with the decrease of the actual transmitted active power and reaches its minimum at $P_t = -P_B$. To ensure that the system has adequate margin under any operating points, Equation (42) should exceed a certain positive value noted as *M*_{min}. Hence, the critical values of certain system parameters can be easily obtained.

$$
\begin{cases}\nK_V^{\text{max}} = \frac{P_B}{(M_{\text{min}} - 1)U_{dcn}} + \frac{r_0 I C_{dc} U_{dcn}}{(l_0 I + 2L_T)} \\
C_{dc}^{\text{min}} = \frac{(l_0 I + 2L_T)}{r_0 I U_{dcn}^2} \left(\frac{P_B}{1 - M_{\text{min}}} + K_V U_{dcn}\right)\n\end{cases} (43)
$$

Based on Equation (43), the relative parameters can be optimised to guarantee the DC‐side stability of the studied VSC‐HVDC system even with the worst operating points.

4 | **NUMERICAL SIMULATIONS**

To validate the proposed stabilising conditions for the grid‐tied converter under DC‐side perturbations, a test VSC‐HVDC system is simulated in the PSCAD/EMTDC. The transmitted active power can be regulated as a constant value or it can be changed automatically according to the universal droopbased control strategy. And the ideal DC bus is regarded as a constant DC voltage source in this case so that the mutual interaction between grid‐tied converter controllers and DC networks can be thoroughly investigated. Furthermore, in order to achieve the fast‐tracking property of the grid‐tied converter, the time constant is set to 2 ms. The initial values of system parameters are listed in Table 1.

4.1 | **Method verification**

Figure [7](#page-9-0) compares the electrical torque analysis method and the modal analysis method to verify the validity of the proposed method in DC‐side stability analysis. In this case, the initial transmitted active power is set to −50 MW, and the negative sign indicates that the grid‐tied converter is operating as an inverter.

Figures [7a,b](#page-9-0) depict the system dynamics under different active power perturbations. It apparently reveals that the system stability deteriorates with the decrease of the transmitted active power. Particularly, in this case, the divergent oscillation phenomenon will appear when the transmitted active power drops to −130 MW and the damping coefficient becomes a negative value of −0.0006, which well proves the feasibility of the electrical torque analysis method. It also indicates that the system stability may be compromised once the transmitted active power exceeds the critical value by Euation ([41](#page-7-0)).

TABL E 1 Parameters of the test VSC‐HVDC system.

Symbol	Item	Value
C_{dc}	DC-link capacitance	$330 \mu F$
L_T	Smoothing reactor	50 mH
r_0	Unit DC cable resistance	$0.015 \Omega/km$
l_0	Unit DC cable inductance	1.635 mH/km
l	Length of DC cables	400 km
U_{dcn}	Rated DC voltage	200 kV
$U_{\rm{gen}}$	Rated AC voltage (line to line)	220 kV
R_{ac}	Resistance of converter-tied AC system	0.016Ω
L_{ac}	Inductance of converter-tied AC system	150 mH
K_P^C	Proportional gain of inner current control	75
K_I^C	Integral gain of inner current control	8
K_p^V	Proportional gain of outer power control	1
K_I^V	Integral gain of outer power control	10
K_V	Droop gain of power-voltage control	$\overline{0}$
τ_c	Time constant of grid-tied converter	2 ms
S_B	Base capacity of the test system	150 MVA
SCR	Short circuit ratio of the test system	1.5

Figure [7c](#page-9-0) illustrates the root locus of the VSC-HVDC system under active power perturbations. It is apparently indicated that the dominant characteristic roots λ_1 and λ_2 gradually move towards the right half of the complex plane with the decrease of transmitted active power, which reveals that the system stability deteriorates. In contrast, the rest characteristic roots $(\lambda_3, \lambda_4$ and $\lambda_5)$ remain the same, implying that they have little impacts on system stability. Figure [7d](#page-9-0) illustrates the variation trends of the real part of the dominant characteristic root and the damping coefficient with transmitted active power variations. It indicates that the real part of the dominant characteristic roots increases while the damping coefficient decays with the decrease of the transmitted active power. It is interesting to find that the signs of both stability indexes reverse almost at the same operating point $(P_t = 105.04$ MW). The above results fully prove that the electrical torque analysis method can be utilised to judge and analyse the DC‐side stability of high‐order power systems without excessive model reduction and numerical calculations.

4.2 | **Impacts of physical parameter variations on DC‐side stability**

The impacts of physical parameters on the DC-side stability are thoroughly investigated in Figure [8.](#page-9-0) Figure [8a](#page-9-0) reveals that the damping coefficient increases, and the dominant characteristic roots move towards the left half of the complex plane with the increase of C_{dc} . In contrast, promoting L_T will make

the damping coefficient approaches to and eventually less than zero as shown in Figure 8b. Meanwhile, the dominant characteristic roots will move to the right half of the complex plane. The above results again verify the feasibility of the electrical torque analysis method in the DC‐side stability analysis of high order power system, which has a certain application prospect. Figures 8c,d illustrate the system dynamic response with different physical parameters after a small perturbation. It is indicated by the non-linear simulation results that the system damping decays with the increase of L_T or the decrease of C_{dc} , which can be well explained by Equations ([39](#page-7-0)) and [\(41\)](#page-7-0). Once the damping coefficient

becomes negative, the divergent oscillation may appear, and the system stability will be compromised. In addition, Figure 8 also reveals that C_{dc} and L_{dc} are highly related to the system oscillation frequency, which can be proved by [\(15\)](#page-4-0). The system oscillation frequency can be reduced by elevating C_{dc} and L_T . However, in the general operation of VSC-HVDC systems, the variation range of physical parameters is not large, which indicates that the system oscillation frequency is always around 5 Hz. Hence, it is reasonable to use *Cdc* and *Ldc* to estimate the dominant oscillation frequency of the HVDC systems.

Damping Coefficient/1×10⁻³

 $\overline{2}$

 -2

 $\mathbf{1}$

 -2

 -0.2

 -0.4

 -0.6

 -0.2

 -0.3

 -0.4

 -0.5

DC-side Current/kA

DC-side Current/kA

Damping Coefficient/1×10⁻³

2.1024.1. Изущение призульту как долого состал состал призульта на состал призульта на состал призульта состал призульта на состал призульта на состал призульта на состал призульта на состал на состал призульта на состал

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4.3 | **Impacts of control parameter variations on DC‐side stability**

Figure 9 depicts the impacts of the bandwidth of PQ controllers on DC‐side stability. The perturbation is set as the transmitted active power drop from 50 MW to 100 MW. From Figure $9a$, the system is slightly improved with the increase of the time constant of inner current control, which can be proved by Equation (37) . In addition, Figures $9b$, c reveal that changing the proportional and integral gains of outer power control hardly influence the DC‐side stability of the grid‐tied converter, which can be explained by Equation ([39](#page-7-0)). Hence, it is feasible to properly ignore the PQ controller dynamics when studying the small-signal stability of CIG dominated power systems, as some papers have assumed.

Figure [10](#page-11-0) depicts the impacts of power-voltage droopbased controller on DC‐side stability. Figure [10a](#page-11-0) presents the system root locus under various droop gains. With the increase of droop gain K_V , the dominant characteristic roots λ_1 and λ_2 gradually move towards the right half of the complex plane, which indicates that the system stability deteriorates. While the eigenvalue λ_5 associated with control dynamics moves in the positive direction of the real axis with the decrease of K_V . However, it is far from the imaginary axis so that the system stability will not be jeopardised. As for the rest roots, λ_3 and λ_4 remain constant with various K_V nor they can influence the stability of the overall system. In addition, as shown in Figure [10b](#page-11-0), the damping coefficient decays with the increase of K_V , which is consistent with the trend of dominant eigenvalues, implying that the system damping gradually decreases and the system stability cannot be guaranteed. Figure [10c](#page-11-0) shows the dynamics of DC-side current with different droop gains under the same perturbation. With increase of K_V , the system stability is worsened, and the divergent oscillation will finally appear. The above results once again demonstrate the feasibility of the electrical torque analysis method in analysing the DC‐side stability of CIG dominated power systems. Furthermore, the system stability can be greatly improved by selecting appropriate droop gain based on Equation ([41](#page-7-0)).

4.4 | **Impacts of system parameter variations on stability margin**

Figure [11](#page-11-0) illustrates the impacts of system parameters on the transmitted active power limit and stability margin. As shown in Figure [11a,](#page-11-0) the transmitted active power limit can be greatly enhanced by increasing DC‐link capacitance or deceasing droop gain, which can be proved by Equation [\(41](#page-7-0)). In addition, Figure [11b](#page-11-0) depicts the trend of stability margin under different system parameters. When the $M_{\rm s,min}$ is set to 0.2, the critical values of system parameters noted as K_V^{max} and C^{min} can be calculated, and the results are −0.4123 and 589.06 μF. It is interesting to mention that, compared with adjusting the value DC‐link capacitance, tuning the droop

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FIGUR E 9 Impacts of the bandwidth of controllers on DC‐side stability.

gain can better improve the system stability margin. The rest figures depict the dynamics of the studied VSC‐HVDC system under the condition that the transmitted active power drops to $-P_B$. The non-linear simulations indicate that the divergent oscillations may appear once the system parameters are outside the specified range, and the whole system stability can no more be guaranteed due to the negative damping coefficient. On the contrary, when the system parameters are appropriately optimised according to Equation ([43](#page-8-0)), the studied system may have sufficient stability margin and the DC-side oscillations can be well suppressed under any possible operation points.

4.5 | **Impacts of AC system dynamics on DC‐side stability**

In order to better illustrate the impacts of AC system dynamics on DC‐side stability, the related simulation results are

FIGURE 10 Impacts of the droop gain on DC-side stability.

presented in this study, which is also beneficial to the explanation of the hypothesis about converter‐tied AC system. As shown in Figure [12a](#page-12-0), when the system perturbation is set as an active power fluctuation from 50 MW to 120 MW, the frequency and magnitude of DC‐link current oscillation have no significant change under converter-tied AC system SCR variations. It indicates that the impacts of SCR variations on DC‐ side stability are minimal. This can be well explained that the strength was limited. In addition, the impacts of control gains of PLL controller on DC‐side stability are described in Figure [12b,c.](#page-12-0) The simulation results reveal that the system stability always maintains with the variation of the bandwidth of the PLL controller, which indicates that the PLL dynamics also have limited impacts on DC‐side stability and may not threaten the stable operation of the converter‐tied DC system. Moreover, when the proportional and integral gains are selected appropriately $(K_{PPLL} = 2, K_{IPLL} = 400)$ (see Ap-pendix [A\)](#page-13-0), the related eigenvalues ($\sigma_{PLL} = -220 \pm 199$ j) are far from the imaginary, which is beneficial to the system overall stability. Hence, the above control gains are used in the simulation studies.

FIGURE 11 Impacts of system parameters on stability margin.

5 | **CONCLUSION AND FUTURE WORK**

5.1 | **Conclusion**

The electrical torque analysis method is firstly applied in analysing the DC‐side stability of VSC‐HVDC systems considering the impacts of controllers, networks and their mutual interactions. The small‐signal model of a single converter connected to an ideal DC bus is formulated and the analytical stabilising conditions of the grid‐tied converter under different control modes are further deduced on the basis of the electrical torque method. Under the constant active power control mode,

FIGURE 12 Impacts of AC system dynamics on DC-side stability.

the system stability is mainly influenced by DC network dynamics. In contrast, the system stability can be well guaranteed when the grid-tied converter is under constant DC-link voltage control mode. It is also indicated by Equation ([39](#page-7-0)) that the system stability may be destroyed once the positive damping of the whole system can no more offset the negative virtual damping contributed by control dynamics. To guarantee sufficient stability margin reserve under any possible operating points, the relative system parameters can be tuned according to the principle by Equation [\(43\)](#page-8-0). In addition, the classical PQ controllers are proven to have limited impacts on the DC‐side stability. In conclusion, the electrical torque analysis method can be utilised to assess the stability of high-order power systems, which may provide some inspiration for future research on the stability analysis of multi‐terminal HVDC links.

5.2 | **Future work**

In order to further investigate the DC‐side stability of VSC‐ HVDC links, some more research works need to be carried out in the future, which are illustrated as follows.

- A. The dynamics of the converter-tied AC system, such as SCR, PLL and AC voltage controller dynamics, have influences on system stability. In addition, the interaction between AC‐side dynamics and DC‐side dynamics may generate different oscillation characteristics. Hence, both of them deserve further research and comparative studies with the results in this study
- B. Even though the single converter connected to an ideal DC bus under different control modes is well studied in this paper, the investigations on the stability of generic twoterminal/multi‐terminal VSC‐HVDC links based on the proposed method have not been completed yet. Accordingly, the grid modelling method of MTDC links will be proposed, and the feasibility and applicability of the electrical torque analysis method in a radial or meshed MTDC links can be verified in future work.

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CONFLICT OF INTEREST STATEMENT

The authors declare no conflicts of interest.

DATA AVAILABILITY STATEMENT

No data was used in the research.

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APPENDIX A

Main parameter s o f the PLL

According to the authors in Ref. [32], the transfer function of the PLL can be expressed as follows.

$$
s\Delta\theta(s) = \left(K_{PPLL} + \frac{K_{IPLL}}{s}\right)\Delta u_p^q(s) \tag{A.1}
$$

where u_p^q is the *q*-axis component of the voltage of the point of common coupling (PCC), and θ denotes the tracking phase angle provided by PLL. K_{PPLL} and K_{IPLL} are separately the proportional and integral gains of the PLL controller, which are configured as 2 and 400 in the simulation studies, respectively.