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# Efficiency improvement in silicon nanowires/ conductive polymer hybrid solar cells based on formic acid treatment

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### Abstract

We investigated the mechanisms causing the power conversion efficiency (PCE) improvement in hybrid silicon nanowires/ poly(3,4-ethylenedioxythiophene): poly(styrenesulfonate) (PEDOT:PSS) solar cells by formic acid treatment. After the five minutes of formic acid treatment at elevated temperature (140 °C), the averaged PCE of the device can be improved from 8.00 % to 9.22 %. Through the formic acid treatment, the conductivity of the PEDOT:PSS film increases from 683 S/cm to 1582 S/cm and the averaged built-in voltage also increased from 0.43 V to 0.48 V. The larger built-in voltage value can suppress the recombination of photogenerated charges in the hybrid solar cells and turn the silicon nanowires/PEDOT:PSS junction barrier more close to ideal mott-schottky barrier. Through XPS and trap state density calculation, we confirmed such improvement is attributed to the decreases of interface states density in the hybrid solar cells after the formic acid treatment. Our findings indicate the critical roles of polymer conductivity and the

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interface quality of organic/inorganic interface in the performance of hybrid silicon nanowires/PEDOT:PSS solar cells.

Keywords:

silicon nanowires, PEDOT:PSS, hybrid solar cell, efficiency improvement, interface states

## **1. Introduction**

Being one of the important alternative energy sources to fossil fuel, solar energy can be used to produce electricity through solar cells directly. To date, although silicon based solar cell is still dominating the market, a number of novel photovoltaic devices based on other materials system such as organic and perovskite materials have been developed. One kind of solar cell which also draws a lot of attentions from the research community is the hybrid cells which combining the advantages of both organic and inorganic devices. [1,2]. For example in the silicon/organic photovoltaics, the high temperature diffusion process can be eliminated and it reduces the energy consumption during the solar cell production process by 35% [3]. As a result, the manufacturing cost of the hybrid solar cells can be significantly lower than the silicon counterparts [4,5]. Among different organic semiconductors which have been utilized in hybrid silicon solar cells such as graphene [6,7], poly(3-hexylthiophene) (P3HT) [8], carbon nanotubes [9,10,11] and poly(3,4-ethylenedioxythiophene):polystyrenesulfonate (PEDOT:PSS) [12,13]. PEDOT:PSS is one of the promising candidates due to its high optical

transparency [14], good thermal stability [15] and solution processability [16]. However, at the same time, the pristine PEDOT:PSS also suffered from the issue of low electrical conductivity (less than 1S/cm) [17].

The low conductivity of the PEDOT:PSS would suppress the carriers collection, promote joule heating and lower the device efficiency. To overcome this challenge, dimethyl sulfoxide (DMSO) has been added to PEDOT:PSS as a co-solvent to increase the conductivity of the pristine PEDOT:PSS film and thus the power conversion efficiency (PCE) of planar silicon/PEDOT:PSS solar cells [18,19,20]. By adding 2.5% by weight DMSO to the PEDOT:PSS solution, the conductivity of PEDOT:PSS film can be enhanced extensively from less than 1 to around 99 S/cm and PCE was increase from 2.8% to 8.5% [20]. The PCE improvement is attributed to more interconnected conduction paths in the DMSO-added PEDOT:PSS films so that better charge carrier transportion and separation in the solar cell can be achieved [20]. The conductivity can be further enhanced by using the nitric acid vapor post-treatment. Zhao et al. demonstrated the conductivity of the DMSO-added PEDOT:PSS film can be increased from 300 to 1000 S/cm and PCE of the planar silicon/PEDOT:PSS solar cell can improves from 4.52% to 8.81% by exposing the device under nitric acid vapor environment for around ten seconds [21]. Similarly, formic acid formic acid treated PEDOT:PSS films were also utilized to replace tin doped indium oxide (ITO) layer in the flexible  $poly(3-hexylthiophene)/[6,6]-phenyl-C_{61}-butyric acid methyl ester (P3HT/PCBM)$  polymer solar cells [22]. Actually, other than the post-treatment process on the PEDOT:PSS, the performance of silicon/PEDOT:PSS hybrid solar cells can also be improved by modifying the surface morphologies of the organic/inorganic interface and one effective way to do this is to covert the planar silicon into the nanowires (NWs) form. The increase of junction area by nanowires can enhance photon absorption and charge separation in the device [23,24]. In this work, other than the increases in the PEDOT:PSS conductivity, we are focusing on investigating what are the other possible factors causes the increases of PCE in the formic acid treated SiNWs/PEDOT:PSS solar cells with a special focus on the quality of the interface. The treatment was carried out by dropping formic acid onto the PEDOT:PSS thin film which has been annealed with 5 wt% DMSO. The formic acid treated device shows a PCE improvement in the open circuit voltage  $(V_{oc})$  and fill factor (FF) of the device and thus the PCE of the device can reach 9.22%. The reverse saturation current and ideality factor of the hybrid solar cell are also analyzed from the C-V measurements and the fitting of J-V curves to Lambert's W-function. From the x-ray photoemission spectroscopy (XPS) and atomic force microscopy (AFM) characterizations, we found a drop in the ratio of PSS to PEDOT surface composition of the formic acid treated devices such that the PEDOT is aggregated at the surface layer. We further compared the SiNWs/PEDOIT:PSS interface quality in the hybrid solar cells before and after the formic acid treatment by evaluating the density of interface states. It was found that the density of the interface states is ten times

smaller in the formic acid treated device.

#### 2. Experimental details

The n-type silicon (100) wafers with resistivity of 1-10  $\Omega$ ·cm and thickness of 525  $\mu$ m were cleaned and underwent the electroless etching process to develop silicon nanowires array with a length of around 400 nm. The details of the electroless etching process has been reported elsewhere and will not repeat here [22]. After the formation of the nanowires, the silicon wafers were immersed in dilute hydrofluoric acid to remove the native oxide and followed by twenty minutes of ambient air exposure to improve the surface wetting property. Pristine PEDOT:PSS solution (Clevios PH1000 mixed with 5 wt% DMSO) was filtered via a 0.45 µm pore size membrane and spin-coated on the nanowires textured silicon wafers at 3000rpm for 30s. The wet PEDOT:PSS films were annealed under 140°C for 5 minutes. In our formic acid treatment, 100 µl of 98wt% formic acid is dropped onto the annealed PEDOT:PSS film and then dried at 140°C for another 5 minutes. Finally, the devices were finished by depositing silver anode fingers by screen-printing silver paste on top of PEDOT:PSS and gallium indium eutectic on the backside of the silicon wafers as cathode. The schematic diagram of the silicon/PEDOT:PSS hybrid solar cell is shown in Fig. 1(a).

## 3. Results and discussion

The average performance of totally 50 devices with and without formic acid treatment were shown in Table I. The optical transmittance and the current density-voltage (J-V) curves of the pristine and treated films were shown in Fig. 1(b) and (c), respectively. Although a mild decrease in transmittance can be observed for wavelength larger than 600nm, the devices go through the formic acid treatment in general show a higher open circuit voltage than the pristine devices. From the statistical study plot in Fig. 2, it can be noticed that the device to device variation in the formic acid treated solar cells are smaller, and the averaged PCE increases from 8.00% (pristine) to 9.22% (treated), corresponding to a 15.3% PCE improvement. The solar cell characteristic parameters including light generation current density ( $J_L$ ), reverse saturation current density ( $J_0$ ), ideality factor (n), series resistance ( $R_{sh}$ ) can be further obtained by fitting experimental J-V data with the Lambert's W-function [25] and the results are summarized in table II:

$$J = n \cdot \frac{\left(k_B T / q\right)}{R_s} \cdot lambertw \left(\frac{J_0 R_s}{n \cdot \left(k_B T / q\right) \cdot \left(1 + R_s / R_{sh}\right)} \exp\left(\frac{V + R_s \left(J_0 + J_L\right)}{n\left(k_B T / q\right)\left(1 + R_s / R_{sh}\right)}\right)\right) + \frac{V / R_{sh} - \left(J_0 + J_L\right)}{1 + R_s / R_{sh}}$$
(1)

The  $R_s$  of the device decreases from 1.77 to 1.53  $\Omega$ cm<sup>2</sup> after formic acid treatment which agrees with the four probe conductivity measurements of the PEDOT:PSS films (683 S/cm for pristine and 1582 S/cm for treated). On the other hand, the ideality factor, reverse saturation current density and shunt resistance all show a decrease in the formic acid treated devices. To confirm the origin of the conductivity increases, we utilized XPS to examine the chemical composition at the surface the PEDOT:PSS films (Fig. 3(a)). In the Sulphur (2p) spectra of the films, the binding energy peaks at 169 and 167.8eV are referring to the sulphur atoms in PSS and those at 164.6 and 163.4eV are referring to the sulphur atoms in PEDOT [26]. Based on the area of the energy peaks in Fig. 3(a), the composition ratio of PSS to PEDOT can be evaluated and the composition ratio decreased from 2.94 (pristine) to 1.22 (treated). which is comparable with the observation from Mengistie et al [22]. The reduction in the PSS ratio suggests more hole transporting PEDOT is able to connected with the silver grid anode which can reduce  $R_s$  of the device. From the atomic force microscopy (AFM) height in Fig. 3(b-c), it can be noticed that aggregation of the PEDOT is occurred in the treated PEDOT:PSS film. The brighter area and darker area of the phase image in Fig. 3(d-e) represent the aggregated PEDOT and PSS matrix respectively. The separation distance between the hole conductive PEDOT phase is decreased after formic acid treatment and thus better interconnection of PEDOT clusters is expected. The smaller value of  $R_s$  value also contributed to the improvement in FF in the formic acid treated devices. The averaged FF of the SiNWs/PEDOT:PSS hybrid solar cells improved from 55.8% to 60.1% after formic acid treatment.

The build in-voltage ( $V_{bi}$ ) of a diode actually provides important information on the quality of the junction. One way to evaluate the value of  $V_{bi}$  is by using the x-axis intercept of the 1/C<sup>2</sup>-V plot as shown in Fig. 4(a-b). From the figures, it can be noticed that the values of  $V_{bi}$  are quite similar for the pristine and formic acid treated device and maintained at 0.6V.

However, such  $V_{bi}$  values does not match that obtained from J-V method, where the  $V_{bi}$  values were calculated via the equation [27]:

$$V_{bi} = (-k_B T / q) \ln (J_0 / (A^{**} T^2)) - V_n$$
(2)

here  $A^{**}$  is the effective Richardson constant (110 Acm<sup>-2</sup>K<sup>-2</sup> for n-type silicon) [27], and  $V_n = (k_B T / q) \ln(N_c / N_d)$  where  $N_c$  is the effective density of states in the conduction band of silicon (=3x10<sup>19</sup>cm<sup>-3</sup>) and  $N_d$  is the doping level of silicon (=6x10<sup>14</sup>cm<sup>-3</sup>). By using the equation to perform fitting, the averaged built-in voltage are 0.43V and 0.48V for pristine and after formic acid treatment device respectively. Based on fitting the J-V curves of the twenty five devices into Eq (1) and Eq. (2), we noticed a the built-in voltage and the ideality factor the silicon/PEDOT:PSS hybrid solar cells is linear correlated. It can be observed from Fig. 4(c) that when n=1, the  $V_{bi}$  value is around 0.60V, which agree with the finding by using the C-V measurements. The deviation of the V<sub>bi</sub> values between the C-V and J-V method is actually related with the non-ideal (n>1) behavior of the SiNWs/PEDOT:PSS hybrid solar cells. It is due to the schottky-mott relationship in the  $1/C^2$ -V plot predicts the built-in voltage in the absence of interface states by assuming n=1. As mentioned before, the fitted n values were equal to 2.56 and 2.11 for the pristine and formic acid treated PEDOT:PSS respectively. The deviation of the ideality factor and built in voltage suggests the junction between SiNWs/PEDOT:PSS is non-ideal and interface states in the hybrid solar cells cannot be ignored in determining the actual  $V_{bi}$ .

The origin and the density of the interface states are worth to further study as they are directly related to the performance of the hybrid solar cell. After the preparation of silicon surface by diluted hydrofluoric acid etching, the routine de-ionized water rinsing and drying procedure could unavoidably grow a thin insulating oxide film on the silicon surface. The thickness of the interfacial oxide layer presented at the freshly prepared Si/PEDOT:PSS interface can reach around 1.5 nanometers [28]. The relationship between the interface states  $(D_s)$  and the ideality factor is given by [29]:

$$n = 1 + \frac{\left(\delta / \varepsilon_{i}\right)\left(\varepsilon_{s} / W + qD_{s,Si}\right)}{1 + \left(\delta / \varepsilon_{i}\right)qD_{s,P}}$$
(3)

where  $\delta$  is the interface layer thickness,  $c_i$  and  $c_s$  is the permittivity of the interfacial layer ( $\approx 3.5\varepsilon_0$ ) and the semiconductor ( $\approx 11.7\varepsilon_0$ ) respectively. *W* is the space charge width which can be evaluated by  $W = \sqrt{(2\varepsilon_s V_{bi})/(qN_d)}$ . By using  $V_{bi}$  values of 0.43V for the pristine device and 0.48V for the formic acid treated device, *W* are around 0.89µm (pristine) and 0.94µm (treated). The interface states are contributed from two parts, partly on the PEDOT:PSS ( $D_{s,P}$ ) and partly on the silicon ( $D_{s,Si}$ ). The two curves (open circle and open triangle) in Fig. 5 show the relationship between  $D_{s,P}$  and  $D_{s,Si}$  for the value n = 2.56 and 2.11 respectively. When the device is under zero bias, the two components ( $D_{s,P}$  and  $D_{s,Si}$ ) constitute an interface-state charge density  $Q_{GS}$  [30]:  $Q_{GS} = qD_s(qV_{bi} + qV_n - E_s + \phi_{CNL})$  where  $D_s = D_{s,P} + D_{s,Si}$ . They are represented in the energy band diagram in Fig. 6. On the other hand, by charge neutrality,  $Q_{GS}$  is also equal and opposite to the total charges developed on the PEDOT:PSS surface ( $O_P$ )

and space charge layer  $(Q_{SC})$  in silicon, which can be written as [27]:  $Q_{GS} = (\varepsilon_i / \delta)(\phi_P - (\chi - V_{bi} + V_n)) - \sqrt{2q\varepsilon_s N_d}(V_{bi} - kT/q)$  where  $\phi_P$  is work function of PEDOT:PSS and  $\chi$  is electron affinity of silicon. By using the values in table III, we can obtain  $D_s$  for the devices before  $(3.03 \times 10^{14} \text{ eV}^{-1} \text{ cm}^{-2})$  and after  $(3.97 \times 10^{13} \text{ eV}^{-1} \text{ cm}^{-2})$ treatment as shown by the two curves (black star and red cross, respectively) in Fig 5. By locating these  $D_s$  and n values in Fig.5,  $D_{s,P}$  and  $D_{s,Si}$  are  $1.1 \times 10^{14}$  and  $1.9 \times 10^{14} \text{ eV}^{-1} \text{ cm}^{-2}$  for pristine devices respectively and  $1.2 \times 10^{13}$  and  $2.7 \times 10^{13} \text{ eV}^{-1} \text{ cm}^{-2}$  for formic acid treated devices respectively. Both  $D_{s,P}$  and  $D_{s,Si}$  decreased after formic acid treatment. The decrease of the total interface states after formic acid treatment would result in a larger built-in voltage and barrier height  $(V_{bi}+V_n)$ . The larger barrier height suggested that recombination in the junction between the treated PEDOT:PSS and silicon could be smaller than the pristine PEDOT:PSS and silicon, thus the overall power conversion efficiency was increased.

# 4. Conclusions

The improvement of the PCE of SiNWs/ PEDOT:PSS hybrid solar cells by formic acid treatment is investigated. The average performance of devices shows a significant enhancement from 8% to 9.22%. The four point probe measurement showed the conductivity of the PEDOT:PSS film after formic acid treatment is increased by two times. Moreover, the XPS results indicates that the formic acid treatment led to larger PEDOT domain in the PEDOT:PSS film which can benefit the charge separation and collection. We further

confirmed the improvement of the PCE by evaluating the density of interface states in the pre-treated and after treated devices. We noticed that one order of magnitude drop in the interface states would occur for the device after formic acid treatment. These findings not only quantify the interface states level in the hybrid solar cell, but also provide direct evidences that formic acid treatment is very suitable to improve the efficient of SiNWs/PEDOT:PSS hybrid solar cell.

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TABLE I. The average photovoltaic performances of the SiNWs /PEDOT:PSS hybrid solar cells with and without formic acid treatment.

Treatment	V <sub>oc</sub> (V)	$J_{sc}$ (mA/cm <sup>2</sup> )	FF (%)	PCE (%)
Pristine	0.491	29.22	55.77	8.00
FA treated	0.527	29.14	60.13	9.22

TABLE II. The average values of the characteristic parameters of the SiNWs /PEDOT:PSS hybrid solar cells with and without formic acid treatment calculated by equation (1).

Treatment	$J_0$ (A/cm <sup>2</sup> )	п	$R_s$ ( $\Omega$ cm <sup>2</sup> )	$R_{sh}$ ( $\Omega \mathrm{cm}^2$ )
Pristine	2.59 x10 <sup>-5</sup>	2.56	1.77	5.37x10 <sup>5</sup>
FA treated	2.56 x10 <sup>-6</sup>	2.11	1.53	2.24x10 <sup>5</sup>

Table III. Values for the calculation.

Items	Symbol	Values
Band gap of silicon	$E_g$	1.1 eV

Work function of PEDOT:PSS	$\phi_P$	5 eV
Electron affinity of silicon	χ	4.05 eV
Energy at charges neutrality level [32]	<b>ф</b> СNL	0.4 eV

- Fig. 1 (a) Schematic diagram of the SiNWs/PEDOT:PSS hybrid solar cell. (b) The transmittance curves of the pristine and formic acid (FA) treated film. (c) The current density-voltage (J-V) curves of totally 50 hybrid solar cells with and without FA treatment under AM1.5G 1000W/m<sup>2</sup> illumination.
- Fig. 2 The distribution of open circuit voltage (V<sub>oc</sub>), short circuit current (J<sub>sc</sub>), fill factor (FF) and power conversion efficiency (PCE) of the pristine and formic acid (FA) treated devices.
- Fig. 3 (a) XPS Sulphur 2p spectra of the PEDOT:PSS film showed the decrease of PSS to PEDOT composition ratio after FA treatment. AFM height and phase images of (b,d) pristine and (c,e) formic acid treated PEDOT:PSS film. The upper are the height images and the bottom are the phase images. All images are 1µm x 1µm size.
- Fig. 4 The x-intercept of the extrapolation of the 1/C<sup>2</sup>-V plot at 100kHz showed the fundamental built-in voltage at the SiNWs/PEDOT:PSS interface of the (a) pristine and (b) formic acid treated devices. (c) The relationship between ideality factor and the built-in voltage of the SiNWs/PEDOT:PSS hybrid solar cells.
- Fig. 5 The relationship between interface states partly equilibrium to PEDOT:PSS  $(D_{s,P})$  and those partly equilibrium to silicon  $(D_{s,Si})$  before and after formic acid treatment.
- Fig. 6 Energy band diagram of a SiNWs/PEDOT:PSS contact with an thin interfacial layer.