

Reversible transformation between bipolar memory switching and bidirectional threshold switching in 2D layered K-birnessite nanosheets

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KEYWORDS: 2D layered nanosheets; K-birnessite; resistive switching; memory switching; threshold switching

ABSTRACT

Birnessite-related manganese dioxides (MnO_2) have recently been studied owing to their diverse low-dimensional layered structures and potential applications in energy devices. The birnessite MnO_2 possesses a layered structure with edge-shared MnO_6 octahedra layer stacked with interlayer of cations. The unique layered structure may provide some distinct electrical properties for the 2D layered nanosheets. In this work, layered K-birnessite MnO_2 samples are synthesized by a hydrothermal method. The resistive switching (RS) devices based on single K-birnessite MnO_2 nanosheets are fabricated by transferring the nanosheets onto SiO_2/Si substrates through a facile and feasible method of mechanical exfoliation. The device exhibits nonvolatile memory switching (MS) behaviors with high current ON/OFF ratio of $\sim 2 \times 10^5$. And more importantly, reversible transformation between the nonvolatile MS and volatile threshold switching (TS) can be achieved in the single layered nanosheet through tuning the magnitude of compliance current (I_{cc}). To be more specific, a relatively high I_{cc} (1 mA) can trigger the non-volatile MS behaviors, while a relatively low I_{cc} ($\leq 100 \mu\text{A}$) can generate volatile TS characteristics. This work not only demonstrates the memristor based on single birnessite-related MnO_2 nanosheet, but also offers an insight into understanding the complex resistive switching types and relevant physical mechanisms of the 2D layered oxide nanosheets.

INTRODUCTION

Nanoscale materials may exhibit promising properties that are rarely present in their bulk counterparts.¹ A wide variety of nanomaterials have been synthesized and studied extensively in this century, from zero-dimensional (0D) nanoparticles and/or quantum dots, one-dimensional (1D) nanotubes and/or nanowires to two-dimensional (2D) layered nanosheets.^{2,3} Among them, 2D layered materials have drawn much attention ever since the discovery of graphene and extensively studied compounds of transition metal dichalcogenides (TMDCs).^{4,5} Beyond graphene and TMDCs, a series of 2D materials have been exfoliated from their bulk crystals and fabricated through physical and chemical methods.^{6,7} Manganese dioxide (MnO_2) is an important functional oxide owing to its structural diversity, amazing physical and chemical properties as well as wide-range applications in energy, catalysts, and biomedicines.^{8,9} Among them, birnessite-related manganese dioxide with A_xMnO_2 structure where A stands for H^+ or metal cation is emerged.¹⁰ The birnessite manganese dioxide has a layered structure that consists of edge-shared MnO_6 octahedra layer with cations and water molecules occupying the interlayer region.^{11,12} Such birnessite-typed manganese dioxides with 1D and 2D forms have been synthesized and characterized, aiming at their potential applications in rechargeable battery.^{13,14} Recently, 1D MnO_x nanorods showed memristive properties under moisture ambient, while the memristive behaviors vanished under dry ambient.¹⁵ Actually, some 1D and 2D materials have been widely used in recently developed memristive devices^{16,17} Generally, a memristor is a two-terminal resistive switching (RS) device with sandwiched structure and its resistance states depend on the external bias voltage. The memristor has received considerable attention towards promising non-volatile memory, logic device and computing applications. Memristors have been fabricated using a diverse range of 2D materials, from graphene,^{18,19} graphene oxide,²⁰ TMDCs,²¹⁻²⁴ III-VI

GaSe,²⁵ hexagonal boron nitride¹⁷ to black phosphorous.²⁶ In comparison with traditional thin film based RS devices, memristors based on the 2D layered nanosheets not only provide atomically thin platform for memory but also show relatively lower operating voltages, suggesting promising applications in energy-efficient memristor-based neural networks.^{22,27} Until now, 2D layered birnessite MnO₂ has been widely studied in energy devices of batteries and capacitors.^{9,28} However, there is no exploration on the memristors using the recently-developed 2D layered birnessite manganese dioxide. By considering the unique 2D layered structure, it is expected that such birnessite MnO₂ nanosheets could demonstrate some intriguing resistive switching properties. In this work, layered K-birnessite MnO₂ samples were synthesized by a hydrothermal method. The RS devices based on single K-birnessite MnO₂ nanosheets were fabricated by transferring the nanosheets onto SiO₂/Si substrates through mechanical exfoliation method. By considering it is difficult to achieve the 2D layered MnO₂ nanosheets through bottom-up techniques, our exfoliation method is a facile and feasible way to fabricate conceptual device. Here, Ag electrodes were prepared through photolithography technique to form lateral device. The nonvolatile memory switching (MS) behaviors can be observed in single K-birnessite MnO₂ nanosheet. Importantly, controllable transformation between the nonvolatile MS and volatile threshold switching (TS) can be achieved in such layered nanosheet through tuning external stimulations of compliance current (I_{cc}). This work would help one to study the complex RS types and understand the underlying physical mechanism based on the unique 2D layered oxide nanosheets.

EXPERIMENTAL SECTION

Material Synthesis: The K-birnessite MnO₂ nanosheets were synthesized by hydrothermal method. 1 mmol potassium permanganate was mixed well with 28 mL deionized water. Then,

0.6 mol potassium hydroxide was quickly added to the mixed solution and then the solution was stirred vigorously. After that, 2 mL manganese sulfate (1 mol/L) was added in the mixed solution. Since the addition of a large amount of potassium hydroxide could generate a large amount of heat during the dissolution process, the mixed solution was stirred for 2 h after all the materials were added. The cooled solution was poured in a 50 mL Teflon-lined autoclave which was sealed and placed in an oven for 175 °C for 48 h. Then, the autoclave was naturally cooled down to room temperature. The black reaction products were washed with distilled water and ethanol, and then stored in distilled water.

Device fabrication: 2D layered K-birnessite MnO₂ nanosheets were prepared and transferred onto SiO₂/Si substrate using micromechanical exfoliation by scotch-tape method. Firstly, a small piece of the prepared MnO₂ sample was put on a scotch tape. The tape was refolded, pressed and then unfolded, in order to achieve 2D layered MnO₂ nanosheets. Then, the nanosheets were transferred from the tape onto SiO₂ substrate. Subsequently, 2D layered MnO₂ nanosheets with the size from a few to dozens of micrometres can be achieved. Then, the electrode pattern was prepared by photolithography on the individual nanosheet. And Ag electrodes with the thickness of ~ 100 nm were prepared by using sputtering method. Then the photoresist and excess Ag were ultrasonically cleaned with acetone. Finally, the fabricated devices were dried at 100 °C.

Characterizations and measurements: The crystal structure of the prepared samples was characterized by the X-ray diffraction (XRD) technique with Cu K α ($\lambda = 1.540598 \text{ \AA}$) radiation (Smart Lab, Rigaku) in the 2θ range of $10^\circ \sim 80^\circ$. The microstructure of the samples was investigated by field emission scanning electron microscope (FESEM, JSM-7800F, Japan) and transmission electron microscope (TEM, JEM-2100, Japan). The surface morphology and thickness of the exfoliated K-birnessite MnO₂ nanosheets were characterized by atomic force

microscopy (AFM, DI Nanoscope 8). Raman microscope (532 nm, Witec Confocal Raman Microscope alpha 300 R) was used to perform Raman and photoluminescence (PL) characterizations. The X-ray photoelectron spectroscope (XPS) (Thermo Scientific ESCALAB 250Xi) was used for determining chemical environment and elemental analysis. All the RS measurements were performed by double channel Keithley 2636B SourceMeter with a four-probe station system. All measurements were carried out in air at room temperature.

RESULTS AND DISCUSSION

The K-birnessite MnO_2 possesses layered structure (Figure 1a) with the edge-shared octahedra layer vertically stacked with adjacent layer of some K cations (K^+) and water molecules. The Mn-O octahedron is formed by manganese at the center and oxygen at each vertex of the octahedron (inset of Figure 1a).^{29,30} In our experiments, the K-birnessite MnO_2 samples were synthesized by hydrothermal method through adding potassium hydroxide solution. The interlayer cations and water molecules maintain the layered structure of the MnO_2 , owing to the presence of interlayer electrostatic attraction and/or van der Waals forces.^{11,30,31} Figure 1b shows the FESEM image of the synthesized K-birnessite MnO_2 , indicating the nanosheet structure of the synthesized materials. Furthermore, XRD technique has been employed to characterize the crystal structure of K-birnessite MnO_2 , as shown in Figure 1c. Two sharp (001) and (002) peaks, located at $\sim 12.5^\circ$ and 25.2° , respectively, indicate excellent crystallinity and prove the existence of layered structure of the prepared materials. The standard card shows that the space group of the crystal is $C/2m$, and its lattice constants are $a = 5.1490 \text{ \AA}$, $b = 2.8430 \text{ \AA}$, $c = 7.1760 \text{ \AA}$, and $\beta = 100.7600^\circ$.³² The interplanar spacing of the crystal plane of (001) is about 7 \AA that corresponds to the lattice constant $c = 7.1760 \text{ \AA}$. There is a weak impurity peak located at $\sim 17.5^\circ$, which can be found to match well with the standard card PDF # 80-0328 of Mn_3O_4 .³³

During the synthesis process, a large amount of heat can be generated when potassium hydroxide is rapidly added into water, which could cause some manganese hydroxide ($\text{Mn}(\text{OH})_2$) to be oxidized into Mn_3O_4 .^{34,35} All the other observed diffraction peaks are consistent with the standard card PDF # 80-1098 K-birnessite MnO_2 ,³⁴ suggesting that the K-birnessite MnO_2 can be successfully synthesized by hydrothermal reaction. Figure 1d shows the Raman spectrum of the material with feature peaks located at 190, 270, 398, 468, 500, 572 and 630 cm^{-1} , respectively^{36,37}. Among them, the feature peak located at 630 cm^{-1} is resulted from the symmetric stretching vibration of ν_2 (Mn-O) in the MnO_6 octahedron. The peak located at 572 cm^{-1} can be attributed to the stretching vibration of ν_3 (Mn-O) the plane in the $[\text{MnO}_6]$ sheet. According to previous reports,^{38,39} the characteristic peak of 570~585 cm^{-1} is particularly intense in layered birnessite. Therefore, the strong feature peak located at 572 cm^{-1} suggests the layered structure of the K-birnessite MnO_2 . Figure 1e shows the XPS results of Mn, O and K element states. The binding energies of Mn 2p_{1/2} and Mn 2p_{3/2} are 653.8 eV and 642.1 eV, respectively. And the energy difference between the two Mn 2P of 11.7 eV proves that K-birnessite MnO_2 is of high purity and successful synthesis. Mn 2p_{3/2} can be subdivided into three peaks that are Mn^{4+} (~ 642.9 eV), Mn^{3+} (~ 641.89 eV) and Mn^{2+} (~ 640.7 eV).^{40,41} The existence of multiple oxidation states of Mn implies that the layered K-birnessite MnO_2 contains oxygen vacancies, which is confirmed in the high-resolution XPS spectrum of O 1s. The O 1s peak can be divided into three peaks with binding energies of ~ 532.2 eV, ~ 531.1 eV, and ~ 529.4 eV, respectively. Among them, the peak at 532.2 eV is the binding energy of H-O-H, corresponding to water molecules chemisorbed and physically adsorbed on the surface. The peak is found at 531.1 eV of the binding energy corresponded to oxygen vacancies, suggesting the oxygen defects formed during the synthesis of the layered K-birnessite MnO_2 . The peak is evident at 529.4 eV originated from the binding

energy of Mn-O, suggesting the formation of K-birnessite MnO₂.^{42,43} For K 2p, there are two spin sub-peaks including K 2p_{1/2} (~ 295.2 eV) and K 2p_{3/2} (~ 292.4 eV). The difference in the binding energy between the two sub-peaks is ~ 2.8 eV, which is consistent with previous reports.⁴⁰ The two sub-peaks can be fitted to K⁺ and K⁰, respectively.⁴⁴ All aforementioned characterizations demonstrate the successful synthesis of layered K-birnessite MnO₂ by the hydrothermal method.

In order to present the device from the 2D layers, the synthesized sample was transferred onto the Si/SiO₂ substrate to achieve individual K-birnessite MnO₂ nanosheet using scotch-tape by micromechanical exfoliation method. Thus, a series of characterizations have been carried out for the individual nanosheet. Figure 2a shows AFM image of the layered K-birnessite MnO₂ nanosheet after being transferred onto the Si/SiO₂ substrate. The top left illustration shows the optical image of the nanosheets, indicating that the prepared K-birnessite MnO₂ samples can be exfoliated into individual nanosheet. The AFM profile in the lower left illustration shows the exfoliated triangle thickness of ~ 25 nm.⁴⁵ Figure 2b shows bright-field TEM image of the K-birnessite MnO₂ nanosheet. The high-resolution TEM (HR-TEM) image shown at the upper right corner corresponds to the circled part in the main panel of Figure 2b. The lattice space is 2.53 Å, corresponding to (200) plane,^{34,46} which is consistent with the data (~ 2.5292 Å) confirmed by the diffraction peak of (200) in the XRD pattern (Figure 1c). The Raman spectrum of the individual K-birnessite MnO₂ nanosheet (Figure 2c) shows six characteristic peaks in total at ~ 277, 401, 468, 504, 574 and 640 cm⁻¹, respectively.³⁷ The peak located at ~ 520 cm⁻¹ is typically originated from silicon wafer. The six feature peaks are consistent with the prepared bulk K-birnessite MnO₂ samples, indicating that the individual nanosheet has been successfully transferred onto the substrate. Figure 2d shows PL spectrum of the K-birnessite MnO₂ nanosheet. The emission

peak sites at ~ 665 nm, suggesting the band gap energy of ~ 1.86 eV.⁴⁷ All the characterizations of the individual nanosheets suggest that the K-birnessite MnO_2 sample prepared by hydrothermal method can be mechanically exfoliated and transferred onto target substrates for the following device fabrication.

Then the RS device was prepared with lateral structure by using Ag as the two terminal electrodes through photolithography technique, as shown in Figure 3a of the device schematics. Figure 3b shows an optical image of the device with planar structure of Ag/ MnO_2 /Ag on Si/SiO₂ substrate. The fabricated RS device with the channel length of ~ 4 μm based on the 2D nanosheet with the thickness of ~ 23 nm. Figure 3c shows the typical current–voltage (I – V) characteristics of the individual K-birnessite MnO_2 nanosheets in linear scale. It is known that the RS behaviors really depend on the device’s processing. As shown in the inset of Figure 3c, no obvious RS loop can be observed in the initial I – V curve with the maximum bias voltage of 3 V. After several cycles, the RS behaviors can be detected, suggesting the forming process. In the typical RS I – V curve, as shown in Figure 3d, the high-resistance state (HRS) is suddenly changed to the low-resistance state (LRS) at ~ 0.60 V in the device, which means the “SET” process. The LRS can be retained in the bias range from 3 to 0 V. Then, in the negative scanning of 0 V to -3 V, the LRS is transferred to HRS at -0.24 V, suggesting a “RESET” process. The HRS can be maintained until a positive voltage is applied. These results confirm that the fabricated device is non-volatile with the ON/OFF current ratio up to $\sim 2 \times 10^5$. In the whole measurement, the I_{cc} of 1 mA was applied to avoid destroying the device. It should be noted that the prepared 2D layered nanosheets possess similar thickness in the range of 20 to 30 nm with the triangle size between a few and dozens of microns in our experiments. The fabricated RS device shows comparable RS properties based on the MnO_2 nanosheets. To further understand the RS behaviors, the inset of

Figure 3d shows the enlarged graph in the positive voltage part of the $I-V$ curve in double logarithmic coordinate axis. The initial state of the device is a HRS, and the slope is ~ 0.3 when a low voltage is applied to the device in the HRS. As the applied voltage increases, the fitting slope increases to ~ 2.07 . This observation can be understood by the typical space charge limited current (SCLC) mechanism.⁴⁸ For the LRS, the slope is ~ 1.14 , suggesting the Ohmic contact behaviors, which are consistent with the conducting filament (CF) model.⁴⁹ In the K-birnessite MnO_2 nanosheets, there are a large number of oxygen vacancies according to the XPS characterization. The oxygen vacancies may migrate under the bias voltage and form the CFs, giving rise to the observed LRS. Then, a reverse voltage can induce the reverse migration of oxygen vacancies and then the breakdown of CFs, inducing the subsequent HRS.

Then, the retention and endurance properties of the memristors were characterized at room temperature. The resistances of both HRS and LRS are recorded by the pulse voltages with the magnitude of 0.2 V, the pulse duration of 500 ms and the pulse duration of 5 s. Figure 4a shows the retention characteristics of the memristor. At the initial stage, there are some fluctuations for the LRS. Then the LRS shows a gradual decrease as time elapsed. While, the HRS remains almost unchanged during the entire period of measurement. The memristor shows good retention behaviors with the ON/OFF ratio up to 10^4 during the retention time up to 3.5×10^4 s. Figure 4b shows the endurance characteristics of the memory device under pulse conditions. The device is tested under the repetitive pulses with duration of 500 ms and magnitude of 3 V for SET and -3 V for RESET. The HRS and LRS can be maintained up to 800 cycles without overlap between them. There is little fluctuation in both LRS and HRS. However, a relatively high ON/OFF ratio close to 10^4 can be achieved. Thus, we successfully prepared a non-volatile memory based on

individual K-birnessite MnO₂ nanosheet with the good retention and endurance behaviors, which shows good reproducibility and reliability of the memristors for applications.

All aforementioned RS behaviors were measured with the I_{cc} of 1 mA. It is intriguing that the nonvolatile MS behaviors can be changed into the volatile TS behaviors by decreasing the I_{cc} . Figure 5a shows the TS behaviors with I_{cc} of 100 μ A under the work voltages of ± 5 V in the scan sequence of 0 V \rightarrow 5 V \rightarrow 0 V \rightarrow -5 V \rightarrow 0 V. The SET and RESET processes occur at the same polarity voltages, at ~ 3 V (-3 V) and ~ 0.40 V (-0.35 V), respectively. For the scanning in the positive side, the initial HRS can be switched into the LRS at ~ 3 V, i.e., the SET process. While the LRS is not stable and reverted to the HRS at ~ 0.40 V, i.e., the RESET process. For the scanning in the negative side, the device exhibits nearly symmetric RS loop to that of the positive scanning, indicating that the device has bidirectional TS characteristics. Importantly, the current ON/OFF ratio still can reach up to nearly 10^5 , which is favorable for the peripheral circuit to recognize the existence state of the device. Next, as shown in the Figure 5b, we continue to decrease the I_{cc} to 10 μ A, the device shows similar TS behaviors to those of the 100 μ A. Furthermore, the nonvolatile MS behaviors also can be reverted when the I_{cc} is increased to 1 mA. Thus, the I_{cc} can effectively tune the RS behaviors between MS and TS in the K-birnessite MnO₂ nanosheets.

Generally, there are two modes of RS including nonvolatile MS and volatile TS. Both the LRS and HRS can be retained after removing the bias voltage in the MS mode. On the other hand, the LRS will return to the HRS when the bias voltage is decreased to a threshold value in the TS mode.⁵⁰ The former MS shows promising applications in data storage and the recently developed multifunctional synaptic neurons, while the latter TS demonstrates potential applications in the suppression of crosstalk current in the crossbar array.⁵¹ The controllable transformation between

TS and MS can generally be achieved in a single RS device when suitable external stimulation is applied.⁵¹⁻⁵³ Typically, the transition can be achieved by setting different I_{cc} . Thus, in electronic and optoelectronic applications, the device can work in both MS and TS modes and be used as a memory or a selector through tuning the compliance current levels. Such devices with multi-functions would be helpful for simplifying the integrated process and technology. In our experiments, a relatively high I_{cc} (1 mA) can trigger the non-volatile MS behaviors, while a relatively low I_{cc} ($\leq 100 \mu\text{A}$) can generate volatile TS characteristics. When the device is at a relatively low I_{cc} , some weak conductive filaments (CFs) can be formed in the MnO_2 nanosheets, inducing the LRS. However, the generated CFs are unstable. And such CFs can be spontaneously dissolved due to Joule heat induced by the electric current, resulting the recovery of the HRS when the applied voltage is decreased to zero under the identical polarity. Thus, the rupture of the unstable CFs leads to the TS behaviors in the 2D layered K-birnessite MnO_2 nanosheets. For MS behaviors, the relatively high I_{cc} can contribute to the formation of the stable CFs, which cannot be damaged by the Joule heat generated by the electric current. Thus, the breakdown of the stable CFs needs reverse and large voltage, giving rise to the MS behaviors in K-birnessite MnO_2 nanosheets. Therefore, through setting different I_{cc} , reversible transition between nonvolatile MS and bidirectional TS can be experimentally achieved in K-birnessite MnO_2 nanosheets.

CONCLUSIONS

In summary, we prepared memristors based on single K-birnessite MnO_2 nanosheets with Ag electrodes. The device exhibits nonvolatile MS behaviors with high current ON/OFF ratio of $\sim 2 \times 10^5$, long retention time up to 3.5×10^4 s, and good endurance of 800 cycles. It is intriguing that the nonvolatile MS behaviors can be changed into the volatile TS behaviors by decreasing the I_{cc} .

And more importantly, the nonvolatile MS behaviors can be reverted when the I_{cc} is increased to 1 mA. Thus, reversible transition between the nonvolatile MS and volatile TS can be achieved in the single layered K-birnessite MnO_2 nanosheets through tuning the external stimulations of the I_{cc} . A relatively high I_{cc} (1 mA) can trigger the non-volatile MS behaviors, while a relatively low I_{cc} ($\leq 100 \mu A$) can generate volatile TS characteristics. The transformation from MS to TS can be ascribed to the spontaneous breakdown of weak CFs formed at low I_{cc} . This work would provide guidelines for complex RS types based on 2D oxide nanosheets and help one to understand the underlying physical mechanism of different RS types in the 2D platform.

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Notes

The authors declare no competing financial interest.

ACKNOWLEDGMENT

This work was supported by the grants from National Natural Science Foundation of China (No. 61804023, No. 61971108, and No. 61974097), the Open Foundation of State Key Laboratory of Electronic Thin Films and Integrated Devices (KFJJ201805), the Open Foundation of National

Engineering Research Center of Electromagnetic Radiation Control Materials (ZYGX2019K003-1), Sichuan Youth Science and Technology Foundation (19JCQN0106), Key R&D Program of Sichuan Province (No. 2018GZ0527) and Hong Kong RGC GRF (PolyU 153033/17P).

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Figures

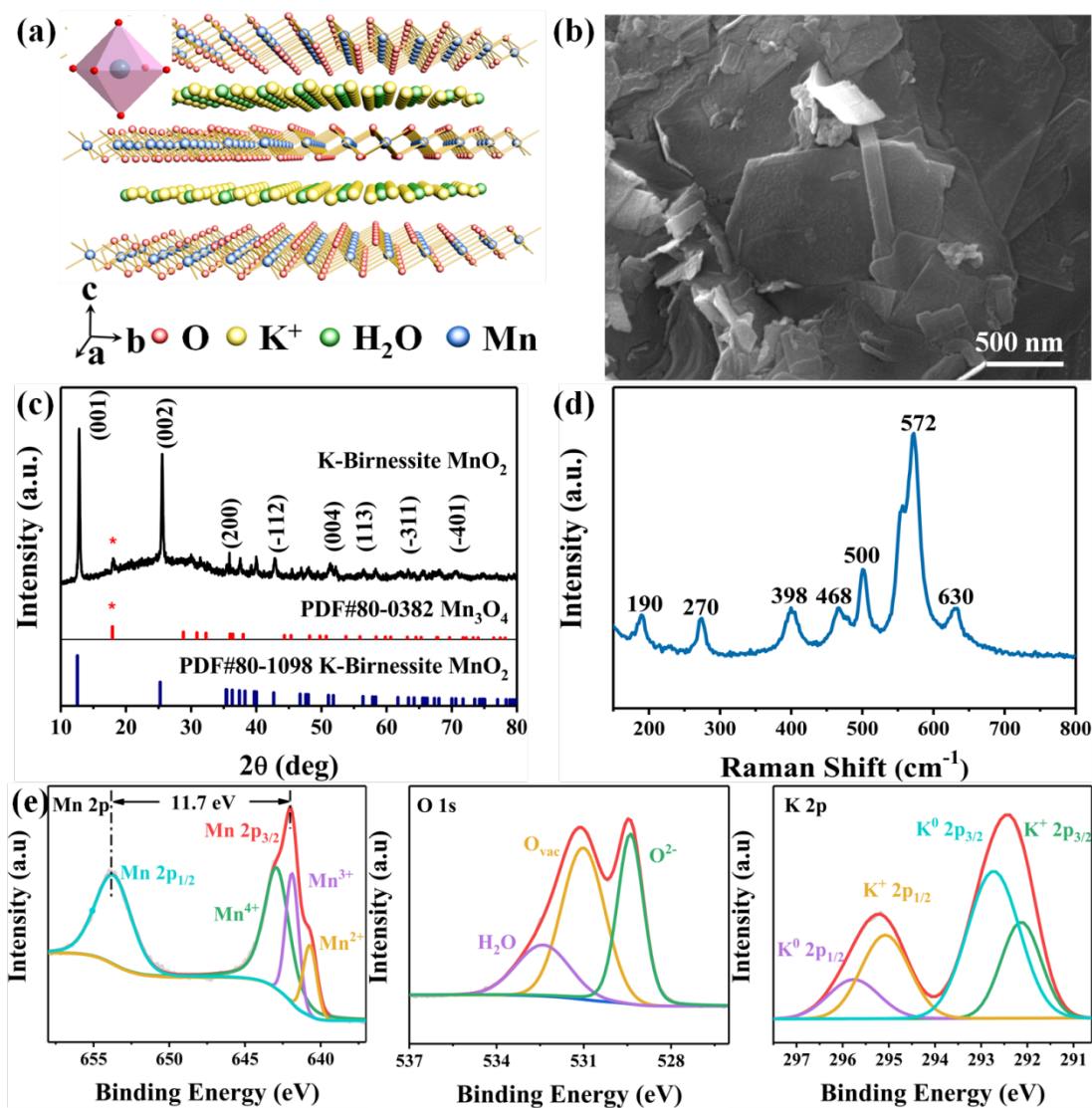


Figure 1. Characterization of bulk sample of layered K-birnessite MnO₂. (a) Schematic of atomic structure. Inset shows the MnO₆ octahedron. (b) Field emission scanning electron microscope image. (c) X-ray diffraction pattern. (d) Raman spectrum. (e) X-ray photoelectron spectroscopy spectra of Mn, O and K element, respectively.

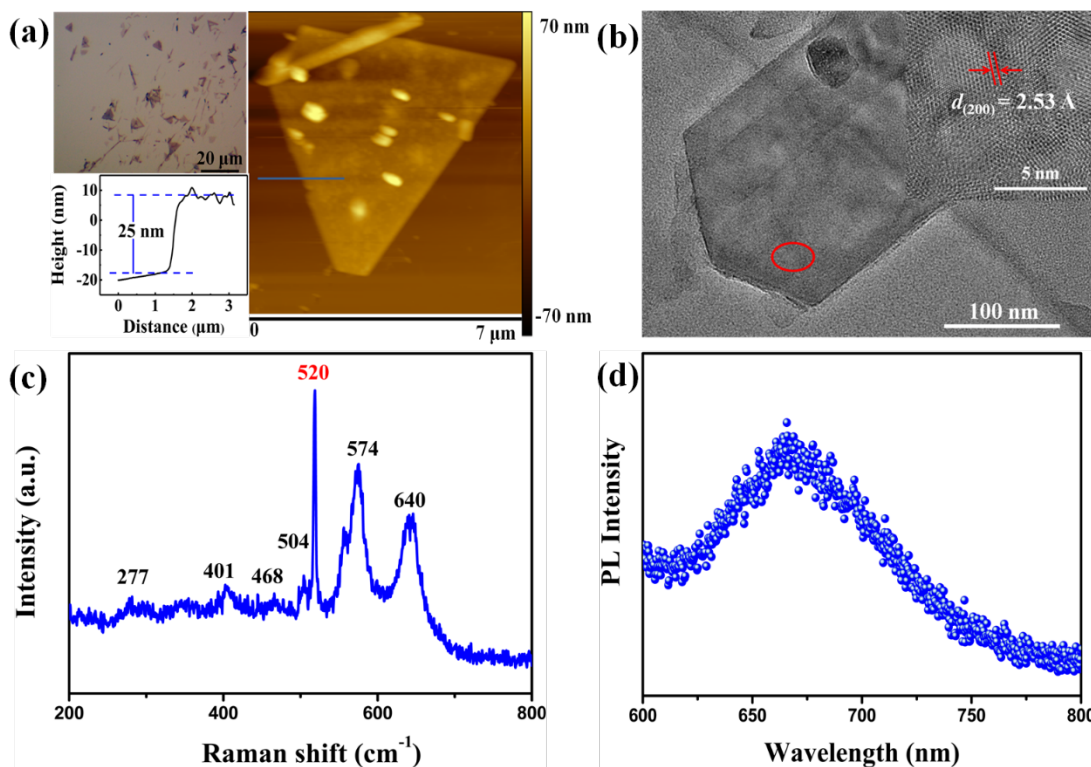


Figure 2. Characterization of the individual K-birnessite MnO₂ nanosheets. (a) The atomic force microscope image the layered K-birnessite MnO₂ nanosheets transferred onto the Si/SiO₂ substrate. Upper left illustration: optical image. Lower left illustration: height profile corresponding to the blue line in the AFM image. (b) The bright-field transmission electron microscope image of the nanosheet materials transferred onto the copper foil. The upper right corner shows the high-resolution TEM image. (c) Raman spectrum of the nanosheets transferred onto the Si/SiO₂ substrate. (d) Photoluminescence spectrum of the nanosheets transferred onto the Si/SiO₂ substrate.

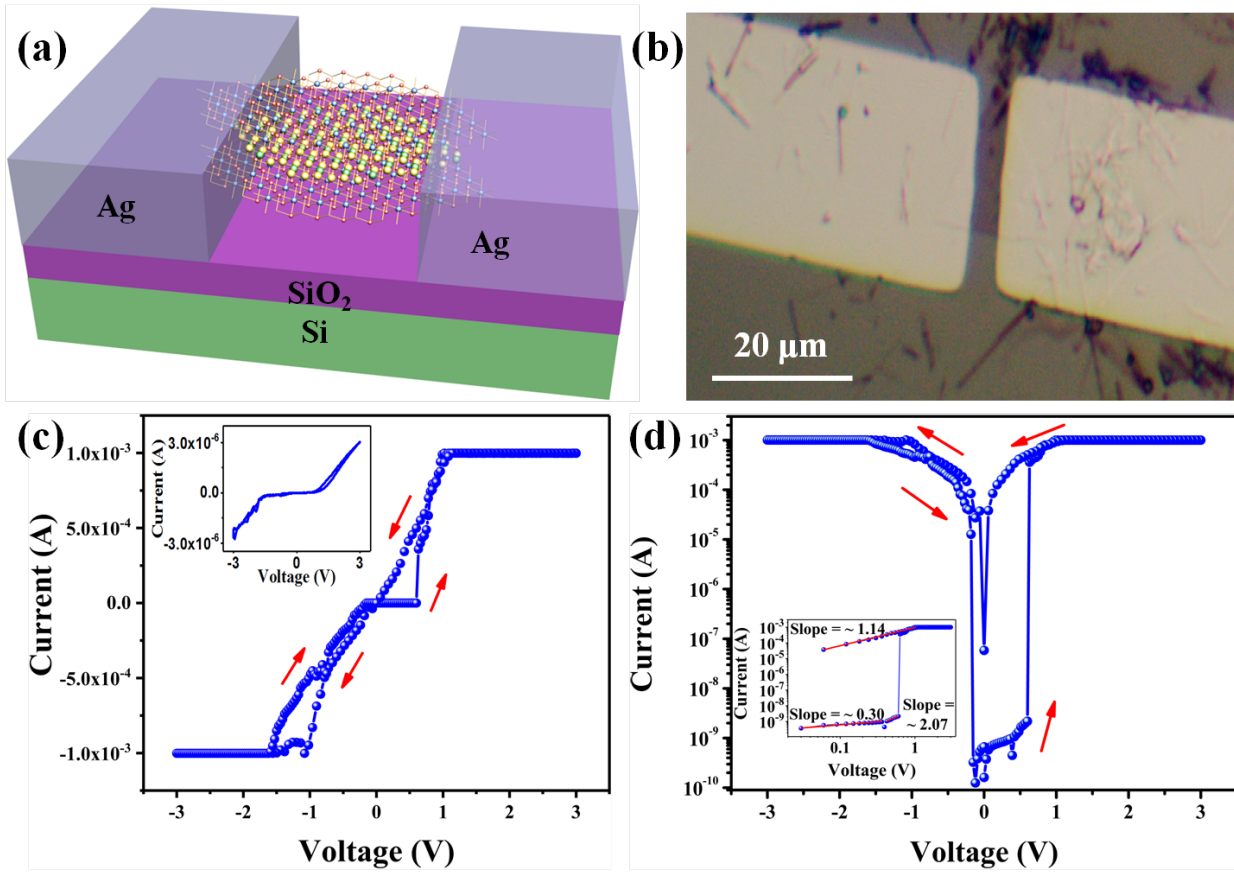


Figure 3. Measurements of the RS behaviors of the prepared of K-birnessite MnO_2 nanosheets. (a) Schematics and (b) optical image of the memristor based on individual K-birnessite MnO_2 nanosheet with Ag electrodes on Si/SiO₂ substrate. (c) Typical I - V curve of K-birnessite MnO_2 in linear scale. Inset: the initial I - V curve of the device before the forming process. (d) Typical I - V curve of K-birnessite MnO_2 with current in logarithmic scale. Inset is the re-plotted positive voltage part of the I - V curve in double logarithmic coordinate axis and linear fitting of the curve.

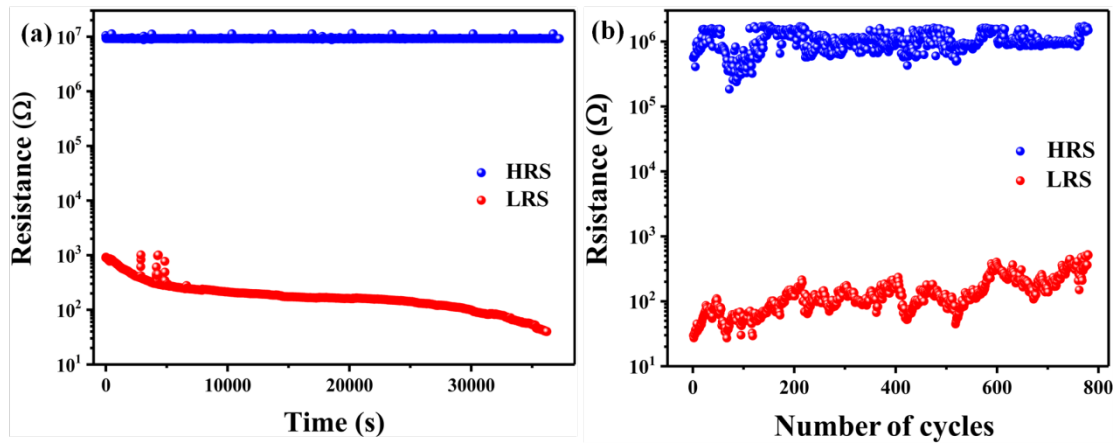


Figure 4. (a) Retention characteristics of memristor based on the individual K-birnessite MnO₂ nanosheets memory device with the compliance current of 1 mA at the voltage of 0.2 V. (b) Characterization of the cycle endurance for the memristor in the HRS and LRS.

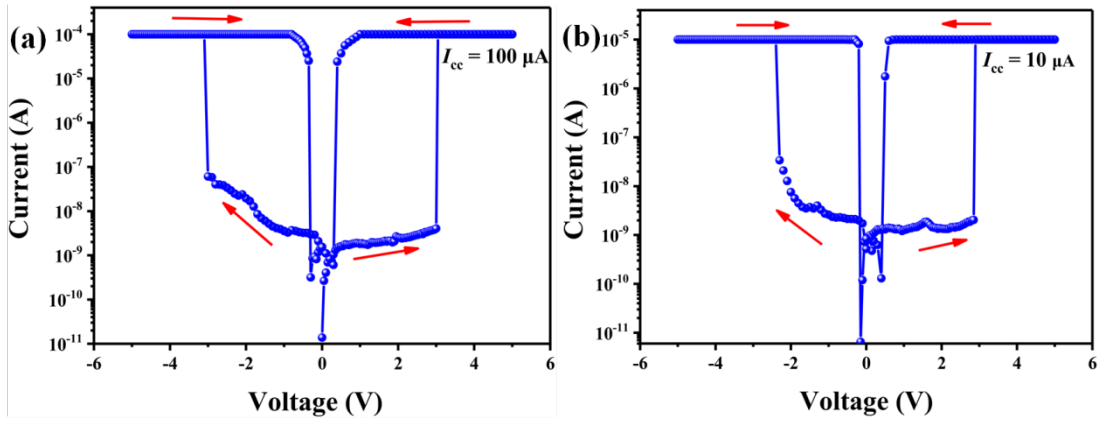


Figure 5. The TS behaviors of the prepared of K-birnessite MnO₂ nanosheets with different compliance current. (a) $I_{cc} = 100 \mu\text{A}$. (b) $I_{cc} = 10 \mu\text{A}$.

Nonvolatile memory switching ←→ Volatile threshold switching

