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Electrical properties of ferroelectric $BaTiO_3$ thin film on $SrTiO_3$ buffered GaAs by laser molecular beam epitaxy

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Ferroelectric BaTiO₃ thin films were epitaxially grown on (001) GaAs substrate using SrTiO₃ as a buffer layer by laser molecular beam epitaxy. The perovskite SrTiO₃ buffer layer present a body centered cubic structure by formation of an interfacial layer with the [100]SrTiO₃||[110]GaAs in-plane relationship. Thereupon, a highly *c*-oriented BaTiO₃ thin film was grown on SrTiO₃/GaAs in a layer by layer mode. Enhanced electrical properties of the heteroepitaxial structure were demonstrated. The BaTiO₃(150 nm)/SrTiO₃/GaAs system demonstrates hysteresis loops with a remnant polarization of 2.5 μ C/cm² at 600 kV/cm and a small leakage current density of 2.9 $\times 10^{-7}$ A/cm² at 200 kV/cm. © 2009 American Institute of Physics. [DOI: 10.1063/1.3075955]

III-V semiconductors such as GaAs have direct band gap and higher saturated electron mobility compared to Si. Great efforts have been made to develop GaAs-based metalferroelectric-insluator-semiconductor (MFIS) functional heterostructure.¹⁻³ Much research has been focused on the electrical properties of amorphous and polycrystalline MFIS structure via a buffered insulator layer.⁴⁻⁶ The epitaxial growth of perovskite titanate thin films on Si has been studied extensively in the last decades.^{7–9} Integration with GaAs has attracted much less attention. Murphy et al.⁵ reported the ferroelectric properties in epitaxial grown BaTiO₃ (BTO) on GaAs via MgO buffer and found that these properties are greatly sensitive to the interface structure and buffer thickness. Chen et al.⁶ reported that optical waveguiding was observed in BTO/MgO/Al_xO_y/GaAs structure showing primary optical confinement in the BTO thin film. Recent study showed that the MgO with a thickness of a few nanometers on GaAs may suffer from the high charge injection while increasing reverse bias voltage due to a MgO/GaAs interface current effect.¹⁰ Ferroelectric oxides on MgO buffered GaAs exhibits weak ferroelectric properties with a large leakage.⁵ Compared with MgO, SrTiO₃ (STO) with a cubic perovskite structure has a relatively large dielectric constant and low loss.¹¹ STO has also widely been used as a suitable interface template for the growth of high-quality functional oxides.^{12–14} So far, the epitaxial thin film of STO grown on GaAs has only been achieved via a complex molecular beam epitaxy (MBE) with a submonolayer of titanium as buffer.¹⁵ In this work, we employ O₂ flowing laser MBE technique to grow epitaxial BTO thin films on STO/GaAs.

The BTO/STO/GaAs (001) heterostructure was grown by O₂ flowing pulsed laser MBE system with an operation wavelength of KrF (λ =248 nm). The laser energy density was about 6 J/cm² at a frequency of 1 Hz. A system of reflective high energy electron diffraction (RHEED) was used to monitor the film growth process. As our recent report,¹⁶ STO layer was first deposited on GaAs (001) substrates at 600 °C. During the deposition, the chamber was evacuated to a base pressure of 5×10^{-5} Pa to avoid

oxidation of the GaAs substrate, although the GaAs surface reacts less severely with oxygen in comparison with Si. Then, the buffered GaAs was heated to 650 °C and the subsequent ferroelectric BTO film was grown under a flowing O_2 atmosphere of 1 Pa. The deposited films were then *in situ* annealed in ambient of high oxygen pressure before being cooled down to room temperature. Finally, Pt electrodes were deposited for electrical measurements. P-type GaAs was used as a bottom electrode in our measurement. The resistivity of used GaAs wafer was estimated to be $2-6 \ \Omega$ cm. The film structure was determined by x-ray diffraction (XRD) and the interface characteristic was investigated by transmission electron microscopy (TEM). The dependence of current density on electrical field (J-E) of the system was measured. The ferroelectric properties were measured by using a ferroelectric test system (Radiant Inc.).

The RHEED patterns (Fig. 1) show the epitaxial features of BTO films grown on GaAs substrates using STO as a buffer layer. The diffraction spots of GaAs representing (002) plane cluster in Fig. 1(a) gradually faded away as the STO started to grow. Figure 1(b) shows the diffraction pattern with two unit cell of STO (about 8 Å), indicating the film was coherent at the onset of the STO growth. Then two elongated bright spots emerging with a wider separation



FIG. 1. RHEED patterns for heteroepitaxial growth of BTO thin films on GaAs via STO buffer layer at various thicknesses: (a) 0 Å STO, (b) 8 Å STO, (c) 30 Å STO, and (d) 200 Å BTO.

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FIG. 2. XRD pattern of the heteroepitaxial growth BTO films on STO buffered GaAs (001). The inset shows the rocking curve of BTO (002).

could be observed. According to Bragg's diffraction law, STO unit began to grow with a 45° in-plane rotation on GaAs to get a comfortable lattice match due to a large mismatch between STO(a=3.905 Å) and GaAs(a=5.65 Å), which is consistent with the observation in Ref. 15. The spotty diffraction pattern in Fig. 1(b) also suggests that STO possibly grew in coherent two-dimensional-island behavior at the STO/GaAs interface, which may be due to the large number of dislocations due to mismatch strain between STO and GaAs. This coherent behavior remained with film thickness of up to approximately 30 Å as shown in Fig. 1(c), then the spot gradually faded away when the thickness was greater than 30 Å, suggesting that the STO film began to relax when the growth went beyond this thickness. For the subsequent growth of BTO film on STO buffered GaAs, conspicuous streaky diffraction patterns of the (001) plane of BTO could be observed in Fig. 1(d). This unchanged streaky diffraction pattern indicates that the growth of BTO becomes a two dimensional layer-by-layer growth on STO buffered GaAs. It also suggests that the smoothness and crystallinity of BTO film remained satisfactory via a STO interfacial layer. During the whole growth process, no unidentifiable RHEED patterns were detected.

Figure 2 shows a typical θ -2 θ XRD scan of the BTO/ STO/GaAs heterostructure. Only the (00l) peaks of the BTO appear in the diffraction patterns indicating that the BTO is basically c-axis oriented growth on STO buffered GaAs substrate. There is no apparent peak corresponding to STO in Fig. 2 due to the small thickness of STO layer. To further investigate the degree of in-plane orientations, Φ scan was performed (not shown here), which reveals that the BTO film is epitaxially grown on the (001) surface of STO and has an in-plane film-substrate orientation relationship of [100]BTO||[100]STO||[110]GaAs. The BTO film on STO buffered GaAs has good single crystallinity, which is evident from the small full width at half maximum value of the rocking curve of (002) BTO (seen in inset of Fig. 2). In addition, the lattice constant c of BTO was then calculated to be 4.125 Å, which is slightly larger than that of bulk BTO (c=4.01 Å) from powder diffraction. It suggests that the BTO film is slightly compressively strained by the underlying STO buffered layer (a=3.903 Å) and the interface of STO buffer layer considerably affects the BTO crystallographic structure. The cross-sectional TEM images in Fig. 3



FIG. 3. Cross-sectional TEM image of the BTO/STO/GaAs heterostructure.

reveal a sharp interface structure of the heterostructure. It can be seen that the BTO film with uniform thickness of 150 nm has grown columnarly on a 10-nm-thick STO buffer layer.

In order to evaluate electrical properties of the BTO/ STO/GaAs heterostructure for applications, the leakage current and polarization properties were measured as a function of electrical field. As shown in Fig. 4(a), the leakage current density J of the BTO/STO/GaAs heterostructure is considerably reduced by inserting STO buffer layer under a given electric field. For instance, the value of current density is 2.9×10^{-7} A/cm² for BTO thin films deposited on a 10 nm STO buffer layer at a field of 200 kV/cm, almost two orders of magnitude lower than that $(1.7 \times 10^{-5} \text{ A/cm}^2)$ for BTO thin films without using a STO buffer layer under the same field. However, obvious asymmetry was observed in the plot of the leakage current versus electric field. This feature may be attributed to interface states caused by stress-induced dis-



FIG. 4. Electrical and ferroelectric properties of the BTO/STO/GaAs heterostructure. (a) J-E property. (b) P-E loop.

location, which has also been reported in some metal-oxidesemiconductor structures.¹⁷ It is known that the microstructure in the vicinity of the film/substrate interface plays an important role in defining the leakage current in films. As an excellent isolating material, the STO buffer layer can effectively suppress the interdiffusion between BTO and GaAs. The P-E characteristics for varying applied electrical field are shown in the Fig. 4(b). Compared with previous reports on MgO buffered BTO film on GaAs,⁵ the BTO/STO/GaAs heterostructure exhibits a more saturating P-E loop with a remnant polarization (P_r) of about 2.5 μ C/cm² at the applied electric field of 600 kV/cm. Although the measured P-E loop windows are still smaller than the theoretical calculated values, the charge injection was significantly suppressed and the dielectric behavior is therefore enhanced. This is somewhat similar to the improved property in MFIS structures, such as BFO/ZrO/Si, SrBi₂Ta₂O₉/HfTaO/Si.^{18,19} The high structure-dependent films always show good performances by inducing a good interfacial layer. Therefore, the P-E loop with a small leakage current can also be considered and that the interface between BTO and GaAs is improved by inserting STO as buffer layer, and thus a good epitaxy of BTO layer exhibits enhanced electrical properties.

In conclusion, a technique of O_2 flowing laser MBE was used to grow heteroepitaxial structure of BTO/STO/GaAs. We found that structural and electrical properties of the heterostructure are greatly improved. The BTO/STO/GaAs prepared by laser MBE can serve as a model system of dielectric materials integrated with III–V semiconductors for a variety of applications.

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