

A Scanning Microwave Impedance Microscopy Study of α - In_2Se_3 Ferroelectric Semiconductor

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Van der Waals ferroelectric semiconductors, which encompass both ferroelectricity and semiconductivity, have garnered intensive research interests for developing novel non-volatile functional devices. Previous studies focus on ferroelectricity characterization and device demonstration, with little attention paid to the fundamental electronic properties of these materials and their functional structures, which are essential for both device design and optimization. In this study, scanning microwave impedance microscopy (sMIM) is utilized to investigate the ferroelectric semiconductor of α -phase indium selenide (α - In_2Se_3) and its synaptic field effect transistors. α - In_2Se_3 nanoflakes of varying thicknesses are visualized through capacitive signal detection, whose responses are consistent with finite element simulations manifesting dependence on both flake thickness and its semiconductor property. sMIM spectroscopy performed on α - In_2Se_3 -based metal-oxide-semiconductor (MOS) structures reveals typical MOS capacitance-voltage characteristics, with additional hysteresis arising from the ferroelectric switching of α - In_2Se_3 . The local conductance state changes of synaptic α - In_2Se_3 ferroelectric semiconductor transistors (FeSFET) in response to gate voltage stimuli are effectively detected by in situ sMIM, in good agreement with electrical device transport properties. This work deepens the understanding of ferroelectric semiconductor physics toward their practical device application.

in-memory computing and neuromorphic electronics.^[1–8] Compared with oxides ferroelectric counterparts, the ability to retain ferroelectricity down to monolayer thickness addresses the issues related to critical thicknesses for ferroelectricity.^[9–12] Moreover, their semiconducting character makes possible the construction of ferroelectric semiconductor field effect transistors (FeSFETs), where the layered ferroelectric semiconductor serves as channel material as opposed to gate dielectric, further alleviating the charge trapping and gate leakage current issues confronting the complex gate stack in conventional ferroelectric field effect transistors (FeFETs).^[13] α - In_2Se_3 is among the most widely studied vdW ferroelectric materials.^[14–19] FeSFETs based on α - In_2Se_3 have demonstrated large memory window and tremendous potential for ultrafast neuromorphic computing with superior controllability and suppressed cycle-to-cycle variation.^[13,17–18] Conventional studies are mainly focused on the ferroelectricity characterization of materials and electrical transport measurement of devices. Consequently, there is

less understanding on the switching behavior of a ferroelectric semiconductor, which can convolute both dipole switching and modulation of carrier density.^[19] Ferroelectric semiconductors possess distinct electronic properties from conventional wide-gap ferroelectrics or non-ferroelectric semiconductors. It is critical to gain a good understanding of their material physics for developing devices toward real application. Meanwhile, measurements of devices' electrical transport characteristics inherently render averaged response of the device over large area with little insight into its local electrical properties, and consequently are incapable of understanding the differences in electronic properties between devices caused by spatial inhomogeneities such as doping and structural defects.^[20–22] Derived from atomic force microscopy (AFM), scanning microwave impedance microscopy (sMIM) delivers a microwave signal at a few GHz to the tip apex to interact with the sample, and consequently probes its local electrical properties from analyzing the reflected microwave response.^[23] sMIM offers outstanding versatility to electrically image diverse samples covering insulators, semiconductors, conductors, and even buried structures.^[24–34]

In this work, we applied sMIM to characterize ferroelectric semiconductor α - In_2Se_3 nanoflakes and its metal-oxide-semiconductor (MOS) structures, and probe the evolution of

1. Introduction

The discovery of van der Waals (vdW) ferroelectric materials have stimulated considerable interests in ferroelectric-based

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channel conductance state of α -In₂Se₃-based FeSFETs in a non-invasive manner. The capacitive sMIM signal (sMIM-C) component decreases with thickness for α -In₂Se₃, and saturates for thickness beyond 100 nm, which reflects the thickness-dependent depletion situation of the flakes and is consistent with our finite element simulations. sMIM-C spectroscopy performed on α -In₂Se₃ MOS structure reveals typical MOS-like capacitance-voltage (*C-V*) characteristics that are distinct from conventional ferroelectrics, with additional hysteresis arising from ferroelectric switching of α -In₂Se₃. Furthermore, the in situ sMIM characterization of the high resistance state (HRS) and low resistance state (LRS) states of α -In₂Se₃ synaptic device correlates well with its electrical transport result, evidencing the capability of sMIM as a convenient technique in quantitatively characterizing local electrical properties of ferroelectric neuromorphic devices.

2. Results and Discussion

2.1. Ferroelectricity Characterization of α -In₂Se₃

α -In₂Se₃ can exhibit either rhombohedral or hexagonal crystal structures, referred to as 2H (*P6₃mc* space group) and 3R (*R3m* space group) phases, respectively. Both structures are non-centrosymmetric and possess coupled out-of-plane and in-plane ferroelectricity. 2H α -In₂Se₃ was used in this study (Figure 1a), as confirmed by the Raman spectrum, which reveals vibration peaks at 90, 104, 180, and 195 cm⁻¹ (Figure 1b; Figure S1; Table S1, Supporting Information). Piezoresponse force microscopy (PFM) was used to characterize the ferroelectric properties of α -In₂Se₃. In Figure 1d–f, distinct ferroelectric domains are disclosed on the exfoliated flake with contrary out-of-plane PFM responses, where the 180-degree difference in the PFM phase indicates opposite polarization directions. The polarization switching of α -In₂Se₃ is illustrated by the hysteresis loops of PFM phase and amplitude versus DC bias (Figure 1g,h). The PFM image (Figure 1i) reveals reversed PFM phase in the region undergoing a poling process, demonstrating the manipulation of α -In₂Se₃ polarization orientation. Notably, no topography changes are observed, indicating that the observed phenomenon is primarily due to polarization switching rather than other processes (Figure S2, Supporting Information).

2.2. sMIM Characterization of α -In₂Se₃ Nanoflakes

A schematic of the AFM-derived sMIM setup is illustrated in Figure 2a. During sMIM operation, the excitation microwave signal is transmitted to the tip apex to interact with the sample. The reflected microwave signal contains the information of the admittance/impedance of the tip-sample system, which depends on the local permittivity and conductivity of the sample of study. Consequently, the variation of the electronic properties results in changes of the reflected microwave signal, which is then detected by the radio frequency electronics module and processed into sMIM output, that is, real (sMIM-R) and imaginary (sMIM-C) components of the tip-sample impedance.

In this work, the changes in sMIM signal with the conductivity of α -In₂Se₃ film on different substrates (Si/Al₂O₃ and Au film)

are simulated through finite element analysis (FEA). As shown in Figure 2b (see also Figure S3, Supporting Information), the sMIM-C signal increases monotonically with the film conductivity, and saturates at the insulating and conductive limit, with the most sensitive region present between the two regimes. In contrast, the sMIM-R signal, being an indicator of the effective loss of the tip-sample interaction, approaches zero in the low and high conductivity limit of the sample, and peaks at an intermediate conductivity of 5 S m⁻¹.

We began by examining the ability of sMIM to detect α -In₂Se₃ flakes exfoliated on conductive substrates. Figure 2c,d shows the topography and sMIM images, respectively (see Figures S4 and S5, Supporting Information for more results). α -In₂Se₃ flakes of varying thicknesses are unambiguously detected and differentiated from the Au film in the sMIM-C channel. When the probe is in contact with Au film, the sMIM-C signal is the highest because the Au film serves as a ground plane terminating all the electric fields. In contrast, the α -In₂Se₃ flakes yield smaller sMIM-C signals due to their relatively low conductivity and their dielectric property (see Figure S6, Supporting Information). To derive a scaling relationship, the sMIM-C signals difference between Au substrate and α -In₂Se₃ nanosheets are extracted and plotted as a function of flake thickness. In Figure 2e, a quasi-inverse relationship of sMIM-C signal with thickness of α -In₂Se₃ is observed below the thickness of 100 nm (see Figure S7, Supporting Information). However, with further increased thickness, the sMIM-C signal deviates from the inverse relationship. Assuming a constant permittivity of α -In₂Se₃ for the investigated thickness range^[29] and a weak conductivity of the depleted region, this trend agrees well with our numerical FEA simulations. Particularly, when the α -In₂Se₃ flake becomes sufficiently thick, the free carriers within the flake are no longer fully depleted and the flake possesses a finite conductivity, leading to deviation from the thickness scaling relationship of the sMIM-C signal. These results demonstrate the importance of intrinsic semiconductor nature in understanding the material physics of vdW ferroelectrics.

2.3. sMIM Characterization of α -In₂Se₃ Domains and MOS Structures

Next, we provide evidence of the distinct sMIM responses exhibited by opposite α -In₂Se₃ domains. Figure 3a,b showcase the PFM phase and sMIM-C image, respectively, of an α -In₂Se₃ flake after undergoing a poling process (additional corresponding results can be found in Figures S9 and S10, Supporting Information). It is evident that the opposing ferroelectric domains yield contrasting sMIM-C responses, indicating different material conductivities resulting from polarization modulation. This observation reveals the fundamental principle underlying α -In₂Se₃ non-volatile FeSFETs. The line profile in Figure 3c demonstrates a conductance evolution width of 147 nm across the domain wall, primarily determined by the diameter of the tip apex, which is consistent with previous findings in the literature.^[30,35]

Before delving into the device study, we investigate the electronic properties (i.e., *C-V* characteristics) of α -In₂Se₃-based MOS structures, which constitute the essential functional structure of α -In₂Se₃ FeSFETs. To obtain the equivalent of the *C-V* plot, we record the sMIM signal on flakes exfoliated onto a

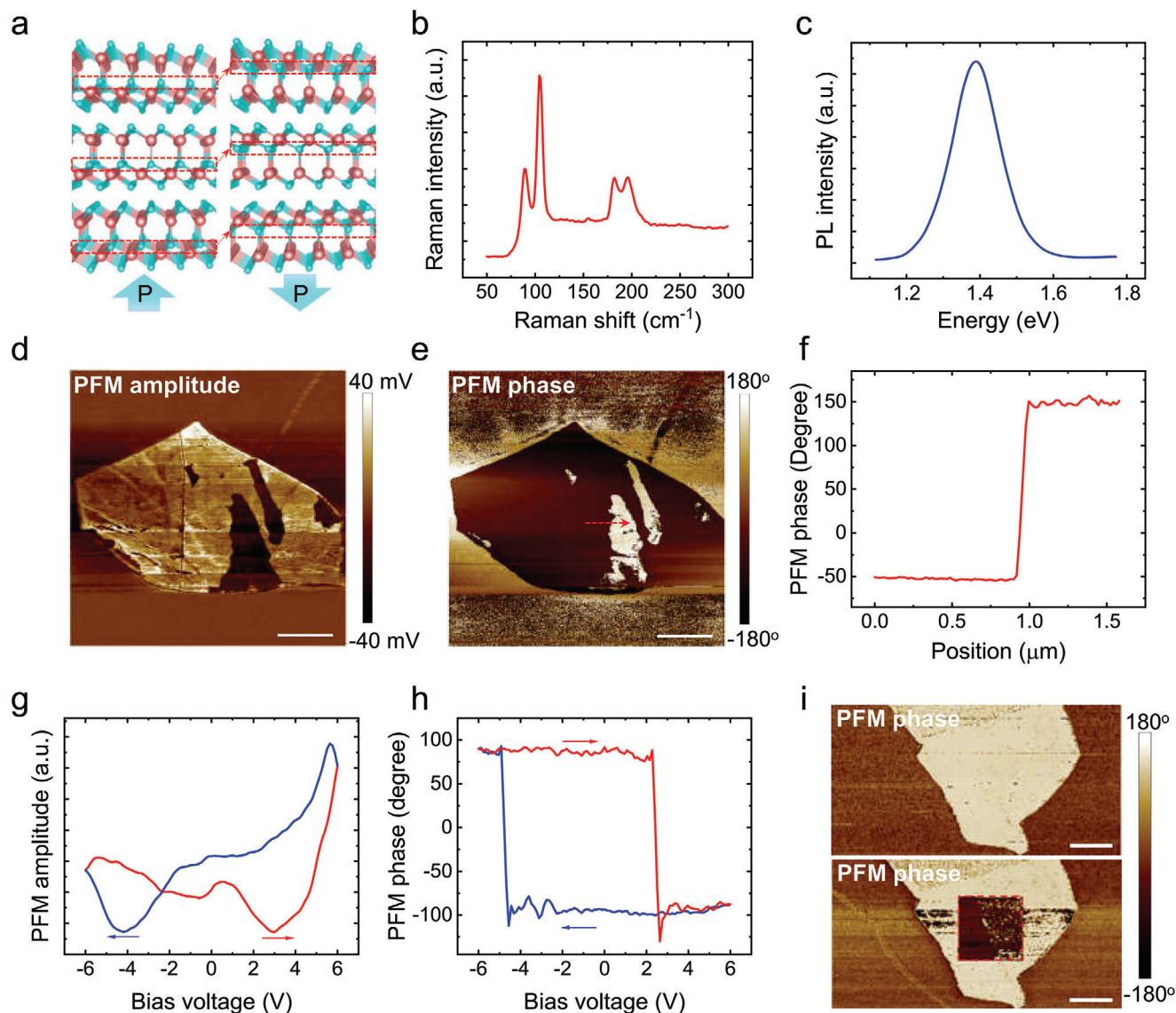


Figure 1. Material and ferroelectricity characterization of α - In_2Se_3 . a) Schematic atomic structure of 2H α - In_2Se_3 in its two distinct polarization states. The Se positions corresponding to two distinct polarization states are indicated by red dashed rectangles. b) Raman spectrum of α - In_2Se_3 under laser illumination of 532 nm wavelength peaking at 90, 104, 180, and 195 cm^{-1} , corresponding to E^2 , $A_1(\text{LO} + \text{TO})$, $A_1(\text{LO})$, and $A_1(\text{LO})$ phonon modes of α - In_2Se_3 , respectively. c) Photoluminescence spectrum of an α - In_2Se_3 flake under 633 nm excitation shows peak at 1.39 eV (wavelength of 892.7 nm), suggesting its semiconducting property. d, e) Representative out-of-plane PFM amplitude and phase images displaying opposite ferroelectric polarization within the scanned α - In_2Se_3 nanoflake. Scale bars are 2 μm . f) PFM phase profile along the dashed line indicated in (e) shows a 180-degree difference for opposite polarization orientation. g, h) Hysteretic behavior of out-of-plane PFM response as a function of tip bias voltage for a flake of 37 nm thickness, manifesting ferroelectric polarization switching. The flake is exfoliated onto a 50 nm thick Au film. An AC voltage of 1000 mV amplitude was used for excitation. i) PFM phase images before and after poling the center area by +8 V tip bias, giving rise to opposite PFM phases. Scale bars are 2 μm .

degenerately doped Si substrate as a function of sample bias. A 6 nm thick Al_2O_3 layer is deposited on the sample surface to form the MOS structure (Figure 3d). As shown in Figure 3e (also refer to Figure S11, Supporting Information), the α - In_2Se_3 -based structure exhibits a hysteretic sMIM-C spectrum, resembling the classic C-V properties of MOS structures, albeit with additional hysteresis. The reduced capacitive signal with decreasing DC bias correlates well with the n-type conduction of α - In_2Se_3 , indicating an increasing depletion of the channel resulting in decreased sample capacitance. The observed hysteresis is attributed to the

ferroelectric switching process. It is worth noting that interfacial charges in the MOS structure may also play a role in determining its C-V characteristics and the resultant device behaviors. However, careful analysis has shown that their effect is overshadowed by the polarization switching of the material.^[18]

Regarding the differences in switching voltage across different samples in PFM and sMIM, these variations can be attributed to factors such as flake thickness, inserted insulating layer, surface oxide layer, AC voltage, and even the type of probe used. For instance, the larger tip radius of the sMIM probe compared to

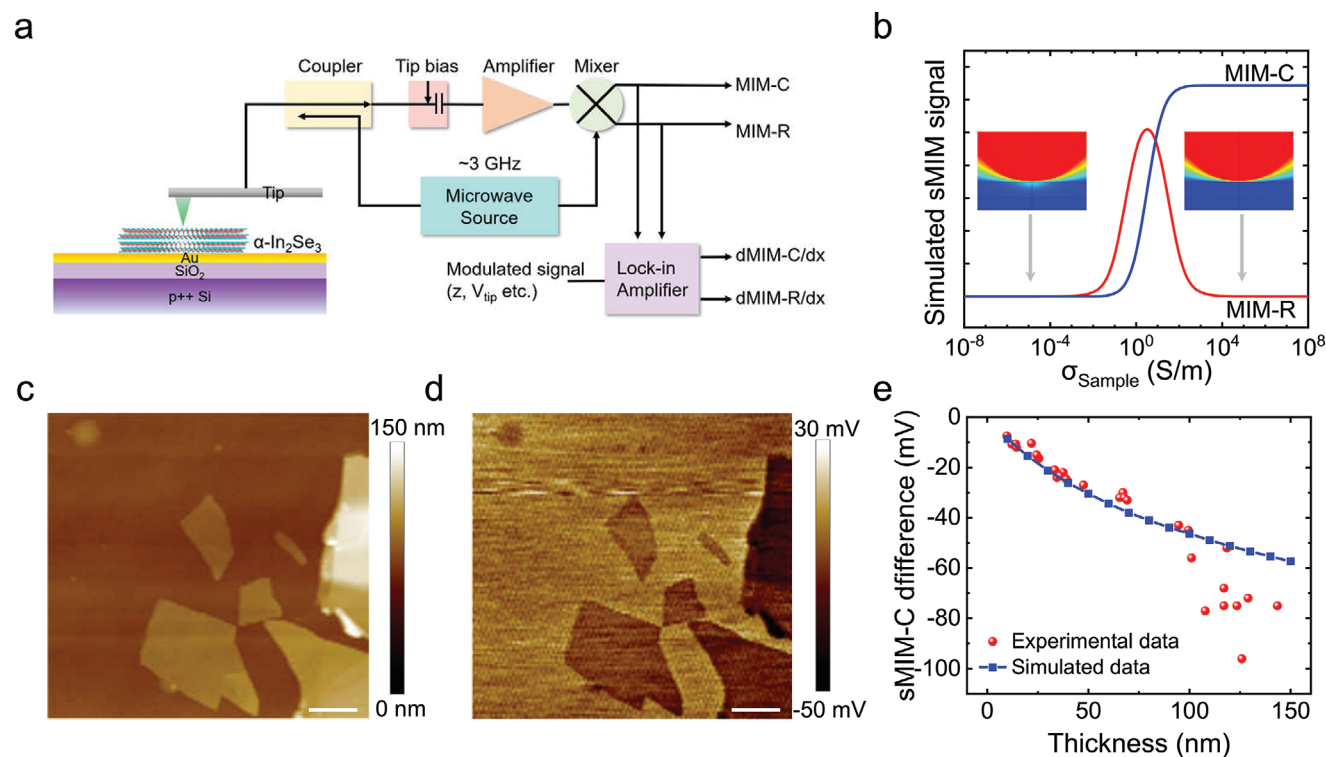


Figure 2. sMIM characterization of α -In₂Se₃ nanoflakes. a) Experimental schematic of the sMIM setup. b) Simulated sMIM response as a function of the conductivity of a 33 nm thick α -In₂Se₃ nanosheet placed on Au substrate. The insets show the calculated quasi-static potential distribution for two different α -In₂Se₃ conductivities. c,d) Representative topography and corresponding sMIM-C images recorded on α -In₂Se₃ flakes on Au film/Si substrate. Scale bars are 2 μ m. e) Extracted experimental and simulated sMIM-C signal (difference between Au and α -In₂Se₃) in dependence of the α -In₂Se₃ flake thickness. The sMIM-C signal is quasi-inversely proportional to the thickness of α -In₂Se₃ below 100 nm.

that in PFM may require a relatively large bias voltage for polarization switching. By superposing a 90 kHz AC modulating voltage on the sample, we acquire and display the characteristic of $d(\text{sMIM-C})/dV$ versus bias voltage in Figure 3f, which exhibits hysteresis with two peaks corresponding to the polarization switching bias voltage. We note that these voltage-dependent capacitive responses are distinct from those of typical insulating ferroelectric PZT (Figure S11f, Supporting Information) due to the interplay of the semiconductor nature of α -In₂Se₃ and the polarization switching. In principle, the semiconductor-based MOS configuration determines the basic shape of the C-V curve, while the ferroelectric switching property leads to bias voltage hysteresis. These findings contribute to a better understanding of the device physics based on vdW ferroelectric semiconductors.

2.4. sMIM Characterization of α -In₂Se₃ FeSFETs

Back-gated α -In₂Se₃ FETs were fabricated based on nanosheets exfoliated onto a Si substrate (p⁺⁺ doped) covered with an Al₂O₃ layer of 50 nm thickness grown by atomic layer deposition (ALD). C-V measurement reveals a permittivity of 7.2 for the Al₂O₃ layer, thus the gate dielectric corresponds to an equivalent oxide thickness (EOT) of 27.1 nm (Figure S12, Supporting Information), allowing a large reduction of the device operation voltage. A schematic of the device structure is given in Figure 4a. Ti/Au was used for source and drain electrodes for low-resistance con-

tact (See Experimental section for fabrication details). An additional encapsulating layer of 15-nm Al₂O₃ was deposited on the sample to serve as a protecting layer. Besides, it can largely improve the device performance in the aspects of on-state current and current on/off ratio, owing to the reduced surface scattering associated with the high- κ dielectric environment and electron doping effect of Al₂O₃ (Figure S13, Supporting Information).^[13]

We have fabricated devices using nanoflakes with thickness in the 20 to 50 nm range (Figure S14, Supporting Information). The transfer characteristics of a representative FeSFET are plotted in Figure 4b in the -8 to 8 V gate voltage (V_{gs}) range at different drain-source bias ($V_{\text{ds}} = 0.01, 0.05, 0.1$ V). The device shows typical n-type conduction behaviors, as can be judged from the increased channel current with gate potential. The channel current on/off ratio exceeds 10^5 , which is underestimated due to the system detection limit of the device off-state current (≈ 20 pA). The output curves (I_{ds} vs V_{ds}) of the device at varying gate voltages from -2 to 10 V (Figure 2c) exhibit good FET characteristics including the linear regime, cut-off regime, and saturation regime. Also, the linear $I_{\text{ds}}-V_{\text{ds}}$ relationship at $V_{\text{ds}} < 0.5$ V reflects the ohmic nature of the device contacts.

In Figure 4b, the device exhibits significant clockwise gate hysteresis, resulting in distinct channel conductance states and a memory window of 3.4 V. To ensure clarity, we define the device state at zero gate voltage in the forward and backward gate transfer characteristics as LRS and HRS, respectively. The conductance ratio between these two states is 3000, and it can

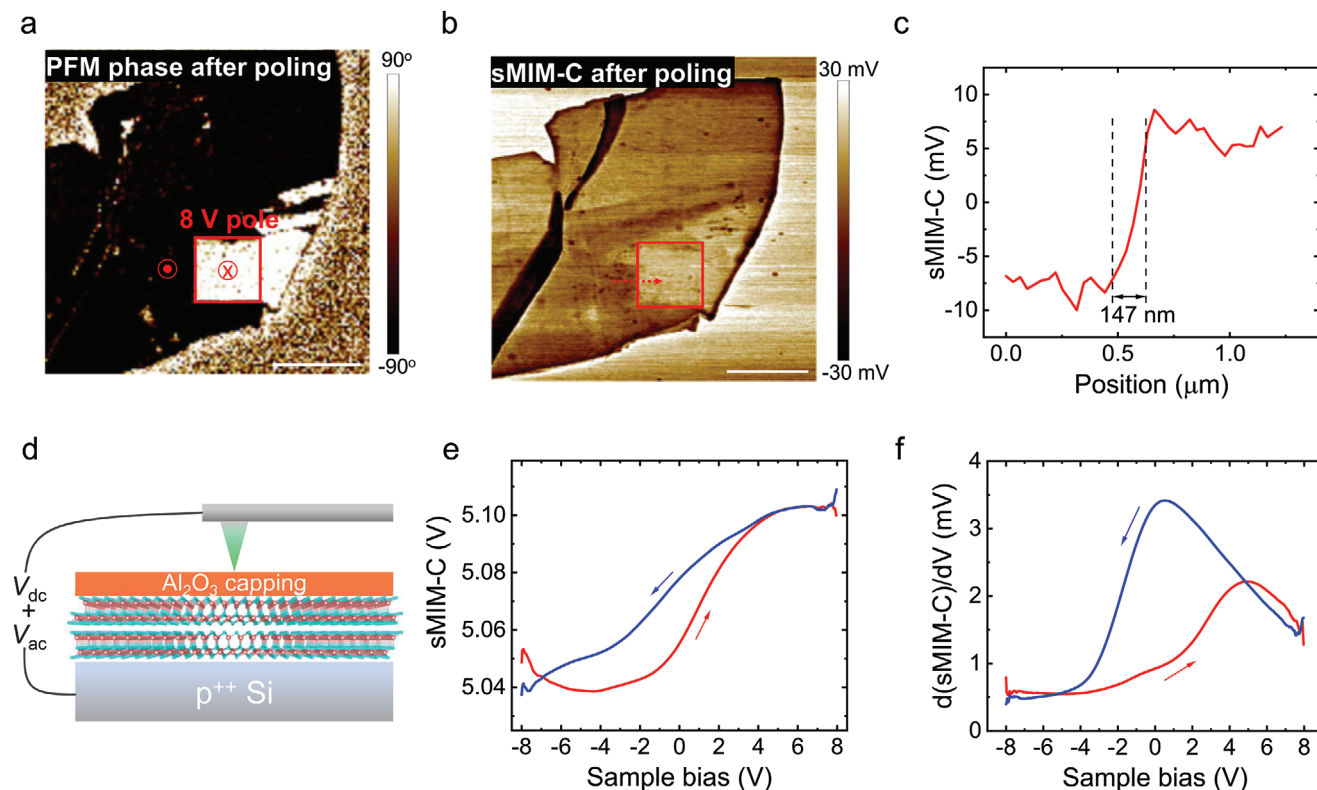


Figure 3. sMIM imaging of opposite α - In_2Se_3 domains and sMIM-C spectroscopy of α - In_2Se_3 based MOS structure. a) PFM phase image of an α - In_2Se_3 flake on $\text{Si}/\text{Al}_2\text{O}_3$ substrate after poling the area indicated by the red rectangle with a tip bias of 8 V. The PFM result prior to poling process is provided in Figure S8 (Supporting Information). b) sMIM-C image of the same area in (a) reveals a contrast between domains of opposite polarization direction, indicating polarization-modulated flake conductivity. c) sMIM-C profile along the red dashed line in (b). Scale bars in (a) and (b) are 2 μm . d) The schematic experimental structure for sMIM spectroscopy. e) sMIM-C response as a function of sample bias exhibiting n-type MOS-like characteristics with additional hysteresis. f) $d(\text{sMIM-C})/dV$ signal in dependence of sample bias acquired with a 2000 mV AC voltage at 90 kHz applied for capacitance modulation.

even exceed 10^5 (Figure S14, Supporting Information), indicating that α - In_2Se_3 FeSFETs possess a large dynamic range and are well-suited for applications such as memories or artificial synapses.^[17,18] The observed large clockwise hysteresis agrees well with the C-V characteristics of MOS structure in Figure 3, which is readily attributed to the gate-modulated ferroelectric polarization switching of the α - In_2Se_3 channel, as illustrated in the insets of Figure 4b.

Under high negative gate voltage, the ferroelectric polarization direction (from negative bond charge to positive bond charge) points from α - In_2Se_3 toward the back-gate Al_2O_3 dielectric. This induces positive charges at the bottom surface of In_2Se_3 , causing the energy band to bend downward. Consequently, this results in a higher carrier density and the LRS device state after the removal of the gate bias. On the other hand, a high positive gate voltage reverses the polarization, leading to a lower charge carrier concentration and the HRS device state upon withdrawal of the gate voltage.

The hysteresis and tunable resistance states empower α - In_2Se_3 FeSFETs to operate as artificial synapses.^[17,18] To test this application, the gate electrode is used as the synaptic input terminal and the source current serves as the post-synaptic current. Figure 4d–f shows the ability of the device to emulate essential synaptic behaviors including excitatory post-synaptic current

(EPSC), inhibitory post-synaptic current (IPSC), long-term potentiation (LTP), and long-term depression (LTD). Particularly, the EPSC and IPSC maintain their signal strength beyond 6000 s, which is indicative of the excellent non-volatility of the synapse state. In the following, we will be focused on the in situ characterization of α - In_2Se_3 FeSFET by sMIM.

The AFM image of a representative device for sMIM characterization made from a 34.2-nm α - In_2Se_3 nanosheet is shown in Figure 5a (see Figure S15, Supporting Information for a full AFM image of the device). The channel length and width are 4.2 and 4 μm , respectively. The electrical characteristics of the device are provided in Figure S14c (Supporting Information). It is worth pointing out that the presence of a capping dielectric layer will prevent access by SPM techniques such as SSRM and conductive AFM to locally characterize the device, both of which require current flowing. Figure 5b reports the sMIM-C images of the device under different gate modulation voltage of -8 , 0, and 8 V. The channel at 8 V V_{gs} gives rise to the highest sMIM-C signal, whereas the sMIM-C image of the channel is darkest for $V_{\text{gs}} = -8$ V, indicating conductive and insulating limits of the channel as a result of electrostatic modulation. The differences in sMIM-C response at different gate potentials are clearly noticed from the line profiles in Figure 5c. While the sMIM signal remains unchanged for Al_2O_3 dielectric due to its insulating property and

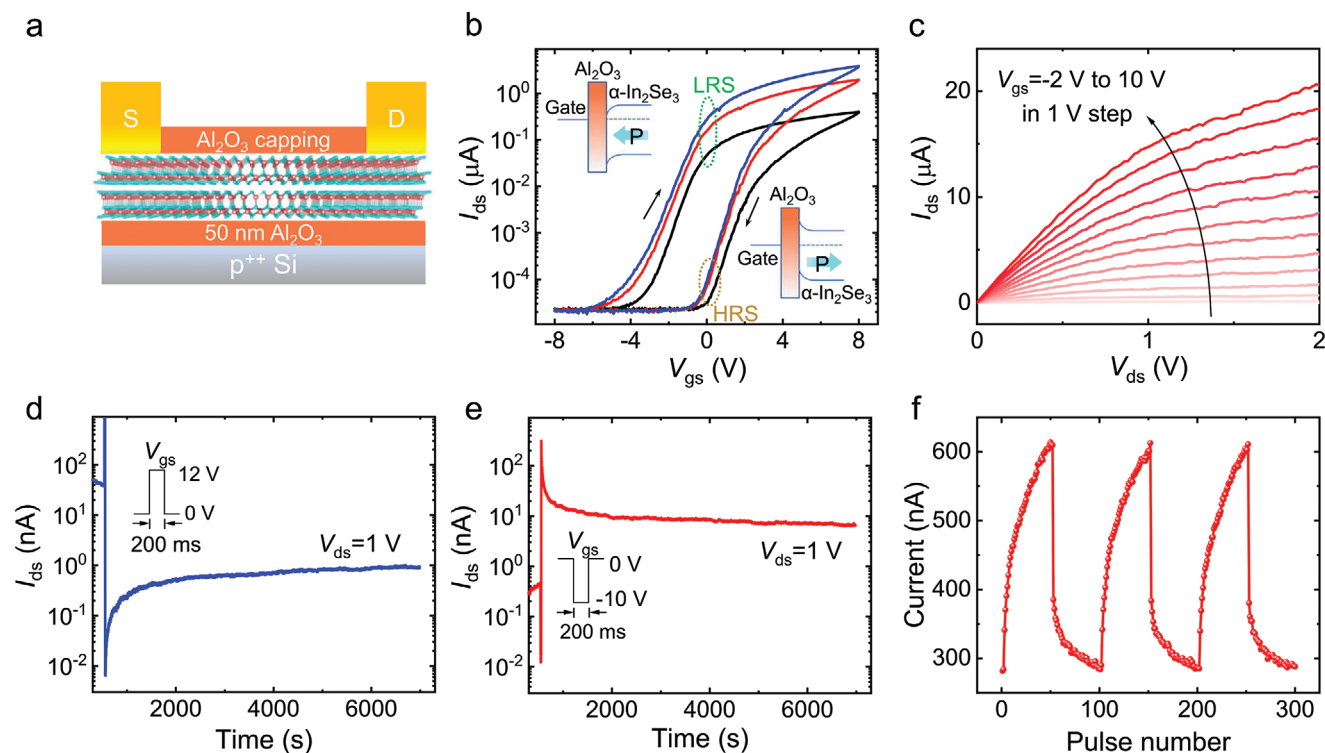


Figure 4. α - In_2Se_3 synaptic FeFETs. a) Schematic structure of the α - In_2Se_3 FeFETs. b) Transfer characteristics of a representative device ($L_{\text{ch}} = 1 \mu\text{m}$, $W_{\text{ch}} = 3 \mu\text{m}$, channel thickness = 47.2 nm) at V_{ds} of 0.01, 0.05, and 0.1 V, exhibiting large clockwise gate hysteresis. Insets show energy band diagrams to illustrate the mechanism of the device HRS and LRS corresponding to different ferroelectric polarization states modulated by V_{gs} . c) Output characteristics of the device for V_{gs} from -2 to 8 V with 1 V step. d–f) Operation of α - In_2Se_3 FeFET to emulate typical synaptic behaviors of IPSC d), EPSC (e), and LTP/LTD f). For LTP/LTD emulation, pulse trains of -5 and 3 V amplitude and $100 \mu\text{s}$ width were used. V_{ds} of 0.5 V were used for monitoring channel current.

absence of gating effect, it increases with gate voltage for the α - In_2Se_3 channel. Specifically, when $V_{\text{gs}} = -8$ V, the sMIM-C for Al_2O_3 dielectric is larger than α - In_2Se_3 . This can be attributed to carrier depletion in α - In_2Se_3 , resulting in a decreased capacitance sensed by the probe due to the insertion of α - In_2Se_3 between the tip and back-gate Al_2O_3 . For $V_{\text{gs}} = 8$ V, the α - In_2Se_3 is switched to the accumulation regime with increased carrier density, thus giving rise to an increased sMIM-C signal. These results correspond well to the off-state regime, subthreshold regime, and on-state regime of the device, respectively, thus demonstrating the ability of sMIM for in situ monitoring of the resistance states in α - In_2Se_3 FeFET. The sMIM-C signal is uniform across the entire channel of the exfoliated monocrystalline α - In_2Se_3 flake, indicating a negligible effect of coupling to nearby metals.

To monitor the evolution of the conductance state at a specific location, the sMIM-C image is collected by scanning the sample along a fixed line at various gate voltages, as shown in Figure 5d. It can be noticed that besides the contrast of sMIM-C responses at different gate voltages (see the line profiles in Figure S16, Supporting Information), the sMIM-C signal exhibits apparent variation at $V_{\text{gs}} = 0$ V following 10 and -10 V V_{gs} scans, indicating the device channel enters different states after being subjected to stimulation by different gate voltages. This is better illustrated by the two averaged line profiles in Figure 5e. Following the application of -10 V (10 V) V_{gs} , an increased (decreased) sMIM-C response is recorded at 0 V V_{gs} for the device channel. This is

in excellent agreement with our device tests, where the application of 10 and -10 V V_{gs} switches the polarization of α - In_2Se_3 , and drives the device to its HRS and LRS, respectively. Thus, the sMIM response provides a quantitative measure of the synaptic state of synaptic α - In_2Se_3 FeFET. Furthermore, since sMIM probes the local electrical properties of the sample, it averts the impact of contact resistance toward understanding device properties, which is frequently encountered in device data analysis.^[36]

In order to evaluate the conductivity modulation of the α - In_2Se_3 channel from the sMIM-C results, we carried out finite element modeling of the tip-sample interaction. The conductivity-dependent microwave admittance is simulated and demodulated, as shown in Figure 5f (see Experimental section for details). It is noted that the distinct sMIM-C signal difference of ~ 30 mV between ± 10 V gating voltage for the channel approaches the simulated sMIM-C signal range, indicating a strong modulation of the channel conductivity by the two gate potentials, which is in good agreement with the $> 10^6$ current on/off ratio from device transport measurement. In addition, by comparing the measured sMIM signal with the simulated sMIM curve, the α - In_2Se_3 channel conductivity at HRS and LRS (at 0 V V_{gs}) are derived to be 10^{-2} and 10 S m^{-1} (red and blue bars in Figure 5f), respectively, which match well with the calculated values from FET device analysis (Table S2, Supporting Information), thus validating the capability of sMIM to monitor the local conductance evolution of α - In_2Se_3 channel.

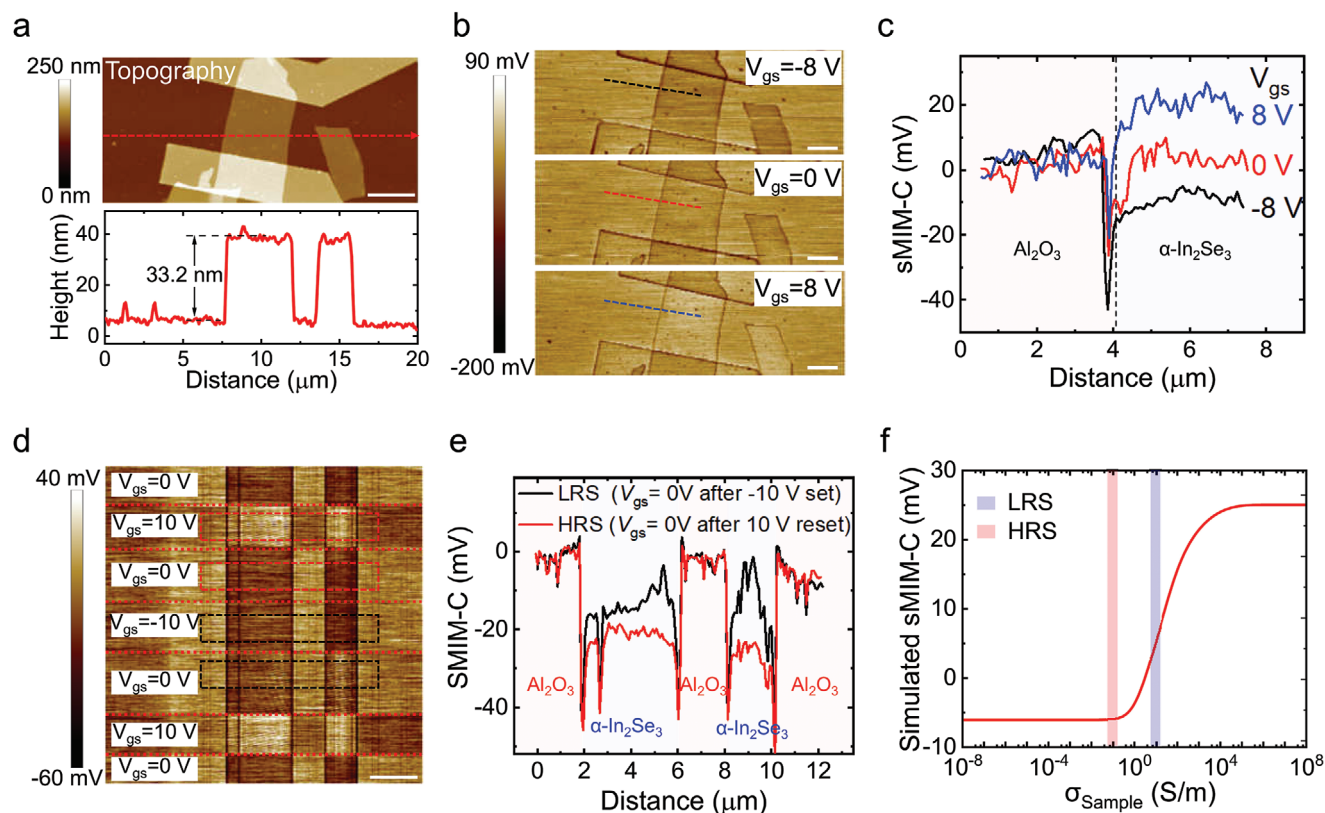


Figure 5. In situ sMIM characterization of α -In₂Se₃ synaptic FeSFET. a) AFM topography image of the tested device and height profile of the α -In₂Se₃ flakes. The scale bar is 3 μ m. b) Contrast sMIM-C images recorded at three different gate voltages of -8 , 0 , and 8 V, showing increased sMIM-C signal for the channel with increased V_{gs} . The scale bar is 2 μ m. c) sMIM-C profiles along the three indicated lines in b. d) A sMIM-C image acquired by fixing the tip scan along the red arrowed line in a and varying the gate voltage at different image zones as indicated. Scale bar is 3 μ m. e) Averaged sMIM-C profiles at 0 V V_{gs} of the indicated areas in d, evidencing distinct sMIM responses after the device is gated by -10 and 10 V V_{gs} . f) FEA simulations of the tip-sample interaction with the two HRS and LRS states indicated by red and blue bars.

3. Conclusion

In summary, we have employed sMIM to investigate the electrical properties of the ferroelectric semiconductor α -In₂Se₃ and its synaptic FeSFETs. In the absence of gate modulation, the sMIM capacitive response of α -In₂Se₃ exhibits an inverse correlation with the flake thickness. Through sMIM spectroscopy, we have characterized the local C-V characteristics of a FET with α -In₂Se₃ as the ferroelectric semiconductor channel showed that the current-voltage behavior is a consequence of its combined semiconductor and ferroelectric behavior. At the device level, sMIM enables quantitative visualization of the channel conductance state in the synaptic α -In₂Se₃ FeSFETs in a non-perturbative manner. The ability of sMIM to offer in situ spatial and temporal mapping of mesoscopic electrical properties will be invaluable for understanding the dynamics of ferroic domains and enhancing the performance of ferroelectric devices.

4. Experimental Section

Materials Characterization: The Raman spectra of exfoliated α -In₂Se₃ flakes were acquired using a WITec Alpha 300R Raman Spectrometer with 532 nm wavelength laser excitation and a grating of 1800 lines mm⁻¹. PL

spectrum was collected with the flake under 633 nm laser illumination. Thicknesses of the flakes were determined by AFM scans with a Bruker Dimension Icon AFM in ScanAsyst mode or sMIM mode. PFM was carried out on α -In₂Se₃ flakes exfoliated onto a Si substrate covered with e-beam deposited Au film with an excitation AC voltage of 1000 mV amplitude near the contact resonance frequency of 280 kHz. The butterfly PFM hysteresis curve and the poled AFM image were collected using an Asylum Research MFP-3D AFM system and a Bruker Dimension Icon AFM, respectively. Probes with a Pt/Ir coating layer and a spring constant of 3 N m⁻¹ were used for PFM.

Device Fabrication: α -In₂Se₃ flakes were mechanically exfoliated from a bulk single crystal onto degenerate p-doped Si substrates covered with 50 nm thick ALD-grown Al₂O₃ for back-gated device fabrication. Electrode pattern was defined by electron beam lithography using a poly(methyl methacrylate) (PMMA) photoresist and 15 nm/65 nm Ti/Au was deposited by e-beam evaporation followed by a standard lift-off process in acetone. A 15 nm Al₂O₃ capping layer was synthesized on the devices for improved stability. All the Al₂O₃ layers were grown in a Savannah ALD system at 150 °C by using trimethylaluminum (TMA) (each pulse of 15 ms) and water (each pulse of 15 ms) as precursors.

Electrical Characterization: The electrical characterization of the devices was performed on a Lake Shore model CPX cryogenic probe station connected to a Keithley 4200SCS semiconductor parameter analyzer with the devices in vacuum ($< 3 \times 10^{-4}$ Torr).

sMIM Characterization: The sMIM experiments were carried out on a Bruker Dimension Icon AFM platform equipped with sMIM module from PrimeNano Inc. The probes for sMIM with electrically shielded cantilevers

are commercially available (sMIM-300 from PrimeNano Inc.). Peakforce tapping sMIM was used for sMIM measurements. The two output channels of sMIM correspond to the real and imaginary parts of the local sample admittance. Prior to experiments on α -In₂Se₃ flakes and devices, the sMIM module is calibrated on a standard sample covered with SiO₂ and Al dots (with \approx 4 nm native oxide). By tuning the phase shifter in front of the I-Q (in-phase and quadrature) mixer, the sMIM channels with and without contrast between SiO₂ and Al dots are adjusted to be sMIM-C (capacitive or imaginary parts of the local sample admittance) and sMIM-R (lossy or real parts of the local sample admittance) signal, respectively. For in situ sMIM measurements, the bias voltages applied to the drain and gate electrodes are separately supplied by a Keithley 2400 SMU, with the source electrode being grounded. FEA simulations are conducted using the commercial software COMSOL 4.4. Details of the FEA are provided in Supporting Information. The conversion between demodulated tip-sample admittance and the sMIM-C output is based on a calibration procedure similar to the work in Ref.[28,36]

Statistical Analysis: The sMIM-C response of α -In₂Se₃ flakes with different thicknesses were obtained from 8 regions on a sample. The number of samples for sMIM-C spectroscopy and d(sMIM-C)/dV amplitude response test was 4 in Figure 3 and Figure S9 (Supporting Information). 6 FeSFETs were tested and the results were shown in Figure 4 and Figure S14 (Supporting Information).

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

Keywords

ferroelectric semiconductor, finite element analysis, neuromorphic device, scanning microwave impedance microscopy, α -In₂Se₃

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