The following publication Z. Y. Yang, C. W. Leung, P. T. Lai and P. W. T. Pong, "A numerical investigation on effects of lateral Si/SiO2 interface traps on magnetic sensitivity of sectorial SD-MAGFET," 2016 5th International Symposium on Next-Generation Electronics (ISNE), Hsinchu, Taiwan, 2016, pp. 1-2 is available at https://doi.org/10.1109/ISNE.2016.7543340.

# A numerical investigation on effects of lateral Si/SiO2 interface traps on magnetic sensitivity of sectorial SD-MAGFET

Zhen Yi Yang<sup>1</sup>, Chi Wah Leung<sup>2</sup>, P. T. Lai<sup>1</sup>, P. W. T. Pong<sup>1\*</sup>

- 1 Dept. of Electrical and Electronics Engineering, The University of Hong Kong, Hong Kong
- 2 Dept. of Applied Physics, The Hong Kong Polytechnic University, Hung Hom, Hong Kong

# **Abstract:**

Split-drain magnetic field-effect transistor (SD-MAGFET) has been widely used as magnetic field sensors, current sensors and temperature-stable arrays due to its small size and CMOS compatibility [1]–[2][3]. Previous work suggested that its sensitivity hysteresis could be caused by the Si/SiO2 interface traps at the sidewalls of the conduction channel [4]. However, the effects of these interface traps on the magnetic sensitivity have not been studied in detail. In this work, the effects of trap type, trap density and trap energy are investigated by Silvaco TCAD.

**Keywords:** Electron traps, Sensitivity, Saturation magnetization, Magnetic hysteresis, Magnetic sensors, Magnetic devices

# Introduction

Split-drain magnetic field-effect transistor (SD-MAGFET) has been widely used as magnetic field sensors, current sensors and temperature-stable arrays due to its small size and CMOS compatibility [1]–[2][3]. Previous work suggested that its sensitivity hysteresis could be caused by the Si/SiO2 interface traps at the sidewalls of the conduction channel [4]. However, the effects of these interface traps on the magnetic sensitivity have not been studied in detail. In this work, the effects of trap type, trap density and trap energy are investigated by Silvaco TCAD.

#### **Numerical Model**

The interface traps at the lateral side of the sectorial SD-MAGFET has a significant impact on the carrier motion in the channel (Figure 1). Acceptor traps (AT) and donor traps (DT) can respectively capture and emit electrons, and this trapping effect will interact with the Hall effect when the device is exposed to a magnetic field. The acceptor traps are neutral when empty and negatively charged when filled with electrons from the conduction or valence band. On the other hand, the donor traps are positive when empty and neutral when filled with electrons from the conduction or valence band (Figure 2). The probability of ionization for the traps depends on the capture cross sections of electron and hole. They are assumed to be constants for all trap energy levels in the forbidden band and are based on the analysis exploit by Simmons and Taylor [5].

#### **Results and Discussion**

Figure 3 exhibits that acceptor-like interface trap and negative oxide charge have same effect on the magnetic sensitivity of the device (for two densities of 1×1011cm-2 and 1×1012cm-2). This is also true between donor-like interface trap and positive oxide charge. Therefore, the filled acceptor traps (after capturing electrons from the channel) act like negative oxide charges to decrease the total drain current by depleting the n-channel near the sidewalls, and increase the drain-current difference by increasing the bending of electron path in the n-channel, both resulting

in higher magnetic sensitivity. On the other hand, the emptied donor traps (after releasing electrons to the n-channel) behave like positive oxide charges to increase the total drain current by enhancing the n-channel near the sidewalls, and decrease the drain-current difference by reducing the bending of the electron path in the n-channel, both leading to lower magnetic sensitivity.

In Figure 4, the magnetic sensitivity with acceptor traps increases dramatically and then saturates because the p-Si near the sidewalls changes from depletion to accumulation. For donor trap, the abrupt drop in magnetic sensitivity is due to large current increase resulted from heavy inversion of the p-Si near the sidewalls.

Figure 5 presents the magnetic sensitivity as a function of the trap energy. Different donor trap levels (EtD) and acceptor trap levels (EtA) (0.5 eV to 1.0 eV) are applied to the energy gap (see Figure 2). EtD and EtA are relative to the valance-band top and conduction-band bottom respectively. For donor traps near to the middle of the energy gap Ei, the probability that the donor trap can emit an electron is smaller than that far above Ei. Therefore, as EtD increases, more traps can emit electrons leading to lower magnetic sensitivity (like the oxide charge in Figure 3). On the other hand, for larger EtA, the AT trap level lies near to the valance band and so are more likely to capture electrons to form negative charges, resulting in higher magnetic sensitivity (like the oxide charge in Figure 3).

# Conclusion

The interface traps at the lateral side of sectorial SD-MAGFET have been investigated in detail, including their most important parameters: trap type, trap density and trap energy. The ionized acceptor traps at the lateral interface are negatively charged and so increase the magnetic sensitivity by increasing IH and decreasing IDS. However, the ionized donor traps are positively charged and thus decrease the magnetic sensitivity by decreasing IH and increasing IDS. Higher sensitivity is observed when the acceptor traps lie nearer to the valance band, or the donor traps are farther from the conduction band. This work can provide better understanding on the lateral

interface properties of the sectorial SD-MAGFET and provide a design guide for improving its performance.

# **Figures**

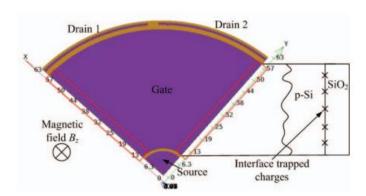


Figure 1. Top view of sectorial SD-MAGFET with 90° source angle, aluminum electrodes for source, gate, drain 1 and drain 2 (x and y are in um). Under a magnetic field Bz normal to the channel, the two drains of the transistor have different currents (IDS1,IDS2) due to the Hall effect, and so the drain-current difference (IH) can be used as a sensing signal. Traps exist at both lateral si/sio2 interfaces of the device.

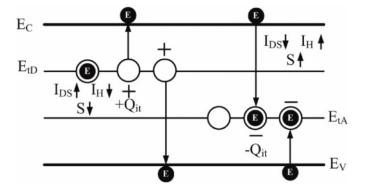


Figure 2. Energy band diagram with acceptor traps and donor traps. The electron exchange between the traps and the conduction band (EC) or valence band (EV) are shown in the diagram. The energy levels of acceptor (EtA) and donor traps (EtD) lie in the energy gap. The acceptor traps capture electrons to become negatively charged while the donor traps emit electrons to become positively charged.

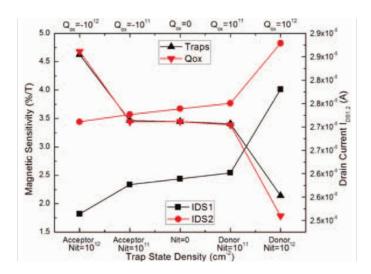


Figure 3. Effects of interface trap (triangles) and oxide charge (inverted triangles) on magnetic sensitivity (=IH/((IDS1+IDS2)Bz)) for two values of density. The two drain currents IDS1 (squares) and IDS2 (circles) as a function of trap density are also shown.

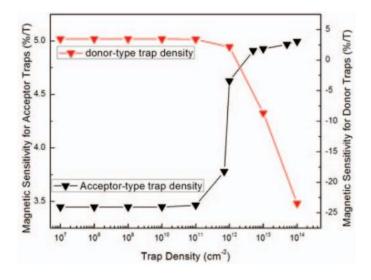


Figure 4. Magnetic sensitivity versus density of acceptor and donor traps.

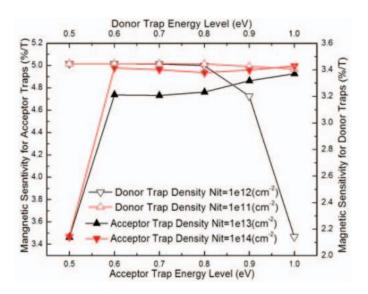


Figure 5. Magnetic sensitivity as a function of trap energy.

# References

- [1] B. Zhang, C. E. Korman, and M. E. Zaghloul, "Circular MAGFET Design and SNR Optimization for Magnetic Bead Detection," IEEE Trans. Magn., vol. 48, pp. 3851-3854, 2012.
- [2] O. Aiello and F. Fiori, "A New MagFET-based Integrated Current Sensor Highly Immune to EMI," Microelectronics Reliability, vol. 53, pp. 573-581, 2013.
- [3] K. Nakano, T. Takahashi, and S. Kawahito, "A highly temperature-stable MAGFET array for CMOS smart rotary encoders," 2005 IEEE Sensors, 2005, pp. 1053-1056.
- [4] Z. Yang, S. L. Siu, W. S. Tam, et. al., "Transient Sensitivity of Sectorial Split-Drain Magnetic Field-Effect Transistor," IEEE Trans. Magn., vol. 49, pp. 4048-4051, 2013.
- [5] J. G. Simmons and G. W. Taylor, "Nonequilibrium Steady-State Statistics and Associated Effects for Insulators and Semiconductors Containing an Arbitrary Distribution of Traps," Phys. Rev. B, vol. 4, pp. 502-511, 1971.